



S8MBG036



60V N+P Dual Channel MOSFETs

General Description

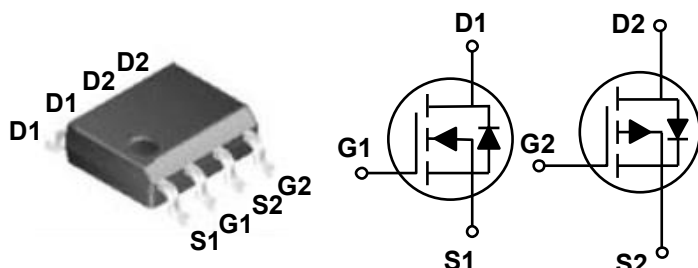
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV_{DSS}	$R_{DS(ON)}$	I_D
60 V	36 m Ω	12.5 A
-60 V	70 m Ω	-9.7 A

Features

- Fast Switching
- Green Device Available

SOP-8 Pin Configuration



Applications

- Boost Driver
- Brushless Motor

Absolute Maximum Ratings $T_c=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Rating		Units
V_{DS}	Drain-Source Voltage	60	-60	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Drain Current - Continuous ($T_A=25^{\circ}\text{C}$)	12.5	-9.7	A
I_{DM}	Drain Current - Pulsed (NOTE 1)	37.5	-22.5	A
P_D	Power Dissipation ($T_A=25^{\circ}\text{C}$)	1.47		W
T_J	Operating Junction Temperature Range	-55 to 150		$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150		$^{\circ}\text{C}$
Marking Code		BG036 , AP10G06S		

Thermal Characteristics

Symbol	Parameter	Rating	Unit
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	85	$^{\circ}\text{C/W}$

**S8MBG036****60V N+P Dual Channel MOSFETs****N Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =48V, V _{GS} =0V	---	---	1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =4A	---	---	36	mΩ
		V _{GS} =4.5V, I _D =2A	---	---	38	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	---	2.5	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =4A	---	21	---	S

Dynamic and Switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q _g	Total Gate Charge	V _{DS} =48V, V _{GS} =4.5V, I _D =4A	---	12.6	---	nC
Q _{gs}	Gate-Source Charge		---	3.2	---	
Q _{gd}	Gate-Drain Charge		---	6.3	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =30V, V _{GS} =10V, R _G =3.3Ω, I _D =4A	---	8	---	nS
T _r	Rise Time		---	14.2	---	
T _{d(off)}	Turn-Off Delay Time		---	24.4	---	
T _f	Fall Time	V _{DS} =15V, V _{GS} =0V, F=1MHz	---	4.6	---	pF
C _{iss}	Input Capacitance		---	1378	---	
C _{oss}	Output Capacitance		---	86	---	
C _{rss}	Reverse Transfer Capacitance	V _{DS} =0V, V _{GS} =0V, F=1MHz	---	64	---	Ω
R _g	Gate Resistance		---	3.2	---	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	4.8	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A	---	---	1.2	V

NOTES :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



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60V N+P Dual Channel MOSFETs

Characteristics Curves

FIG. 1-Forward Characteristics of Body Diode

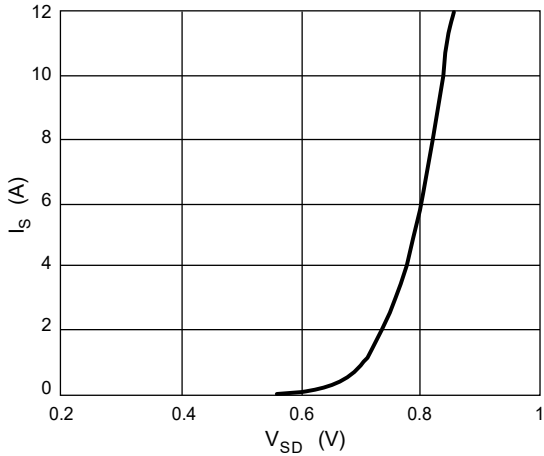


FIG. 2-Normalized $V_{GS(th)}$ vs T_J

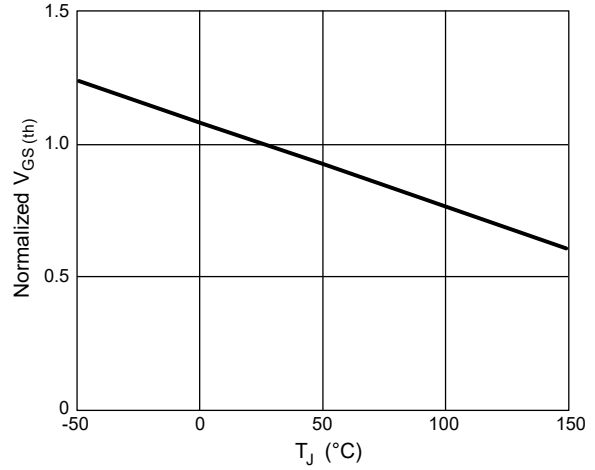


FIG. 2-Normalized $R_{DS(on)}$ vs T_J

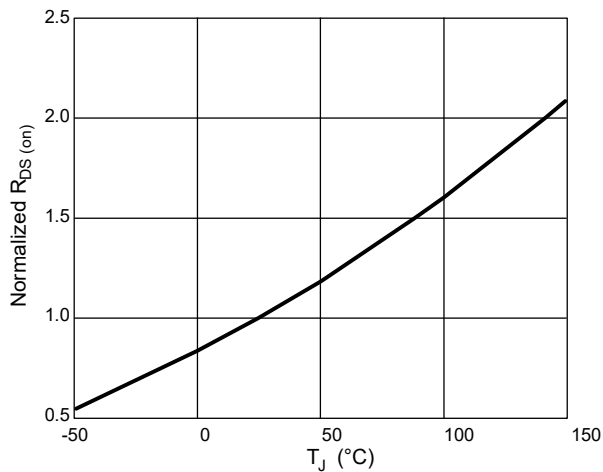


FIG. 4-Gate Charge Characteristics

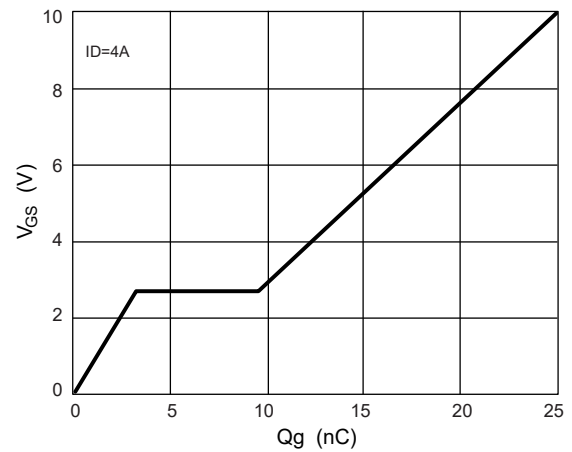


FIG. 5-Safe Operation Area

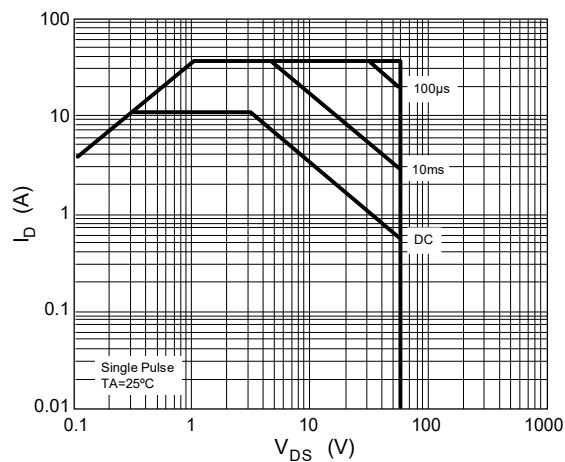
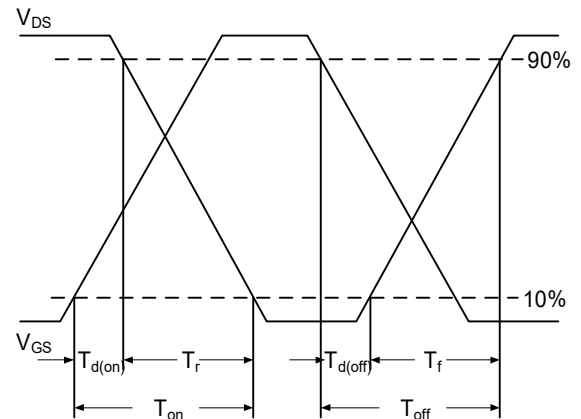


FIG. 6-Switching Time Waveform



**S8MBG036****60V N+P Dual Channel MOSFETs****P Channel Electrical Characteristics (T_J=25°C, unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V , I _D = -250uA	-60	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} = -48V , V _{GS} = 0V	---	---	-1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V , V _{DS} = 0V	---	---	±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = -10V , I _D = -3A	---	---	70	mΩ
		V _{GS} = -4.5V , I _D = -2A	---	---	85	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D = -250uA	-1.2	---	-2.5	V
g _{fs}	Forward Transconductance	V _{DS} = -5V , I _D = -3A	---	15	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q _g	Total Gate Charge	V _{DS} = -48V , V _{GS} = -4.5V , I _D = -3A	---	9.86	---	nC
Q _{gs}	Gate-Source Charge		---	3.1	---	
Q _{gd}	Gate-Drain Charge		---	2.95	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} = -15V , V _{GS} = -10V , R _G = 3.3Ω , I _D = -1A	---	28.8	---	nS
T _r	Rise Time		---	19.8	---	
T _{d(off)}	Turn-Off Delay Time		---	60.8	---	
T _f	Fall Time	V _{DS} = -15V , V _{GS} = 0V , F= 1MHz	---	7.2	---	pF
C _{iss}	Input Capacitance		---	1447	---	
C _{oss}	Output Capacitance		---	97.3	---	
C _{rss}	Reverse Transfer Capacitance	V _{DS} =0V , V _{GS} =0V , F=1MHz	---	70	---	Ω
R _g	Gate Resistance		---	13.5	---	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V , Force Current	---	---	-3.7	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S = -1A	---	---	-1.2	V

NOTES :

- The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
- Essentially independent of operating temperature.



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Characteristics Curves

FIG. 7-Forward Characteristics of Body Diode

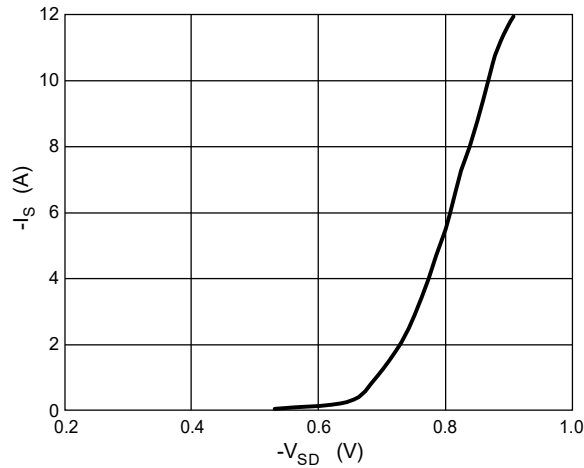


FIG. 8-Normalized $V_{GS(th)}$ vs T_J

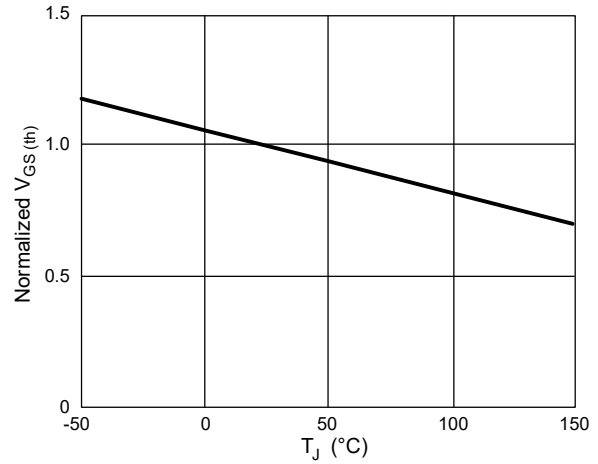


FIG. 9-Normalized $R_{DS(on)}$ vs T_J

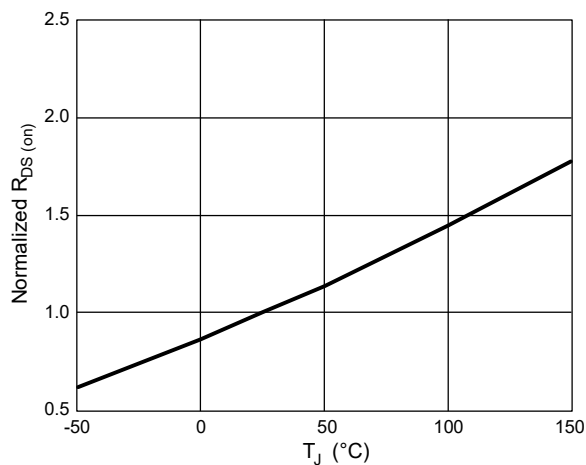


FIG. 10-Gate Charge Characteristics

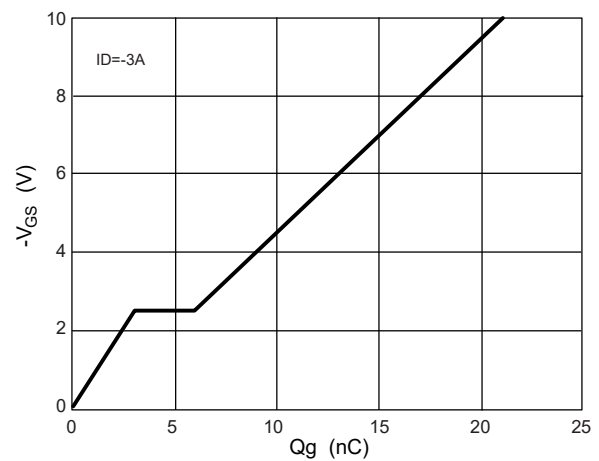


FIG. 11-Safe Operation Area

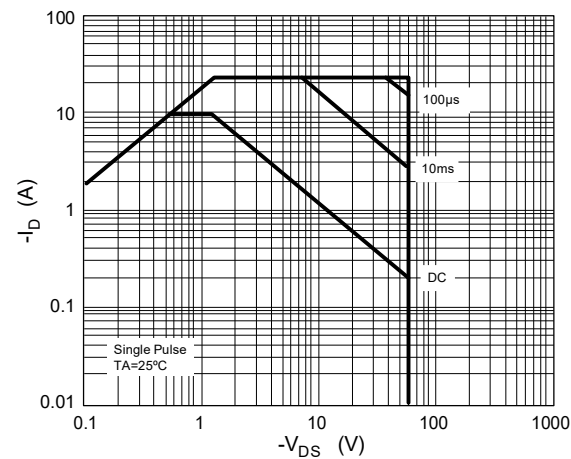
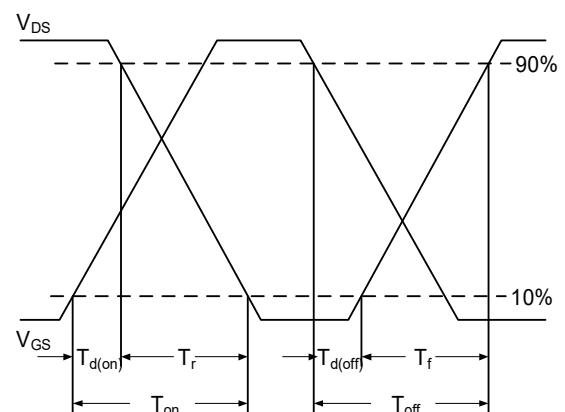


FIG. 12-Switching Time Waveform



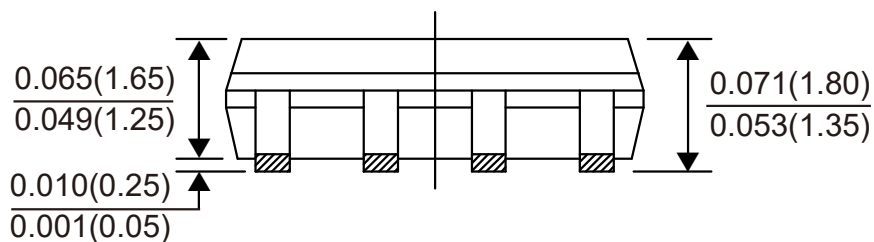
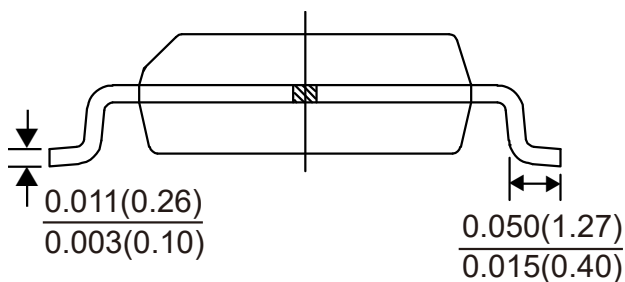
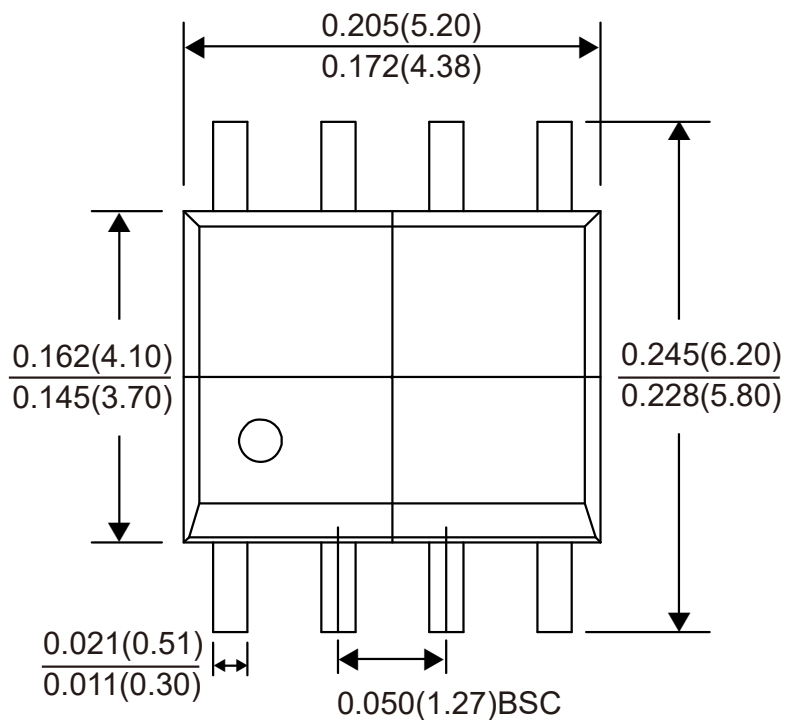


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Package Outline Dimensions



SOP-8

Dimensions in inches and (millimeters)



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