

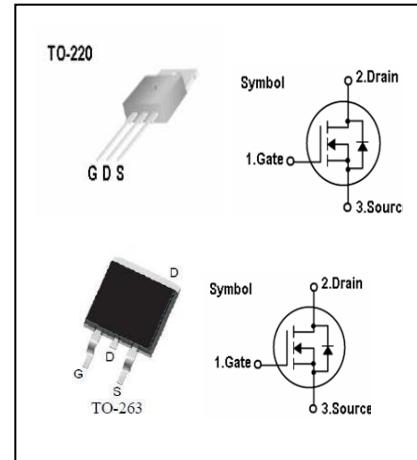
N-Channel MOSFET

Features

- 85V,140A,R_{ds(on)}(typ)=5.8mΩ @ V_{gs}=10V
- High Ruggedness
- Fast Switching
- 100% Avalanche Tested
- Improved dv/dt Capability

General Description

This Power MOSFET is produced using Si-Tech's advanced Trench MOS Technology. This latest technology has been especially designed to minimize on-state resistance, have a high rugged avalanche characteristics. These devices are well suited for low voltage application such as automotive, DC/DC converters, and high efficiency switch for power management in portable and battery products.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V _{DSS}	Drain-Source Voltage	85	V
I _D	Continuous Drain Current (T _c =25°C)	140	A
	Continuous Drain Current (T _c =100°C)	98	A
I _{DM}	Pulsed Drain Current (Note 1)	560	A
V _{GS}	Gate-Source Voltage	± 20	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	992	mJ
P _D	Maximum Power Dissipation (T _c =25°C)	220	W
	Derating Factor above 25°C	1.46	W/°C
T _J	Operating Junction Temperature Range	-55 to +175	°C
T _{STG}	Storage Temperature Range	-55 to +175	°C

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case	0.68	°C/W
R _{th c-s}	Thermal Resistance, Case to Sink	0.5	°C/W
R _{th j-a}	Thermal Resistance, Junction to Ambient	62.5	°C/W

Electrical Characteristics (T_c=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	85	-	-	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
I _{GSS}	Gate Leakage Current, Forward	V _{GS} =20V, V _{DS} =0V	-	-	100	nA
	Gate Leakage Current, Reverse	V _{GS} =-20V, V _{DS} =0V	-	-	-100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	2	-	4	V
R _{D(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =40A	-	5.8	7	mΩ
Q _g	Total Gate Charge	V _{DD} =60V V _{GS} =10V I _D =80A (Note 3)	-	115	-	nC
Q _{gs}	Gate-Source Charge		-	20	-	nC
Q _{gd}	Gate-Drain Charge		-	32	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =37.5V, V _{GS} =10V I _D =45A, R _G =4.7Ω T _c =25°C (Note 3)	-	21	-	ns
t _r	Turn-on Rise Time		-	33	-	ns
t _{d(off)}	Turn-off Delay Time		-	67	-	ns
t _f	Turn-off Fall Time		-	44	-	ns
C _{iss}	Input Capacitance -	V _{DS} =25V V _{GS} =0V f = 1MHz	-	5640	-	pF
C _{oss}	Output Capacitance		-	560	-	pF
C _{rss}	Reverse Transfer Capacitance		-	342	-	pF

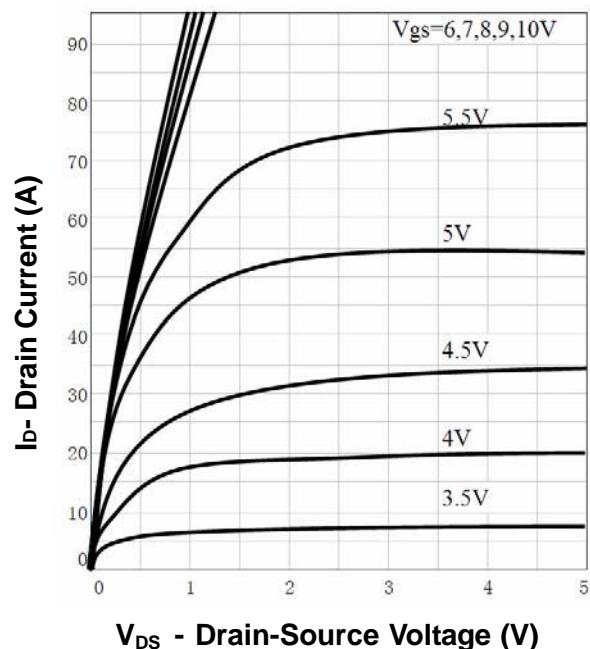
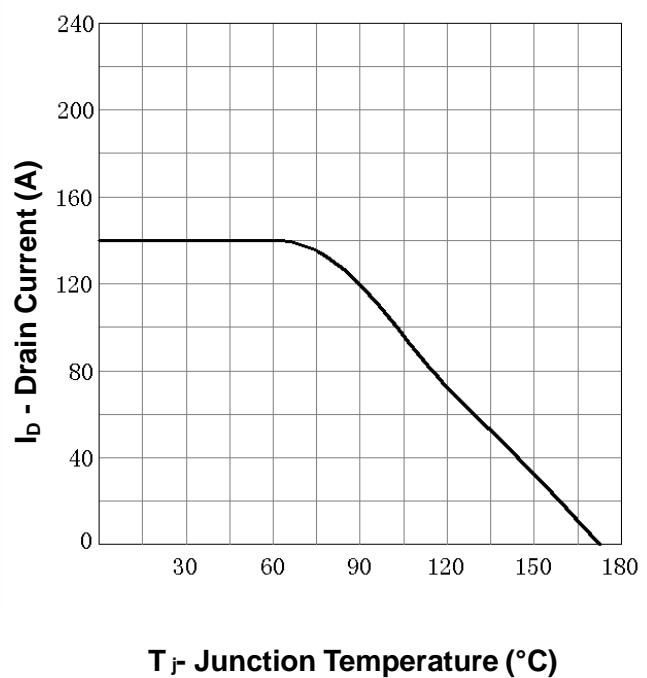
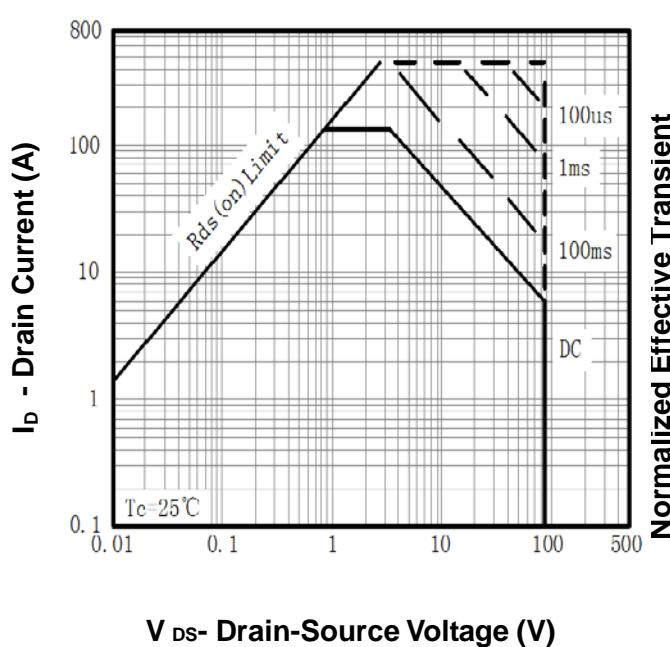
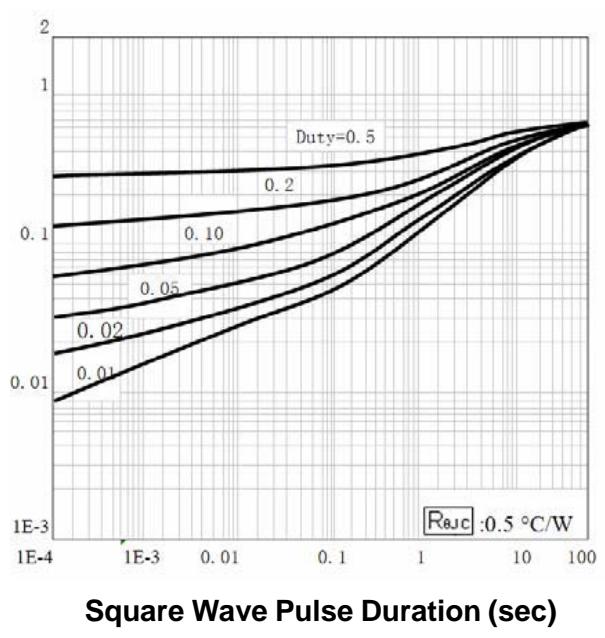
Source-Drain Diode Characteristics (T_c=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I _s	Continuous Source Diode Forward Current	-	-	140	A	
I _{SM}	Pulsed Source Diode Forward Current (Note 1)	-	-	560	A	
V _{SD}	Forward On Voltage	V _{GS} =0V, I _s =45A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _s =45A dI/dt = 100A/us	-		27	ns
	Reverse Recovery Charge		-		50	nC

Notes:

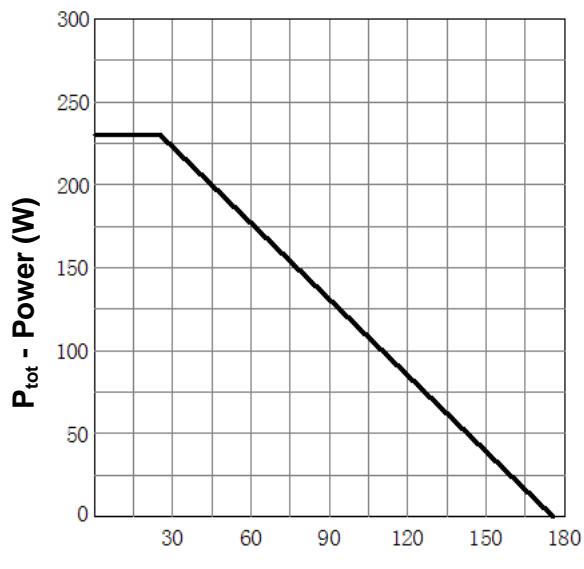
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=0.5mH, V_{DD}=50V, R_G=25 Ω, Starting T_J=25°C
3. Pulse Width ≤ 300 us; Duty Cycle≤2%

Typical Characteristics

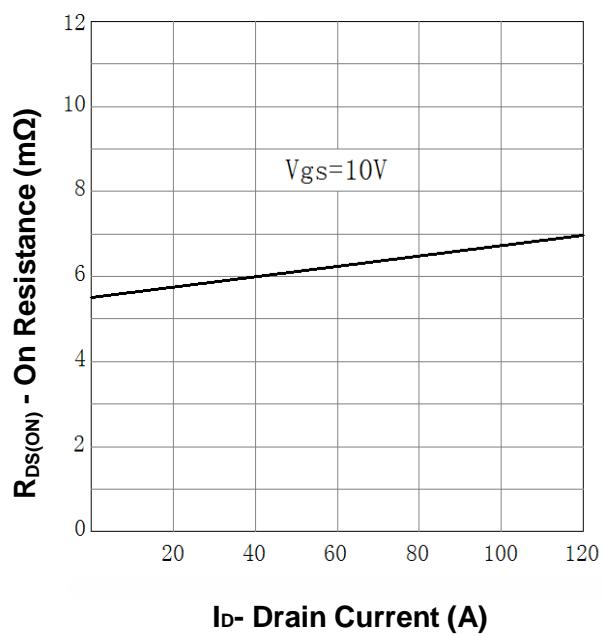
Output Characteristics**Drain Current****Safe Operation Area** **T_j - Junction Temperature (°C)****Thermal Transient Impedance**

Typical Characteristics

Power Dissipation



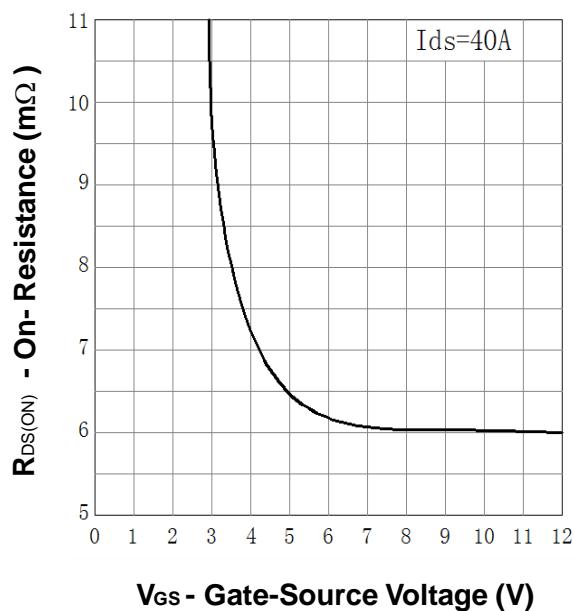
Drain-Source On Resistance



T_j - Junction Temperature (°C)

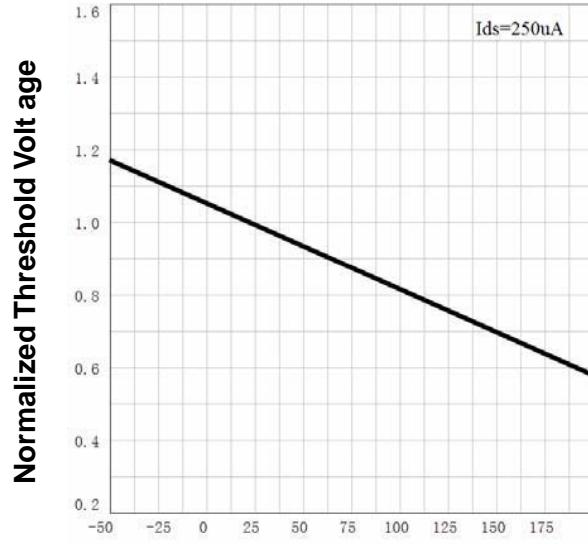
I_D - Drain Current (A)

Drain-Source On Resistance



V_{GS} - Gate-Source Voltage (V)

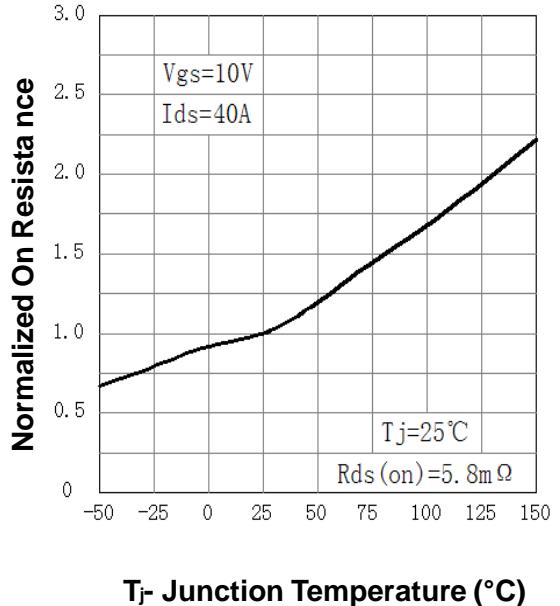
Gate Threshold Voltage



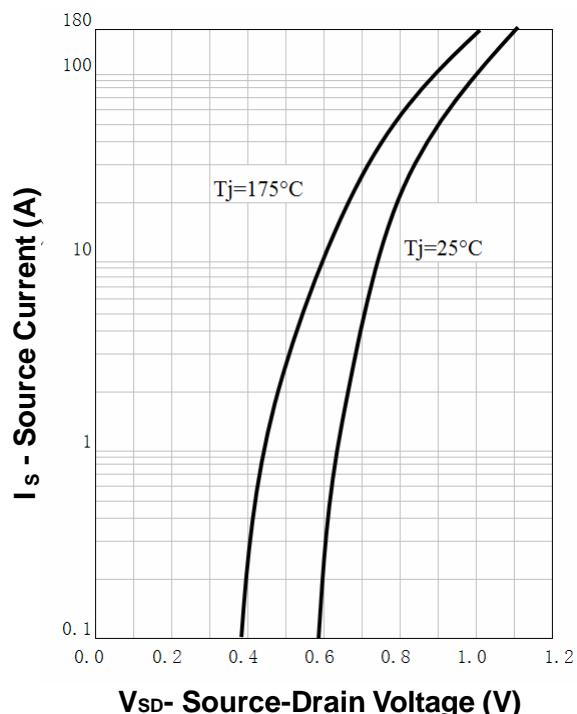
T_j - Junction Temperature (°C)

Typical Characteristics

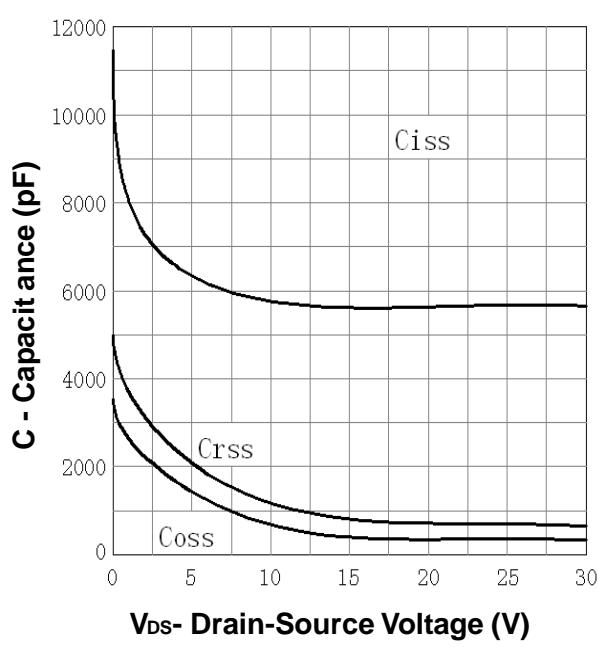
Drain-Source On Resistance



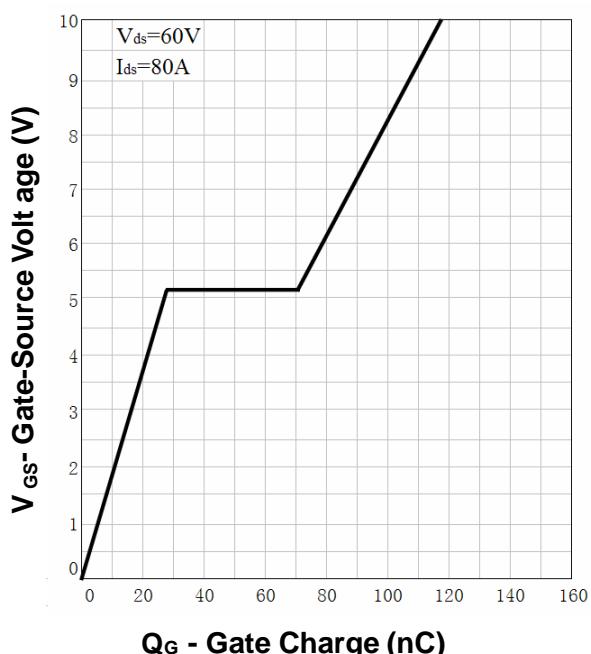
Source-Drain Diode Forward



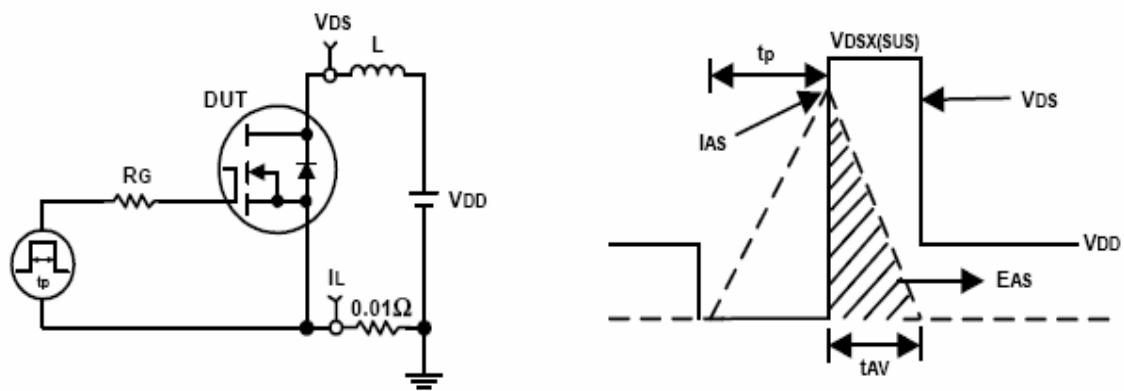
Capacitance



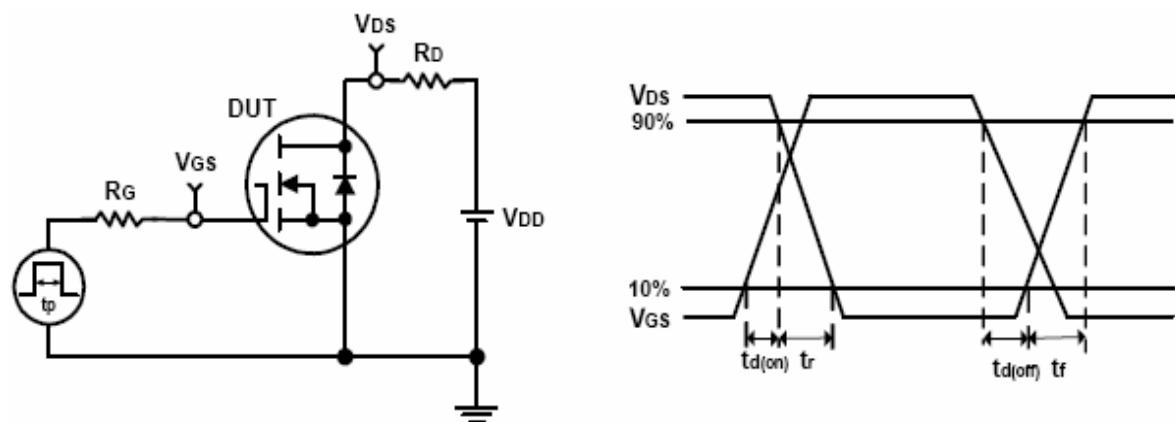
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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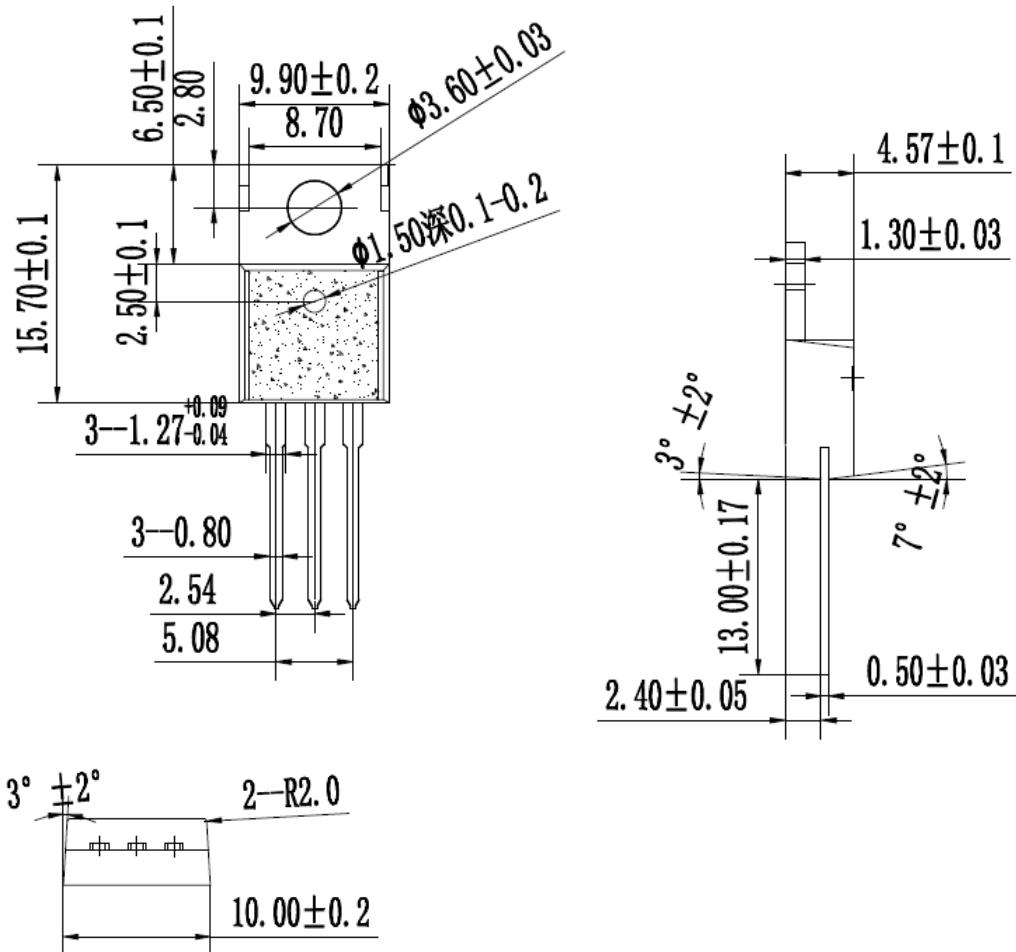
SI-TECH SEMICONDUCTOR CO.,LTD

S85N14R/S/RP

Package Outline

Dimensions are shown in millimeters

R: TO220

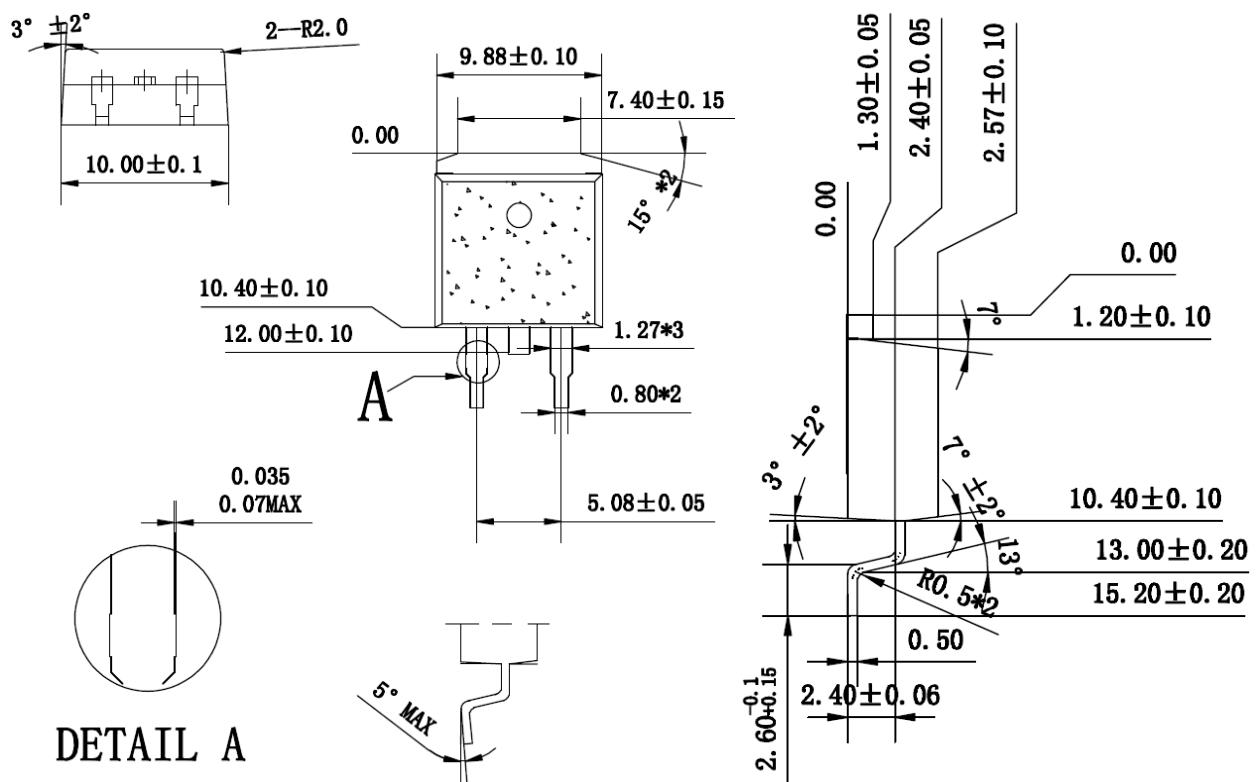


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S: TO263 (D²PAK)



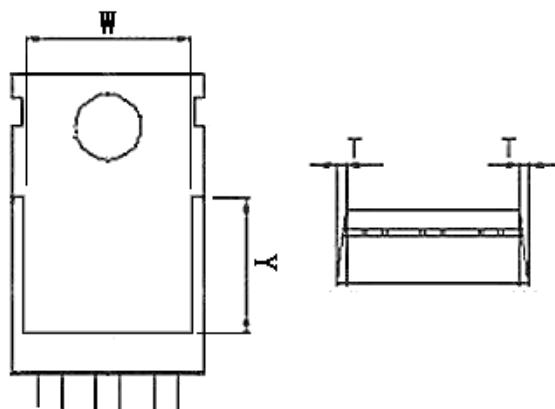
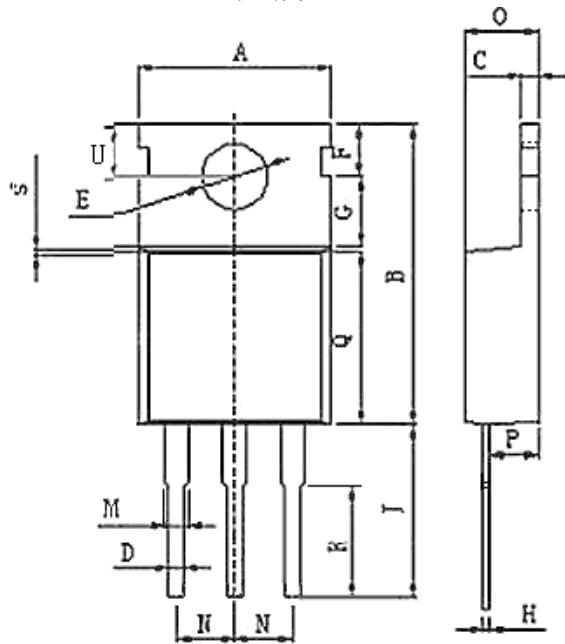
DETAIL A

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S85N14R/S/RP

RP: TO220 (短脚)



DIM	MILLIMETERS
A	10.1±0.2
B	15.6±0.2
C	1.2±0.2
D	0.8±0.2
E	3.7±0.2
F	3.0±0.2
G	3.6±0.2
H	0.5±0.2
J	6.5±0.1
K	3.5±0.1
M	1.3±0.2
N	2.6±0.2
O	4.5±0.2
P	2.0±0.2
Q	9.0±0.2
S	0.25±0.1
T	0.25±0.1
U	2.8±0.07
W	8.0±0.2
Y	6.4±0.2

(单位: mm)