

S60D30C - S60D100C

60A DUAL SCHOTTKY BARRIER RECTIFIER



Features

- Schottky Barrier Chip
- Guard Ring for Transient Protection
- Low Forward Voltage Drop
- Low Power Loss, High Efficiency
- High Surge Current Capability
- Epoxy Meets UL 94V-0 Classification
- Ideally Suited for Use in High Frequency SMPS, Inverters and As Free Wheeling Diodes

Mechanical Data

Case: TO-3P, Molded Plastic

Terminals: Plated Leads Solderable per

MIL-STD-750, Method 2026

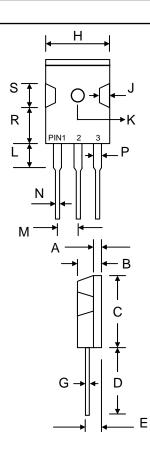
Polarity: See Diagram

Weight: 5.6 grams (approx.)

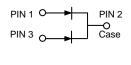
Mounting Position: Any

Mounting Torque: 1.2 N.m Max.

Lead Free: For RoHS / Lead Free Version,
Add "-LF" Suffix to Part Number, See Page 4



TO-3P						
Dim	Min	Max				
Α	1.85	2.15				
В	4.70	5.30				
С	1	23.00				
D	19.00					
Е	2.80	3.20				
G	0.45	0.85				
Н		16.20				
J	1.70	2.70				
K	3.15 Ø	3.65 Ø				
L		4.50				
М	5.25	5.65				
N	1.10	1.40				
Р		2.50				
R	11.70	12.70				
S	5.00	6.00				
All Dimensions in mm						



Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

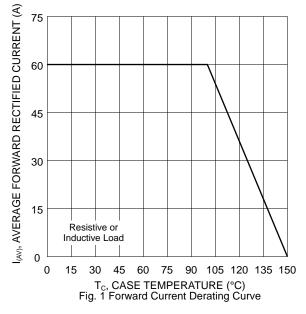
Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

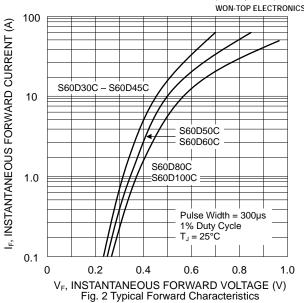
Characteristic	Symbol	S60D 30C	S60D 35C	S60D 40C	S60D 45C	S60D 50C	S60D 60C	\$60D 80C	S60D 100C	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	30	35	40	45	50	60	80	100	٧
RMS Reverse Voltage	VR(RMS)	21	25	28	32	35	42	56	70	V
Average Rectified Output Current Total Device @T _C = 100°C Per Diode	lo	60 30						Α		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	350						А		
Forward Voltage per diode @ $I_F = 30A$, $T_J = 25^{\circ}C$ @ $I_F = 30A$, $T_J = 125^{\circ}C$	VFM	0.65 0.75 0.85 0.55 0.65 0.75						V		
Peak Reverse Current $@T_J = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_J = 100^{\circ}C$	IRM			1.0 50					mA	
Typical Junction Capacitance (Note 1)	Сл	1500 1100					pF			
Thermal Resistance Junction to Ambient per diode Thermal Resistance Junction to Case per diode	R JA R JC	40 1.0						°C/W		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150							°C	

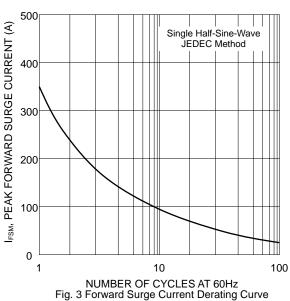
Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

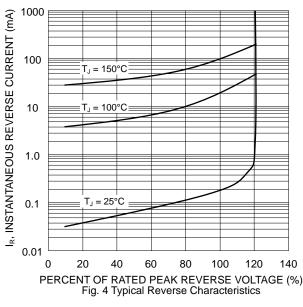
S60D30C - S60D100C

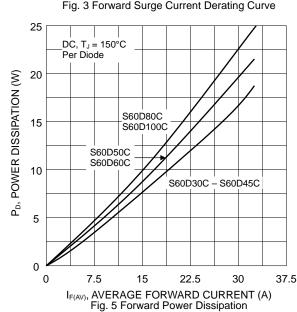


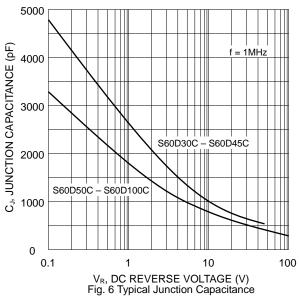














MARKING INFORMATION



S60DxxC = Device Number

xx = 30, 35, 40, 45, 50, 60, 80 or 100

Polarity = As Marked on Body

PACKAGING INFORMATION

BULK

Tube Size	Quantity	Inner Box Size	Quantity	Carton Size	Quantity	Approx. Gross Weight (KG)
L x W x H (mm)	(PCS)	L x W x H (mm)	(PCS)	L x W x H (mm)	(PCS)	
505 x 46 x 6.5	30	520 x 145 x 95	1,200	540 x 306 x 115	2,400	18.0

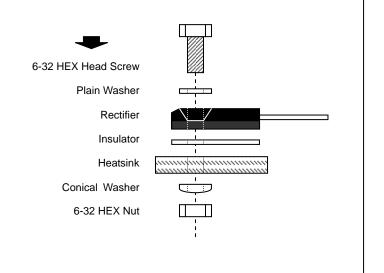
Note: 1. Anti-static tube, water clear color.

RECOMMENDED SCREW MOUNTING ARRANGEMENT

Recommended isolated mounting when screw is at heatsink potential. 6-32 hardware is used.

A conical washer should be used to apply proper force to the device. Screw should not be tightened with any type of air-forced torque or equipment that may cause high impact on device package.

The interface should apply a layer of thermal grease or a highly conductive thermal pad for better heat dissipation.





ORDERING INFORMATION

Product No.	Package Type	Shipping Quantity
S60D30C	TO-3P	30 Units/Tube
S60D35C	TO-3P	30 Units/Tube
S60D40C	TO-3P	30 Units/Tube
S60D45C	TO-3P	30 Units/Tube
S60D50C	TO-3P	30 Units/Tube
S60D60C	TO-3P	30 Units/Tube
S60D80C	TO-3P	30 Units/Tube
S60D100C	TO-3P	30 Units/Tube

- Shipping quantity given is for minimum packing quantity only. For minimum order quantity, please consult the Sales Department.
- To order RoHS / Lead Free version (with Lead Free finish), add "-LF" suffix to part number above. For example, S60D30C-LF.

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WARNING: DO NOT USE IN LIFE SUPPORT EQUIPMENT. WTE power semiconductor products are not authorized for use as critical components in life support devices or systems without the express written approval.

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