



### **Description**

S4580 is the dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application. Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic parts of audio pre-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current, and further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the input low voltage source.

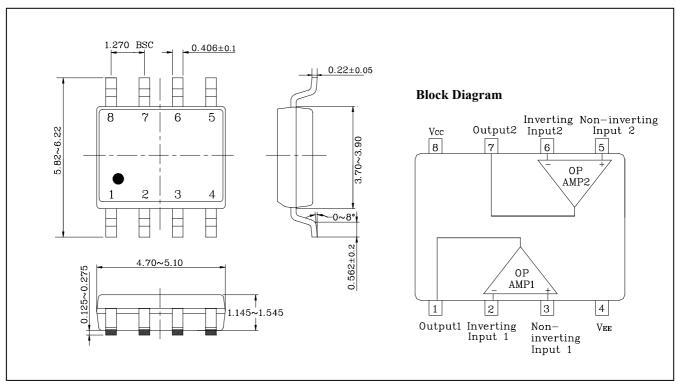
#### **Features**

<ul> <li>Operating voltage.</li> </ul>	$(\pm 2V \sim \pm 18V)$			
<ul> <li>Low input noise voltage.</li> </ul>	(0.8 <i>⋈</i> rms typ.)			
• Wide gain bandwidth product.	(15MHz typ.)			
<ul> <li>Low distortion.</li> </ul>	(0.0005% typ.)			
<ul> <li>Slew rate.</li> </ul>	(5V/µs tvp.)			

### **Ordering Information**

Type NO.	Marking	Package Code		
S4580	S4580	SOP-8		

# Outline Dimensions unit: mm



KSI-K005-001

**Absolute maximum ratings** 

 $Ta = 25 \, ^{\circ}C$ 

Characteristic	Symbol	Ratings	Unit
Supply voltage	$V_{CC}$	±18	V
Differential input voltage	$V_{\mathrm{ID}}$	±30	V
Input voltage	V <sub>IC</sub> (note)	±15	V
Output current	I <sub>O</sub>	±50	mA
Power Dissipation	P <sub>D</sub>	300	mW
Operating temperature	T <sub>opr</sub>	-40 ~ +85	°C
Storage temperature	T <sub>stg</sub>	-40 ~ +125	°C

(note) For supply voltage less than  $\pm 15$ V, the absolute maximum input voltage is equal to the supply voltage.

### **Electrical Characteristics**

(Unless otherwise specified.  $V_{CC}$  = +15V,  $V_{EE}$ = -15V and Ta = 25 °C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Input offset voltage	$V_{IOS}$	Rg ≤ 10 kΩ	-	0.5	3	mV
Input offset current	I <sub>IOS</sub>	-	-	5	200	nA
Input bias current	$I_{IB}$	-	-	100	500	nA
Input common mode Voltage Range	$V_{\rm ICR}$	-	±12	±13.5	-	V
Maximum Output Voltage	V <sub>OM</sub>	$R_L \ge 2 \text{ k}\Omega$	±12	±13.5	-	V
Large signal Voltage Gain	G <sub>V</sub>	Vout=±10V, RL≥2 kΩ	90	110	-	dB
Common mode rejection ratio	CMRR	Rg ≤ 10 kΩ	80	110	-	dB
Power supply voltage rejection ratio	PSRR	Rg ≤ 10 kΩ	80	110	-	dB
Slew Rate	SR	R <sub>L</sub> ≥ 2 kΩ	-	5.0	-	V/us
Supply Current	I <sub>CC</sub>	-	-	6.0	9.0	mA
Equivalent input noise voltage	V <sub>NI</sub>	RIAA, $R_S=2.2 \text{ k}\Omega$ , $f=30 \text{ kHz}$ LPF	-	0.8	-	uVrms
Total harmonic distortion	THD	$A_V$ =20dB, $V_O$ =5V, $R_L$ =2 $k\Omega$ f=1KHz	-	0.0005	-	%
Gain bandwidth product	GB	f=10KHz	-	15	-	MHz

### **Electrical Characteristic Curves**

Fig. 1  $G_V$  - f

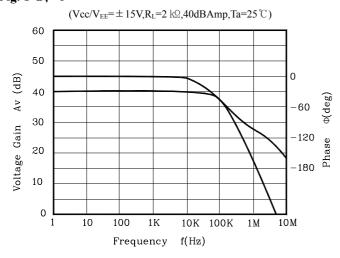


Fig. 2 V<sub>OP-P</sub>- f

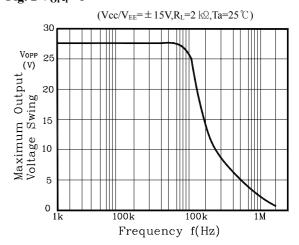


Fig. 3  $I_{IB}$  -  $T_a$ 

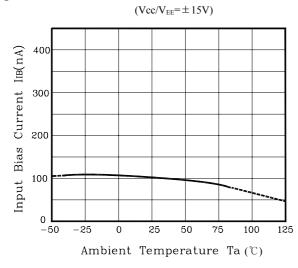


Fig. 4  $V_{OM}$  -  $V_{CC}\!/V_{EE}$ 

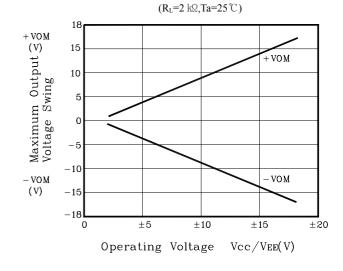


Fig. 5  $V_{OM}$  -  $R_L$ 

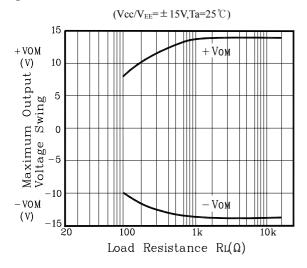
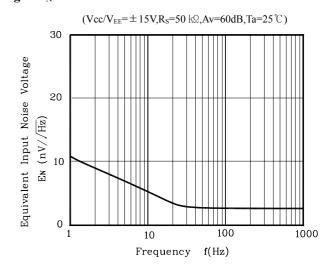


Fig. 6  $E_N$  - f



## **Electrical Characteristic Curves**

Fig. 7  $V_{OM}$  -  $T_a$ 

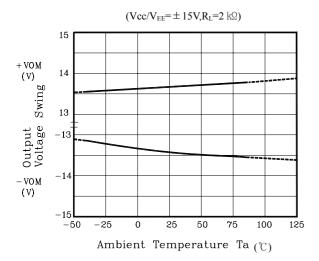


Fig. 8 I<sub>CC</sub> - Ta

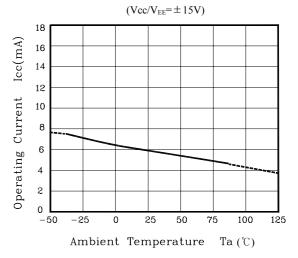


Fig. 9 V<sub>IOS</sub> - Ta

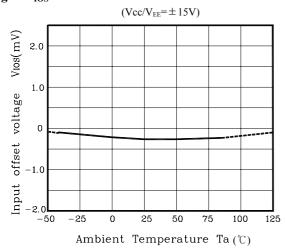
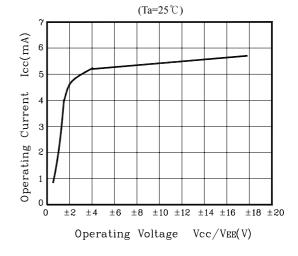


Fig. 10  $I_{CC}$  - Vcc



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