

V _{DSS}	20V
R _{DS(on)} (Max.)	54m Ω
Ι _D	2.5A
P _D	W8.0

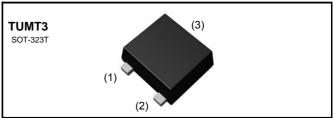
Features

- 1) Low on resistance.
- 2) 1.5V Drive.
- 3) Built-in G-S Protection Diode.
- 4) Small Surface Mount Package (TUMT3).
- 5) Pb-free lead plating ; RoHS compliant

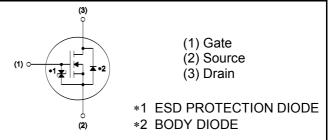
Application

DC/DC converters

Outline



Inner circuit



Packaging specifications

Туре	Packaging	Taping
	Reel size (mm)	180
	Tape width (mm)	8
	Basic ordering unit (pcs)	3,000
	Taping code	TL
	Marking	XE

●Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	20	V
Continuous drain current	ا _D *1	±2.5	А
Pulsed drain current	^{*2} ا _{D,pulse}	±5	А
Gate - Source voltage	V _{GSS}	±10	V
Dower dissinction	P _D *3	0.8	W
Power dissipation	P _D *4	0.32	W
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	–55 to +150	°C

•Thermal resistance

Parameter	Symbol	Values			Unit
Faranielei	Symbol	Min.	Тур.	Max.	Onit
Thermal resistance, junction - ambient	R_{thJA} *3	-	-	156	°C/W
	R_{thJA} *4	-	-	391	°C/W

•Electrical characteristics(T_a = 25°C)

Parameter	Sumbol	Conditions		Values		Unit
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	20	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D =1mA referenced to 25°C	-	20	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 20V, V _{GS} = 0V	-	-	1	μA
Gate - Source leakage current	I _{GSS}	V_{GS} = ±10V, V_{DS} = 0V	-	-	±10	μA
Gate threshold voltage	$V_{GS (th)}$	V _{DS} = 10V, I _D = 1mA	0.3	-	1.3	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D =1mA referenced to 25°C	-	-1.9	-	mV/°C
		V _{GS} =4.5V, I _D =2.5A	-	39	54	
		V _{GS} =2.5V, I _D =2.5A	-	49	68	
Static drain - source on - state resistance	$R_{DS(on)}$ *5	V _{GS} =1.8V, I _D =1.3A	-	65	91	mΩ
		V _{GS} =1.5V, I _D =0.5A	-	80	160	
		V _{GS} =4.5V, I _D =2.5A, T _j =125°C	-	65	95	
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	7.5	I	Ω
Transconductance	g _{fs} *5	V _{DS} =10V, I _D =2.5A	3.6	6.0	-	S

*1 Limited only by maximum temperature allowed.

*2 Pw \leq 10 $\mu s,$ Duty cycle \leq 1%

*3 Mounted on a seramic board (30×30×0.8mm)

- *4 Mounted on a FR4 (15×20×0.8mm)
- *5 Pulsed

•Electrical characteristics($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	370	-	
Output capacitance	C _{oss}	V _{DS} = 10V	-	90	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	50	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 10V, V_{GS} = 4.5V$	-	7	-	
Rise time	t _r *5	I _D = 1.3A	-	15	-	no
Turn - off delay time	t _{d(off)} *5	R _L = 7.7Ω	-	35	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	15	-	

•Gate Charge characteristics($T_a = 25^{\circ}C$)

Parameter	Symbol Conditions	Conditions	Values			Unit
Faranieler	Symbol Conditions		Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*5}	V _{DD} ≃ 10V, I _D =2.5A V _{GS} = 4.5V	-	5	-	
Gate - Source charge	Q_{gs}^{*5}		-	0.9	-	nC
Gate - Drain charge	Q_{gd} *5		-	0.8	-	

•Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions		Values			Unit
Faranieter			Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	ا _S *1	T _a = 25°C	-	-	0.6	A
Forward voltage	V_{SD} *5	V _{GS} = 0V, I _s = 0.6A	-	-	1.2	V

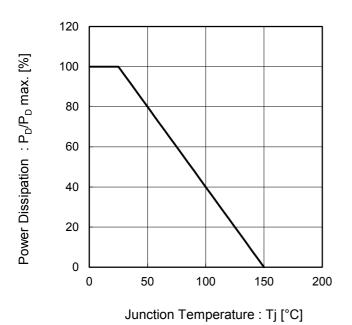


Fig.1 Power Dissipation Derating Curve

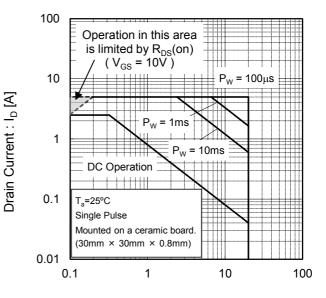


Fig.2 Maximum Safe Operating Area

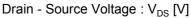


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

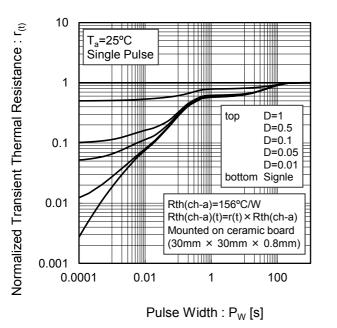
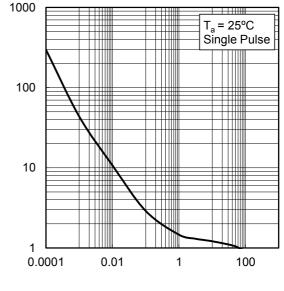


Fig.4 Single Pulse Maxmum Power dissipation



Pulse Width : $\mathsf{P}_{\mathsf{W}}\left[\mathsf{s}\right]$

Peak Transient Power : P(W)

Drain Current : I_D [A]

•Electrical characteristic curves

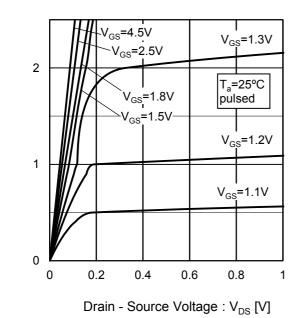
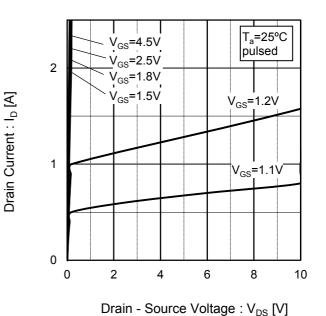


Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)



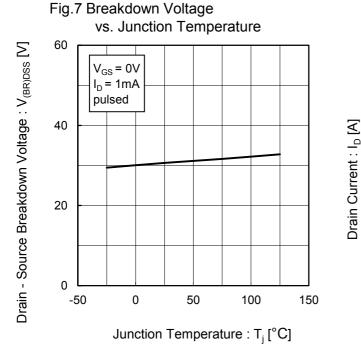
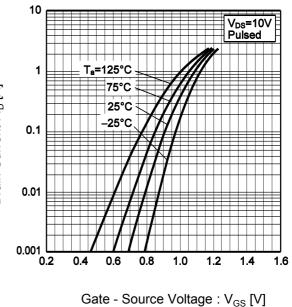
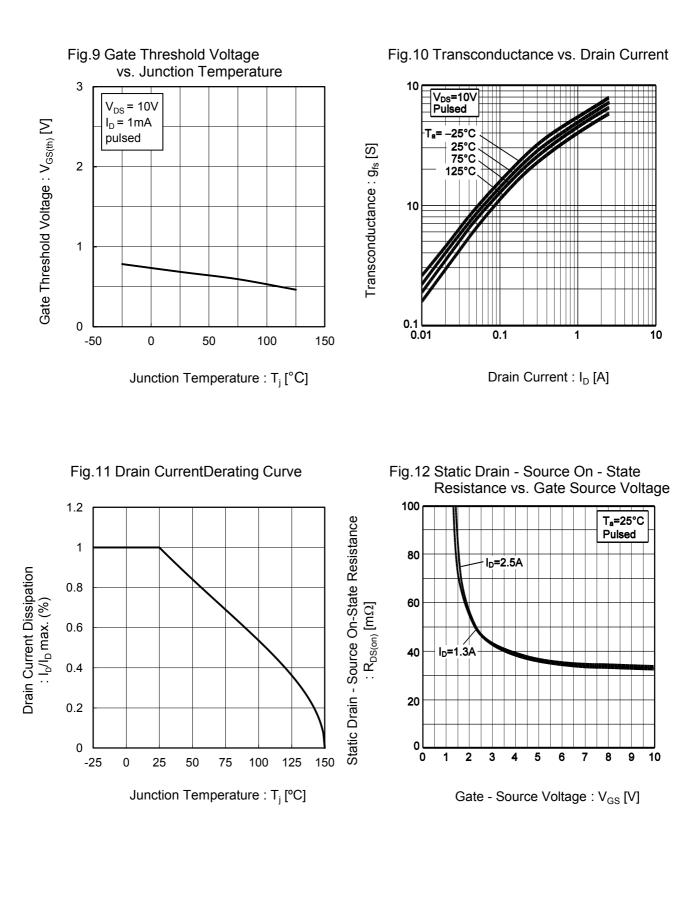
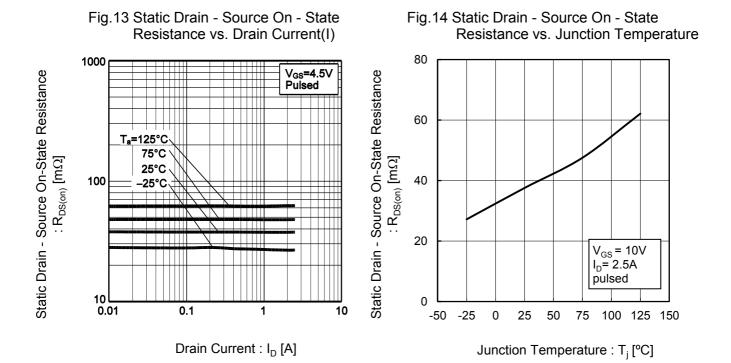
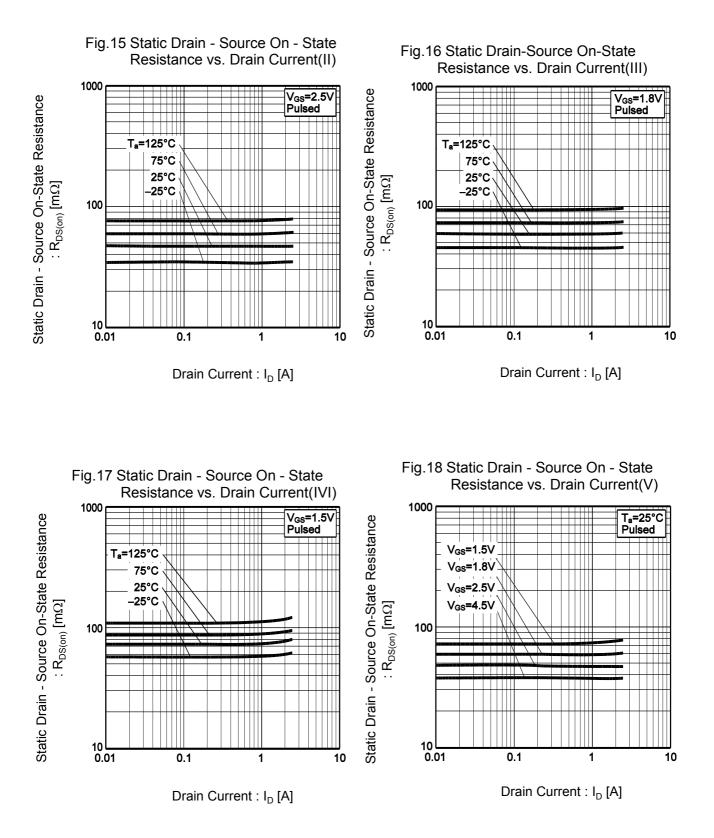


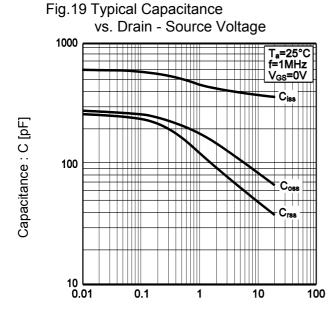
Fig.8 Typical Transfer Characteristics









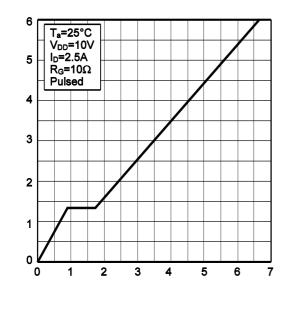


Drain - Source Voltage : V_{DS} [V]

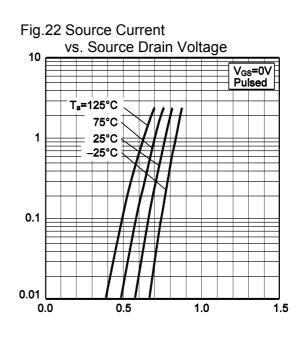


Switching Time : t [ns]

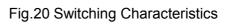
Fig.21 Dynamic Input Characteristics

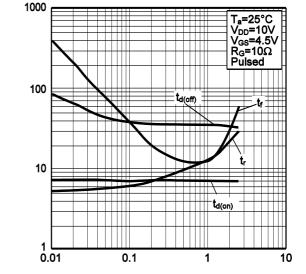


Total Gate Charge : Q_g [nC]



Source-Drain Voltage : V_{SD} [V]

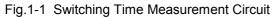




Drain Current : I_D [A]

Source Current : I_s [A]

•Measurement circuits



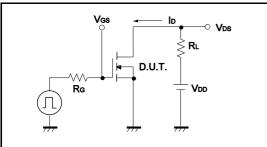


Fig.2-1 Gate Charge Measurement Circuit

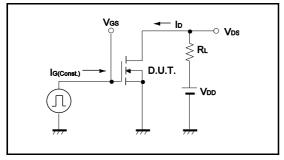
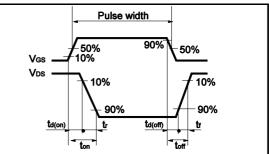
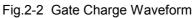
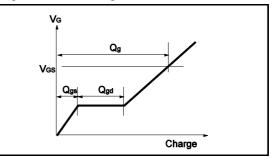


Fig.1-2 Switching Waveforms

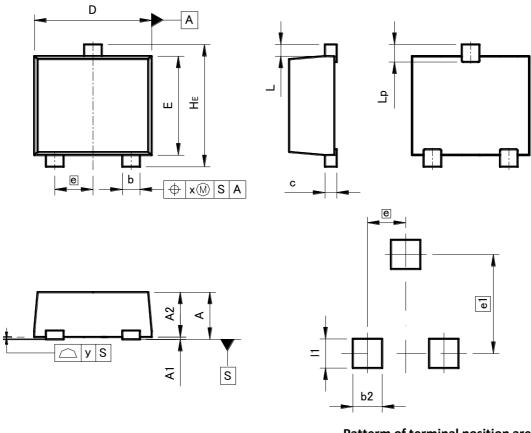






•Dimensions (Unit : mm)

ТИМТ3



Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
А	-	0.85	-	0.033
A1	0.00	0.10	0	0.004
A2	0.72	0.82	0.028	0.032
b	0.25	0.40	0.01	0.016
с	0.12	0.22	0.005	0.009
D	1.90	2.10	0.075	0.083
E	1.60	1.80	0.063	0.071
е	0.0	65	0.0	03
HE	2.00	2.20	0.079	0.087
L	0.20		0.0	01
Lp	_	0.40	_	0.016
х	-	0.10	-	0.004
у	_	0.10	_	0.004

DIM	MILIMETERS		MILIMETERS INCH		HES
DIN	MIN	MAX	MIN	MAX	
e1	1.70		0.067		
b2	-	0.50	-	0.02	
1	Ι	0.50	Ι	0.02	

Dimension in mm/inches

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