RT3PFFM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

RT3PFFM is composite transistor built with two RT1P431 chips in SC-88 package.

FEATURE

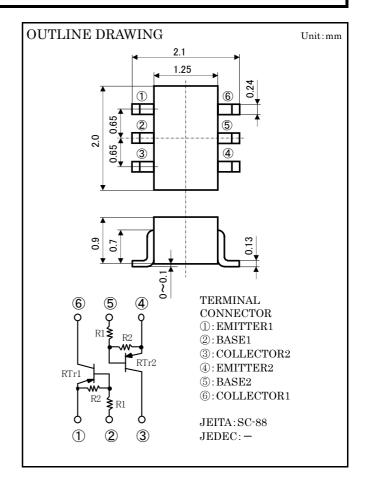
Silicon epitaxial type

Each transistor elements are independent.

Mini package for easy mounting

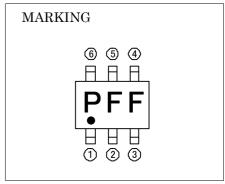
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	-50	V
VEBO	Emitter to Base voltage	-10	V
$V_{\rm CEO}$	Collector to Emitter voltage	-50	V
V_{IN}	Input voltage	-30	V
Ic	Collector current	-100	mA
Icm	Peak Collector current	-200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{ m stg}$	Storage temperature	-55~+150	°C



$ELECTRICAL\ CHARACTERISTICS (Ta=25^{\circ}C) (RTr1,\ RTr2\ COMMON)$

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			112110
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I c=-100μA, R _{BE} =∞	-50	_	_	V
Icbo	Collector cut off current	V _{CB} =-50V, I _E =0	_	_	-0.1	μA
IEBO	Emitter cut off current	V_{EB} =-5V, I $_{C}$ =0	-399	-532	-771	μA
hfe	DC forward current gain	V_{CE} =-5 V , I $_{C}$ =-10 mA	20	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I c =-10mA, I _B =-0.5mA	_	-0.1	-0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =-0.2V, I _C =-5mA	_	-1.4	-2.3	V
$V_{\text{I(OFF)}}$	Input off voltage	V _{CE} =-5V, I _C =-100μA	-0.8	-1.1	_	V
R ₁	Input resistor	_	3.3	4.7	6.1	kΩ
R_2/R_1	Resistor ratio	_	0.8	1.0	1.2	_
f_{T}	Gain band width product	V _{CE} =-6V, I _E =10mA	_	150	_	MHz

RT3PFFM

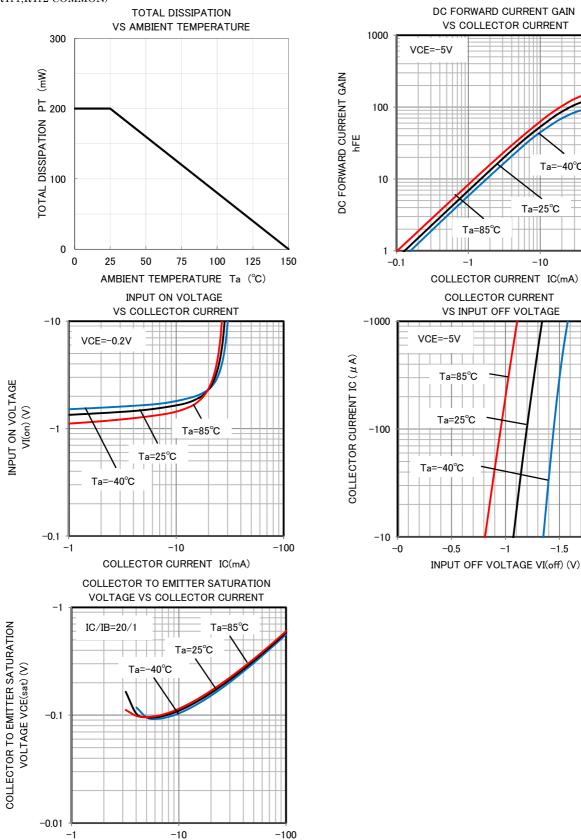
-100

-1.5

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

TYPICAL CHARACTERISTICS

(RTr1,RTr2 COMMON)



COLLECTOR CURRENT IC(mA)



6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

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