

# RT3PEEM

Composite Transistor With Resistor  
For Switching Application  
Silicon Epitaxial Type

## DESCRIPTION

RT3PEEM is compound transistor built with two RT1P234 chips in SC-88 package.

## FEATURE

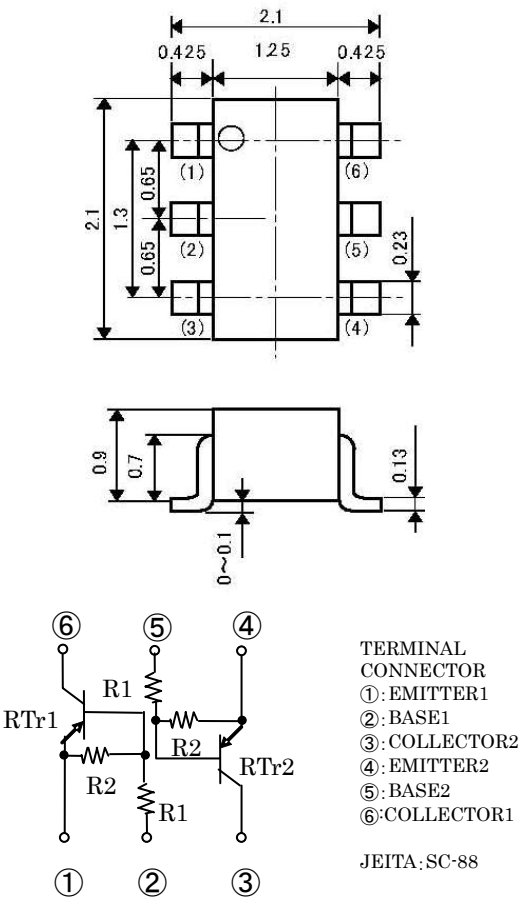
- Silicon epitaxial type
- Each transistor elements are independent.
- Mini package for easy mounting

## APPLICATION

- Inverted circuit, switching circuit,
- interface circuit, driver circuit

## OUTLINE DRAWING

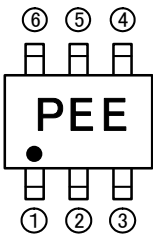
Unit: mm



## MAXIMUM RATING (Ta=25°C) (RTr1, RTr2)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	-50	V
VEBO	Emitter to Base voltage	-6	V
VCEO	Collector to Emitter voltage	-50	V
VIN	Input voltage	-12	V
IC	Collector current	-100	mA
ICM	Peak Collector current	-200	mA
PC	Collector dissipation (Total, Ta=25°C)	150	mW
Tj	Junction temperature	+150	°C
Tstg	Storage temperature	-55~+150	°C

## MARKING



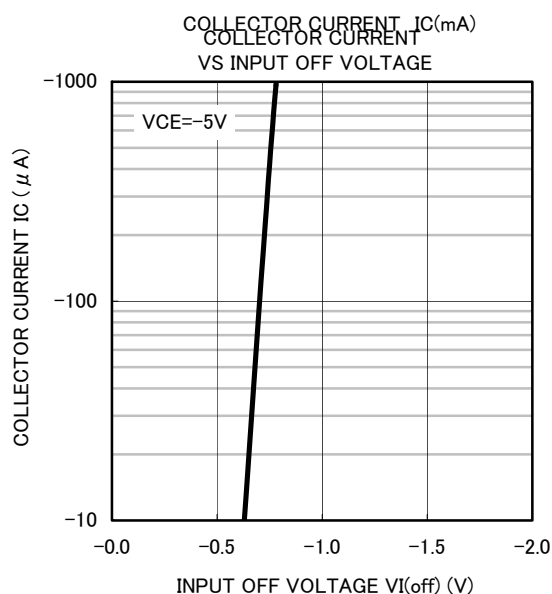
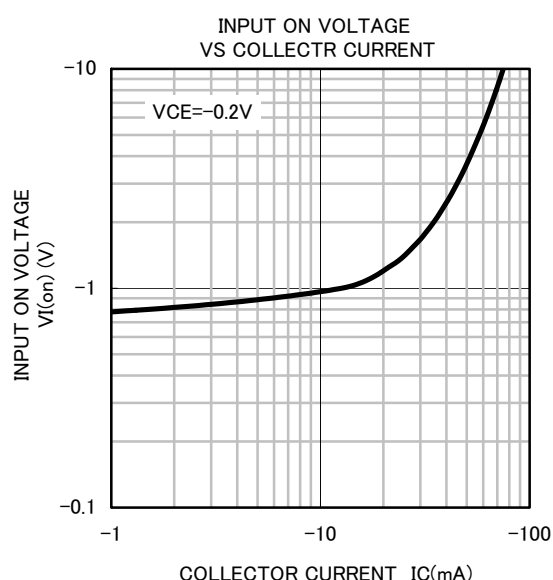
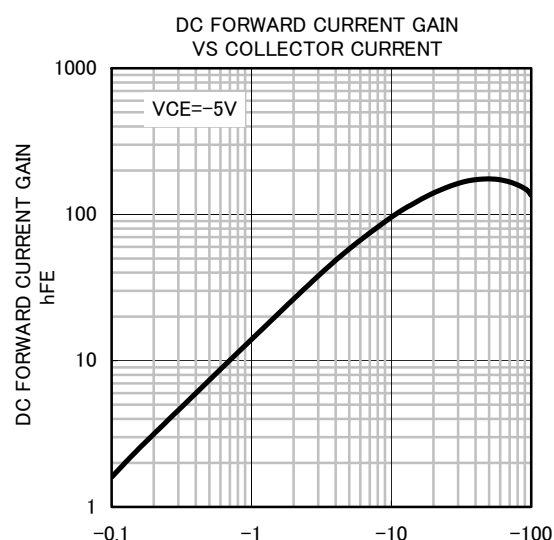
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## ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr1, RTr2)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
V(BR)CEO	Collector to Emitter break down voltage	I <sub>C</sub> =-100μA, R <sub>BE</sub> =∞	-50	—	—	V
I <sub>CBO</sub>	Collector cut off current	V <sub>CB</sub> =-50V, I <sub>E</sub> =0	—	—	-0.1	μA
h <sub>FE</sub>	DC forward current gain	V <sub>CE</sub> =-5V, I <sub>C</sub> =-10mA	33	—	—	-
V <sub>CE(sat)</sub>	Collector to Emitter saturation voltage	I <sub>C</sub> =-10mA, I <sub>B</sub> =-0.5mA	—	—	-0.3	V
V <sub>I(ON)</sub>	Input on voltage	V <sub>CE</sub> =-0.2V, I <sub>C</sub> =-5mA	—	-0.8	-1.4	V
V <sub>I(OFF)</sub>	Input off voltage	V <sub>CE</sub> =-5V, I <sub>C</sub> =-100μA	-0.5	-0.7	—	V
R <sub>1</sub>	Input resistor		1.5	2.2	2.9	kΩ
R <sub>2</sub> /R <sub>1</sub>	Resistor ratio		3.8	4.7	5.6	-
f <sub>T</sub>	Gain band width product	V <sub>CE</sub> =-6V, I <sub>E</sub> =10mA, f=100MHz	—	150	—	MHz

## TYPICAL CHARACTERISTICS (RTr1,RTr2)





6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

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