

RQJ0204XGDQA

Silicon P Channel MOS FET Power Switching

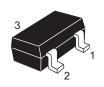
R07DS0293EJ0500 Rev.5.00 Jan 10, 2014

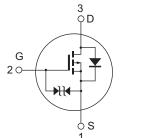
Features

- Low on-resistance
 - $R_{DS(on)} = 219 \text{ m}\Omega \text{ typ } (V_{GS} = -4.5 \text{ V}, I_D = -0.8 \text{ A})$
- Low drive current
- High speed switching
- 2.5 V gate drive

Outline

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)





1. Source

2. Gate

3. Drain

Note: Marking is "XG".

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

			(14 25 0
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-20	V
Gate to source voltage	V _{GSS}	+8 / -12	V
Drain current	I _D	-1.6	А
Drain peak current	I _{D(pulse)} Note1	-4.0	А
Body - drain diode reverse drain current	I _{DR}	-1.6	А
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)

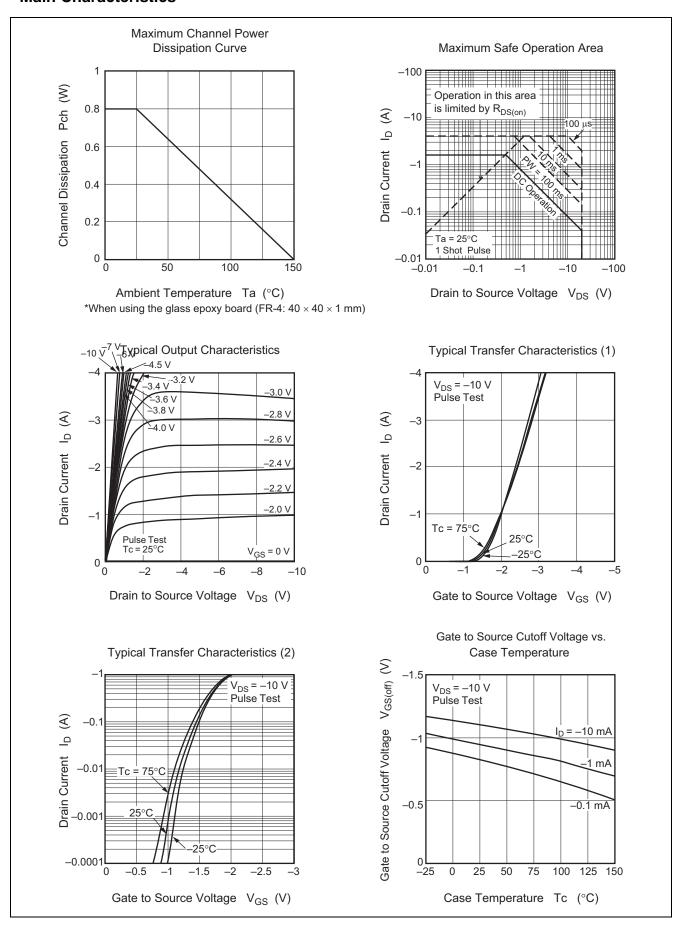
Electrical Characteristics

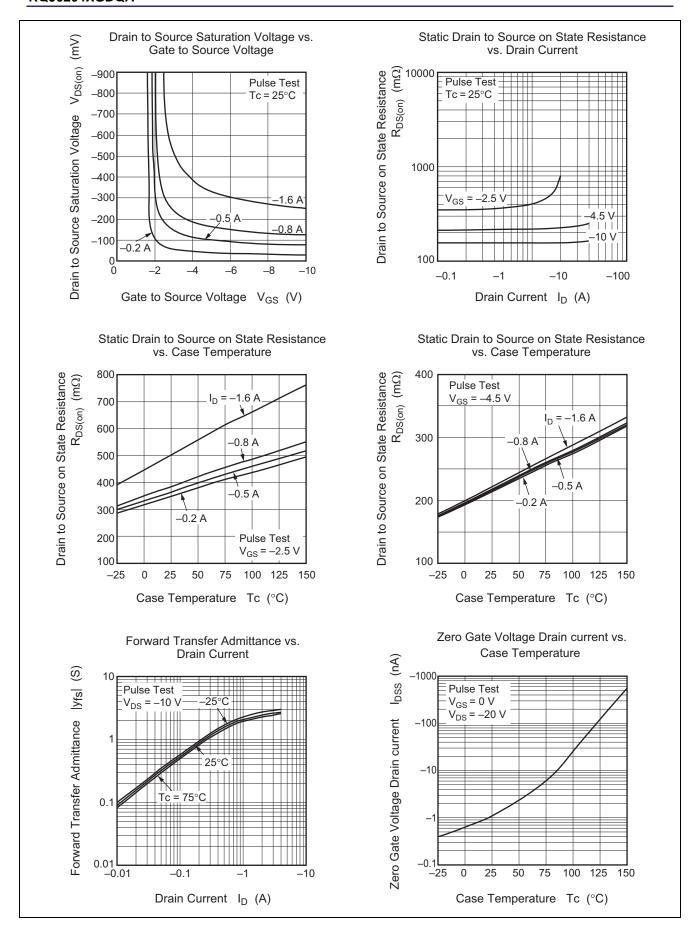
 $(Ta = 25^{\circ}C)$

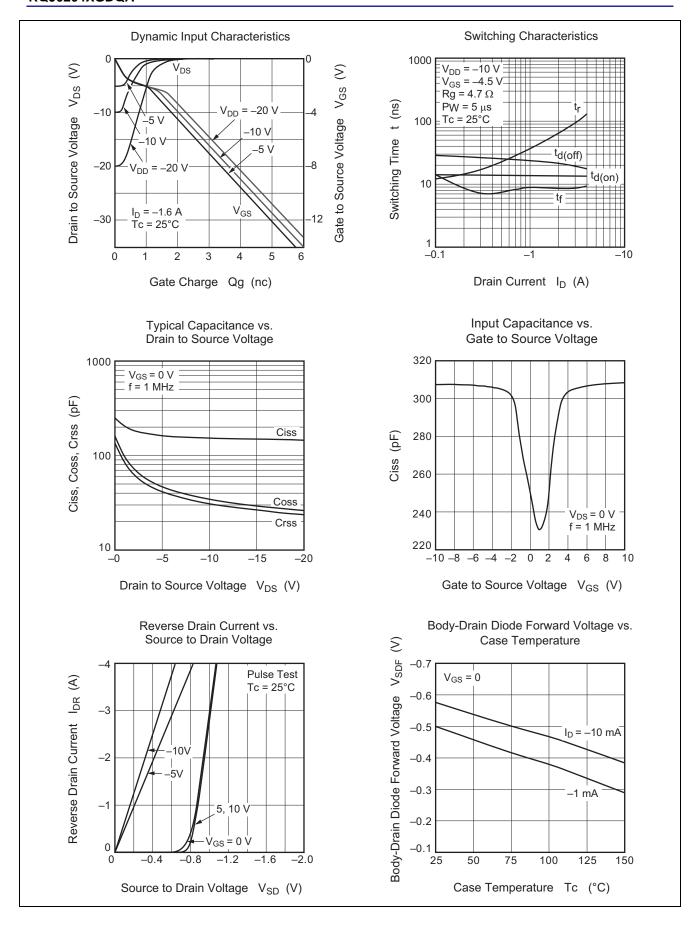
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	-20	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	+8	_	_	V	$I_G = +100 \mu\text{A}, V_{DS} = 0$	
	V _{(BR)GSS}	-12	_		V	$I_G = -100 \mu\text{A}, V_{DS} = 0$	
Gate to source leak current	I _{GSS}		_	+10	μΑ	$V_{GS} = +6 \text{ V}, V_{DS} = 0$	
	I _{GSS}	_	_	-10	μΑ	$V_{GS} = -10 \text{ V}, V_{DS} = 0$	
Drain to source leak current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -20 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	-0.4	_	-1.4	V	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	
Drain to source on state resistance	R _{DS(on)}	_	219	280	mΩ	$I_D = -0.8 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$	
	R _{DS(on)}	_	363	510	mΩ	$I_D = -0.8 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$	
Forward transfer admittance	y _{fs}	1.3	1.9	_	S	$I_D = -0.8 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$	
Input capacitance	Ciss	_	153	_	pF	V _{DS} = -10 V	
Output capacitance	Coss	_	37	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	31	_	pF	f = 1 MHz	
Turn - on delay time	t _{d(on)}	_	14	_	ns	$I_D = -0.8 \text{ A}$	
Rise time	t _r	_	33	_	ns	$V_{GS} = -4.5 \text{ V}$	
Turn - off delay time	$t_{d(off)}$	_	24	_	ns	$R_L = 12.5 \Omega$	
Fall time	t _f	_	8	_	ns	$Rg = 4.7 \Omega$	
Total gate charge	Qg		2.2		nC	V _{DD} = -10 V	
Gate to source charge	Qgs	_	0.5	_	nC	$V_{GS} = -4.5 \text{ V}$	
Gate to drain charge	Qgd	_	0.9	_	nC	$I_D = -1.6A$	
Body - drain diode forward voltage	V_{DF}	_	-0.85	-1.1	V	$I_F = -1.6 \text{ A}, V_{GS} = 0^{\text{Note3}}$	

Notes: 3. Pulse test

Main Characteristics

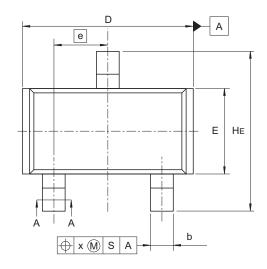


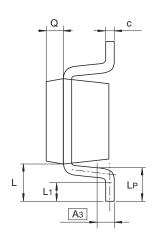


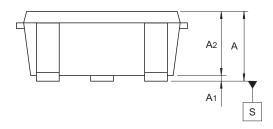


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011









Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	1.0	_	1.3
A ₁	0	_	0.1
A ₂	1.0	1.1	1.2
A_3		0.25	_
b	0.35	0.4	0.5
С	0.1	0.16	0.26
D	2.7	_	3.1
E	1.35	1.5	1.65
е	_	0.95	_
HE	2.2	2.8	3.0
L	0.35	_	0.75
L ₁	0.15	_	0.55
L _P	0.25		0.65
Х			0.05
Q		0.3	

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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0204XGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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