

RQJ0202VGDQA

Silicon P Channel MOS FET Power Switching

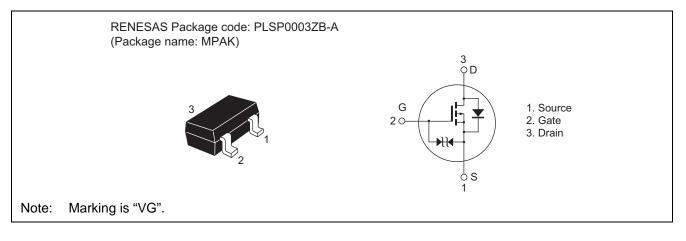
R07DS0291EJ0500 Rev.5.00 Jan 10, 2014

Datasheet

Features

- Low on-resistance
- $R_{DS(on)} = 83 \text{ m}\Omega \text{ typ} (V_{GS} = -4.5 \text{ V}, I_D = -1.4 \text{ A})$
- Low drive current
- High speed switching
- 2.5 V gate drive

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
ltem	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-20	V
Gate to source voltage	V _{GSS}	+8 / -12	V
Drain current	ID	-2.7	А
Drain peak current	I _{D(pulse)} Note1	-8.0	А
Body - drain diode reverse drain current	I _{DR}	-2.7	А
Channel dissipation	Pch ^{Note2}	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)



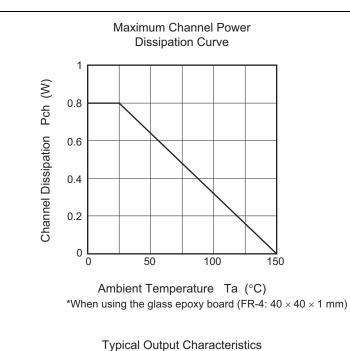
Electrical Characteristics

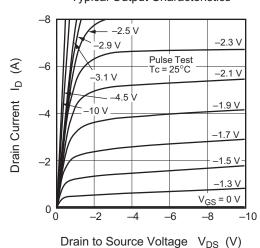
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-20	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	+8		_	V	$I_{G} = +100 \ \mu A, V_{DS} = 0$
	V _{(BR)GSS}	-12		_	V	$I_{G} = -100 \ \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	+10	μA	$V_{GS} = +6 V, V_{DS} = 0$
	I _{GSS}			-10	μΑ	$V_{GS} = -10 V, V_{DS} = 0$
Drain to source leak current	I _{DSS}			-1	μΑ	$V_{DS} = -20 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	-0.4	_	-1.4	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}		83	105	mΩ	$I_D = -1.4 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
	R _{DS(on)}		124	170	mΩ	$I_D = -1.4 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	3	4.5	_	S	$I_D = -1.4 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss		365	_	pF	V _{DS} = -10 V
Output capacitance	Coss		102		pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	70	_	pF	
Turn - on delay time	t _{d(on)}		15	_	ns	$I_{\rm D} = -1.4 \text{ A}$
Rise time	tr		57	_	ns	$V_{GS} = -4.5 V$ $R_L = 7.1 \Omega$ $Rg = 4.7 \Omega$
Turn - off delay time	t _{d(off)}		40	_	ns	
Fall time	t _f	_	21	_	ns	
Total gate charge	Qg		4.3	_	nC	$V_{DD} = -10 V$
Gate to source charge	Qgs	_	0.6	_	nC	$V_{GS} = -4.5 V$ $I_D = -2.7 A$
Gate to drain charge	Qgd	_	1.7	_	nC	
Body - drain diode forward voltage	V _{DF}		-0.85	-1.1	V	$I_F = -2.7 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

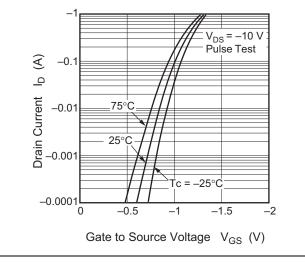


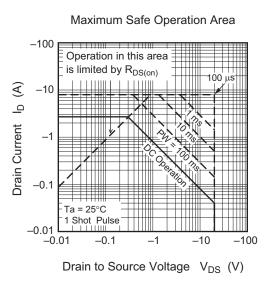
Main Characteristics



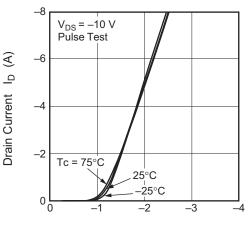




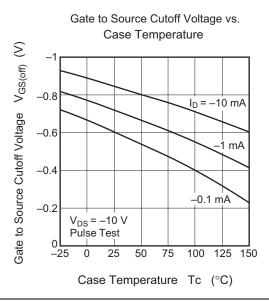




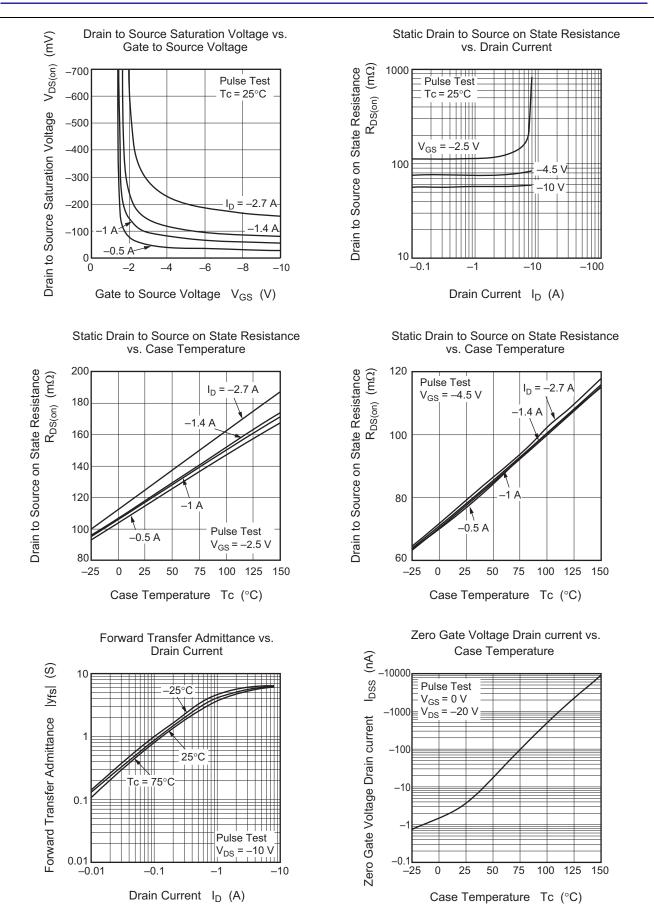
Typical Transfer Characteristics (1)



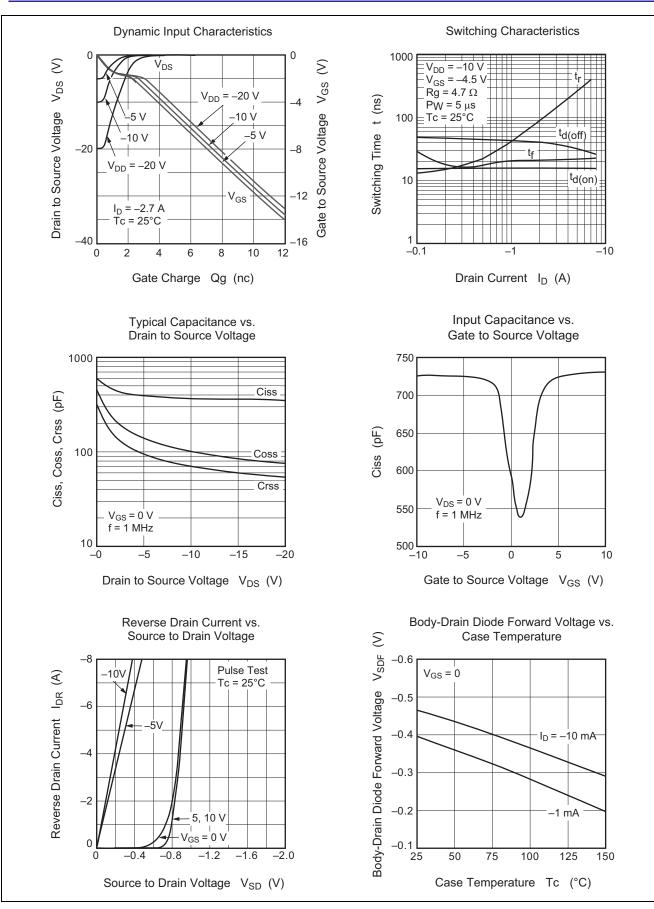
Gate to Source Voltage V_{GS} (V)





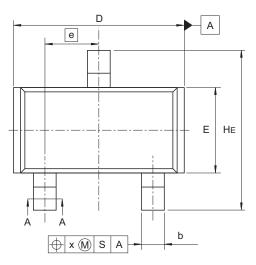


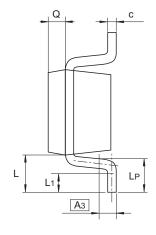


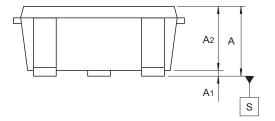


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011











Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	1.0		1.3
A ₁	0		0.1
A ₂	1.0	1.1	1.2
A ₃		0.25	—
b	0.35	0.4	0.5
С	0.1	0.16	0.26
D	2.7	—	3.1
E	1.35	1.5	1.65
е		0.95	
HE	2.2	2.8	3.0
L	0.35	—	0.75
L ₁	0.15		0.55
LP	0.25		0.65
Х			0.05
Q		0.3	_

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Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0202VGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping



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