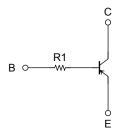
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN2972CT, RN2973CT

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch Small Mold (6pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN1972CT and RN1973CT

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-20	٧
Collector-emitter voltage	V _{CEO}	-20	V
Emitter-base voltage	V _{EBO}	-5	٧
Collector current	IC	-50	mA
Collector power dissipation	P _C *	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 to150	°C

0.15±0.03 0.15±0.03 0.15±0.03 0.15±0.03 0.07±0.03

Unit: mm

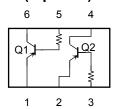
	1.EMIITTER1	(E1)
CST6	2.EMITTER2 3.BASE2 4.COLLECTOR2 5.BASE1 6.COLLECTOR1	(E2) (B2) (C2) (B1) (C1)
JEDEC	_	

2-1K1A

Weight: 1.0 mg (typ.)

Equivalent Circuit (top view)

JEITA TOSHIBA



Note *: Total rating

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

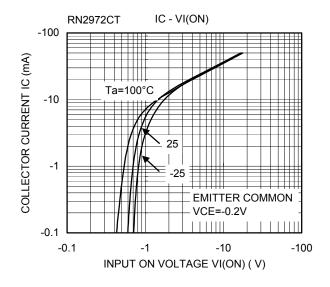
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

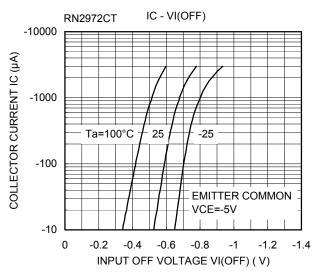


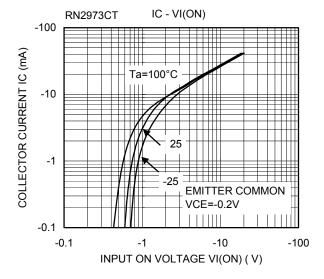
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

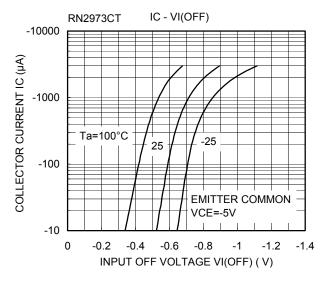
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = -5 \text{ V}, I_C = 0$	_		-100	nA
DC current gain		h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -1 \text{mA}$	300	_	_	_
Collector-emitter saturation voltage		V _{CE (sat)}	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_		-0.15	V
Collector output capacitance		C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	1.2	_	pF
Input resistor	RN2972CT	- R1	_	17.6	22	26.4	- kΩ
	RN2973CT			37.6	47	56.4	

(Q1, Q2 common)



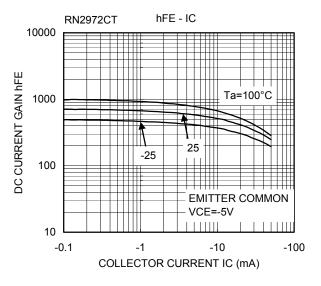


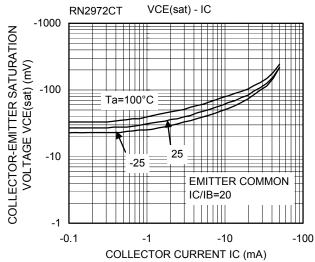


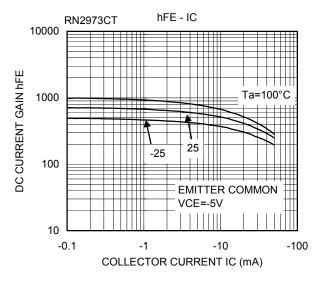


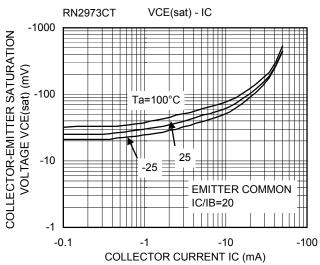
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(Q1, Q2 common)



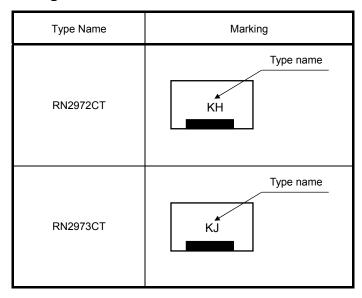








Marking



Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.



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