2.5V Drive Nch MOS FET

RJU002N06

Structure

Silicon N-channel MOS FET

● Features

- 1) Low On-resistance.
- 2) Low voltage drive (2.5V drive).

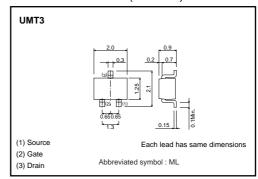
Applications

Switching

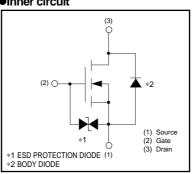
Packaging specifications

	Package	Taping
Type	Code	T106
	Basic ordering unit (pieces)	3000
RJU002N06	0	

●External dimensions (Unit : mm)



●Inner circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	60	V	
Gate-source voltage		V _{GSS}	±12	V	
Drain current	Continuous	I _D	±200	mA	
	Pulsed	IDP *1	±800	mA	
Total power dissipation		P _D *2	200	mW	
Channel temperature		Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C	

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	625	°C/W

^{*} Each terminal mounted on a recommended land

^{*1} Pw≤10μs, Duty cycle≤1% *2 Each terminal mounted on a recommended land

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	1	-	±10	μΑ	Vgs=±12V, Vps=0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	60	_	_	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	_	1	μΑ	V _{DS} = 60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.5	_	1.5	٧	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state resistance	R _{DS (on)} *	-	1.6	2.3	Ω	I _D = 200mA, V _{GS} = 4.5V
		_	1.7	2.4	Ω	I _D = 200mA, V _{GS} = 4V
		_	2.2	3.1	Ω	I _D = 200mA, V _{GS} = 2.5V
Forward transfer admittance	Y _{fs} *	0.1	_	_	S	V _{DS} = 10V, I _D = 200mA
Input capacitance	Ciss	_	18	_	pF	V _{DS} = 10V
Output capacitance	Coss	_	7	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	5	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	7	_	ns	V _{DD} ≒ 30V
Rise time	tr *	_	7	_	ns	ID= 100mA
Turn-off delay time	t _{d (off)} *	_	12	_	ns	V _{GS} = 4V R _L =300Ω
Fall time	t _f *	_	90	_	ns	R _G =10Ω

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	-	-	1.2	V	I _S = 0.16A, V _{GS} =0V

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