

RJK60S5DPN

600V - 20A - MOS FET High Speed Power Switching R07DS0952EJ0200 Rev.2.00 Jan 23, 2013

Features

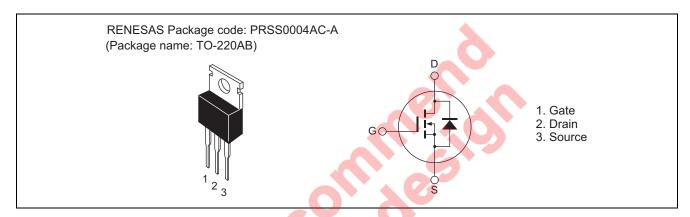
- Superjunction MOSFET
- Low on-resistance

 $R_{DS(on)} = 0.150 \Omega \text{ typ. (at } I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}, Ta = 25 ^{\circ}\text{C})$

• High speed switching

 $t_f = 23 \text{ ns typ.}$ (at $I_D = 10 \text{ A}$, $V_{GS} = 10 \text{ V}$, $R_L = 30 \Omega$, $Rg = 10 \Omega$, $Ta = 25 ^{\circ}\text{C}$)

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	600	V	
Gate to source voltage	V _{GSS}	+30, -20	V	
Drain current Tc = 25°C	I _D Note1	20	Α	
Tc = 100°C	I _D Note1	12.6	Α	
Drain peak current	I _{D (pulse)} Note1	40	Α	
Body-drain diode reverse drain current	I _{DR} Note1	20	Α	
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	40	Α	
Avalanche current	I _{AP} Note2	5	Α	
Avalanche energy	E _{AR} Note2	1.36	mJ	
MOSFET dv/dt ruggedness	dv/dt Note3	150	V/ns	
Channel dissipation	Pch Note2	166.6	W	
Channel to case thermal impedance	θch-c	0.75	°C/W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. Limited by Tch max.

- 2. STch = 25° C, Tch $\leq 150^{\circ}$ C
- 3. Value at Tj = 25°C, $V_{DS} \le 480 \text{ V}$
- 4. Value at Tc = 25°C

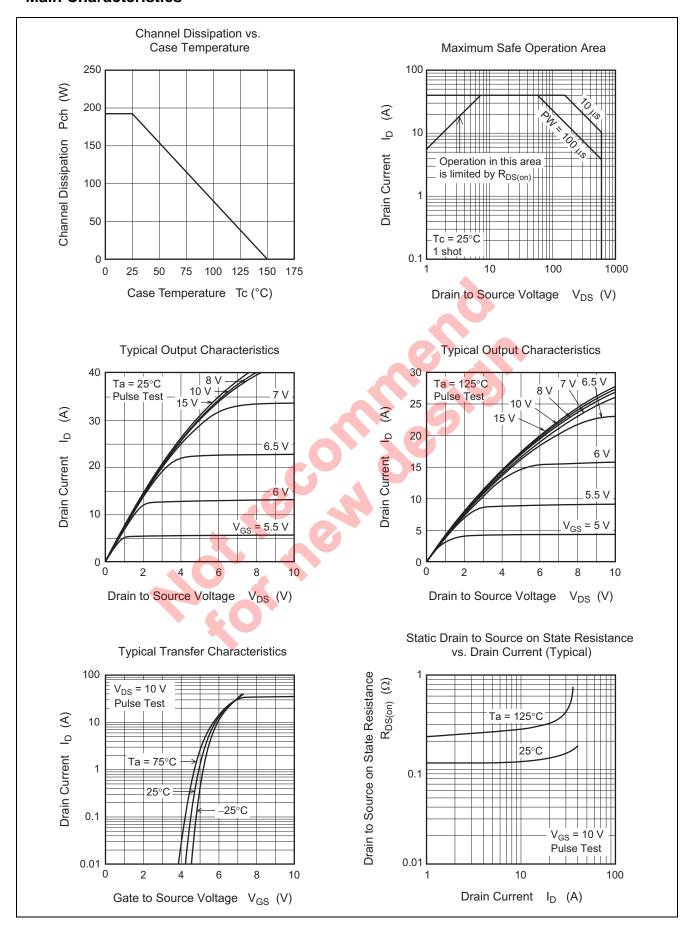
Electrical Characteristics

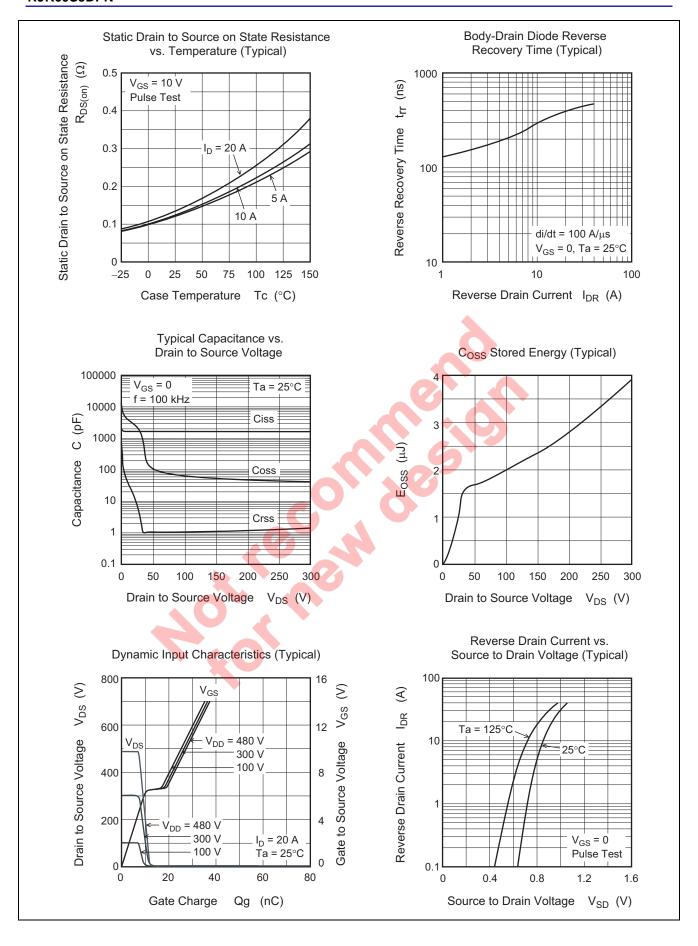
 $(Ta = 25^{\circ}C)$

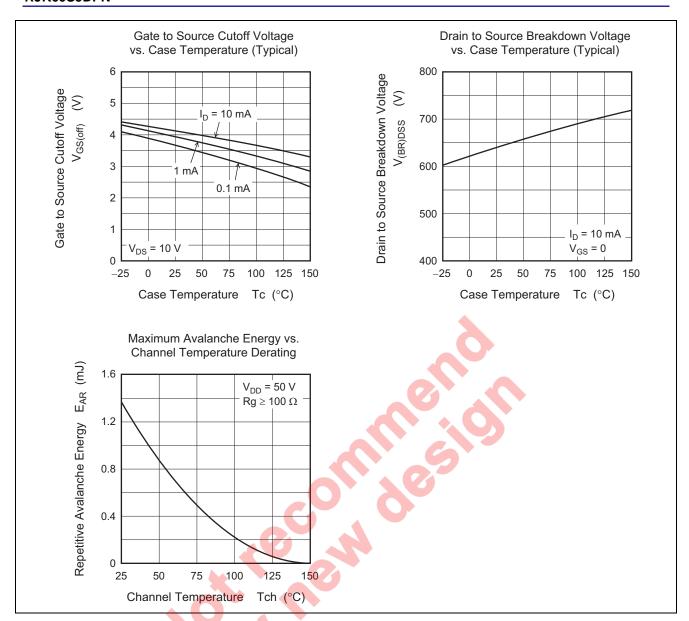
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	V _{(BR)DSS}	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Zero gate voltage drain current	I _{DSS}	_	_	1	mA	$V_{DS} = 600 \text{ V}, V_{GS} = 0$	
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = +30V, -20 V, V_{DS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	3	_	5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Static drain to source on state	R _{DS(on)}	_	0.150	0.178	Ω	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{Note5}$	
resistance	R _{DS(on}		0.375	_	Ω	Ta = 150°C $I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{Note5}$	
Gate resistance	Rg	_	2.5	_	Ω	f = 1 MHz V _{DS} = 25 V, V _{GS} = 0	
Input capacitance	Ciss		1600	_	pF	V _{DS} = 25 V	
Output capacitance	Coss		2160	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	8.2	_	pF	f = 100kHz	
Turn-on delay time	t _{d(on)}	_	23	_	ns	I _D = 10 A	
Rise time	t _r	_	25	_	ns	V _{GS} = 10 V	
Turn-off delay time	t _{d(off)}	_	49	(ns	$R_L = 30 \Omega$	
Fall time	t _f	_	23		ns	$Rg = 10 \Omega^{Note5}$	
Total gate charge	Qg	_	27		nC	V _{DD} = 480 V	
Gate to source charge	Qgs	_	10.5		nC	V _{GS} = 10 V I _D = 20 A ^{Note4}	
Gate to drain charge	Qgd	_	8.5	—	nC		
Body-drain diode forward voltage	V_{DF}	_	0.96	1.60	V	$I_F = 20 \text{ A}, V_{GS} = 0^{\text{Note5}}$	
Body-drain diode reverse recovery time	t _{rr}	_	400	5	ns	I _F = 20 A	
Body-drain diode reverse recovery current	I _{rr}		25	3	Α	$V_{GS} = 0$ $di_F/dt = 100 \text{ A/}\mu\text{s}^{\text{Note5}}$	
Body-drain diode reverse recovery charge	Q _{rr}		5.6	_	μС		
Notes: 5. Pulse test							

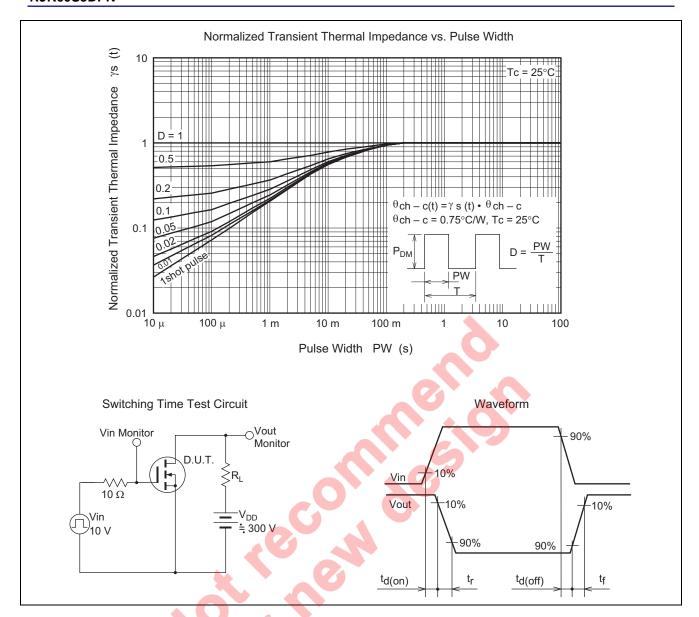
Notes: 5. Pulse test

Main Characteristics

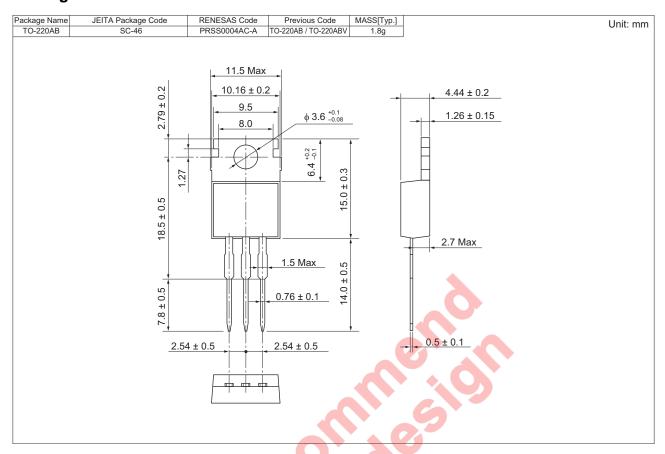








Package Dimension



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK60S5DPN-00#T2	50 pcs	Tube

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