

# RJK1051DPB

Silicon N Channel Power MOS FET Power Switching

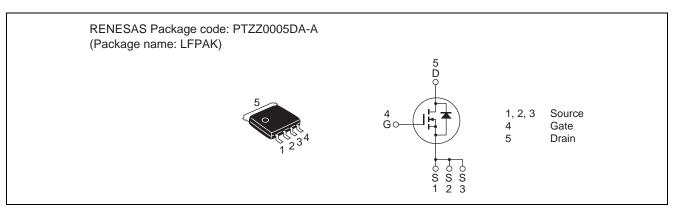
#### Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting

#### R07DS0082EJ0102 (Previous: REJ03G1768-0101) Rev.1.02 Jul 30, 2010

- Low on-resistance  $R_{DS(on)} = 30 \text{ m}\Omega \text{ typ.} (\text{at } V_{GS} = 10 \text{ V})$
- Pb-free
- Halogen-free

### Outline



## Application

• Switching Mode Power Supply

## **Absolute Maximum Ratings**

			$(1a = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	100	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	15	A
Drain peak current	I <sub>D(pulse)</sub> Note1	60	A
Body-drain diode reverse drain current	I <sub>DR</sub>	15	A
Avalanche current	I <sub>AP</sub> Note 2	7.5	A
Avalanche energy	E <sub>AR</sub> Note 2	5.6	mJ
Channel dissipation	Pch Note3	45	W
Channel to Case Thermal Resistance	θch-C	2.78	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

2. Value at Tch = 25°C, Rg  $\ge$  50  $\Omega$ 

3. Tc = 25°C

This product is for the low voltage drive ( $\leq 10$ V).

If the driving voltage is over 10 V under normal conditions, please use the product for high gate to source cutoff voltage  $(V_{GS(off)})$  which characteristics has been improved.



 $(T_0 - 25^{\circ}C)$ 

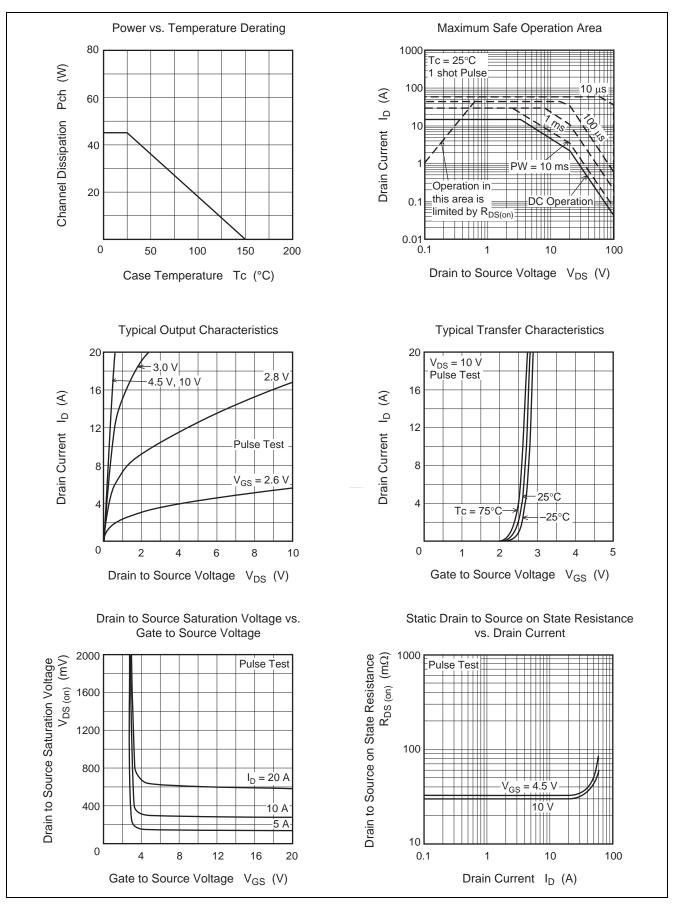
## **Electrical Characteristics**

Item	Symbol	Min	Тур	Max	Unit	(Ta = 25°C
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	100			V	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate to source leak current	I <sub>GSS</sub>			±0.1	μA	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	10	μA	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2		2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	—	30	39	mΩ	$I_D = 7.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	33	46	mΩ	$I_D = 7.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	_	30		S	$I_D = 7.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	2060		pF	$V_{DS} = 10 \text{ V},  V_{GS} = 0 \text{ V},$ f = 1 MHz
Output capacitance	Coss	_	180	_	pF	
Reverse transfer capacitance	Crss	—	70	_	pF	
Gate Resistance	Rg	_	0.7	_	Ω	
Total gate charge	Qg	_	15		nC	$V_{DD} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$ $I_D = 15 \text{ A}$
Gate to source charge	Qgs	_	6.6		nC	
Gate to drain charge	Qgd	—	4.6	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	8.4		ns	$\label{eq:VGS} \begin{array}{l} V_{GS} = 10 \; V, \; I_{D} = 17.5 \; A, \\ V_{DD} \cong 30 \; V, \; R_{L} = 4 \; \Omega, \\ Rg = 4.7 \; \Omega \end{array}$
Rise time	tr	_	4.0	_	ns	
Turn-off delay time	t <sub>d(off)</sub>	—	42	_	ns	
Fall time	t <sub>f</sub>	_	4.5		ns	
Body–drain diode forward voltage	$V_{DF}$	—	0.83	1.1	V	$I_F = 15 \text{ A}, V_{GS} = 0 \text{ V}^{Note4}$
Body–drain diode reverse recovery time	t <sub>rr</sub>	_	40	_	ns	I <sub>F</sub> = 15 A, V <sub>GS</sub> = 0 V di <sub>F</sub> / dt = 100 A/ μs

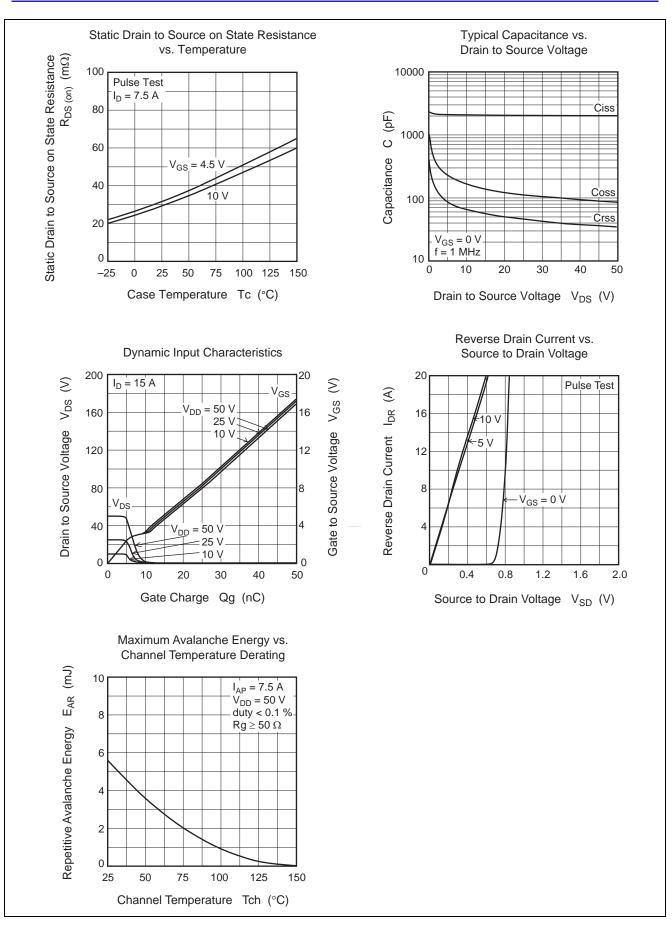
Notes: 4. Pulse test

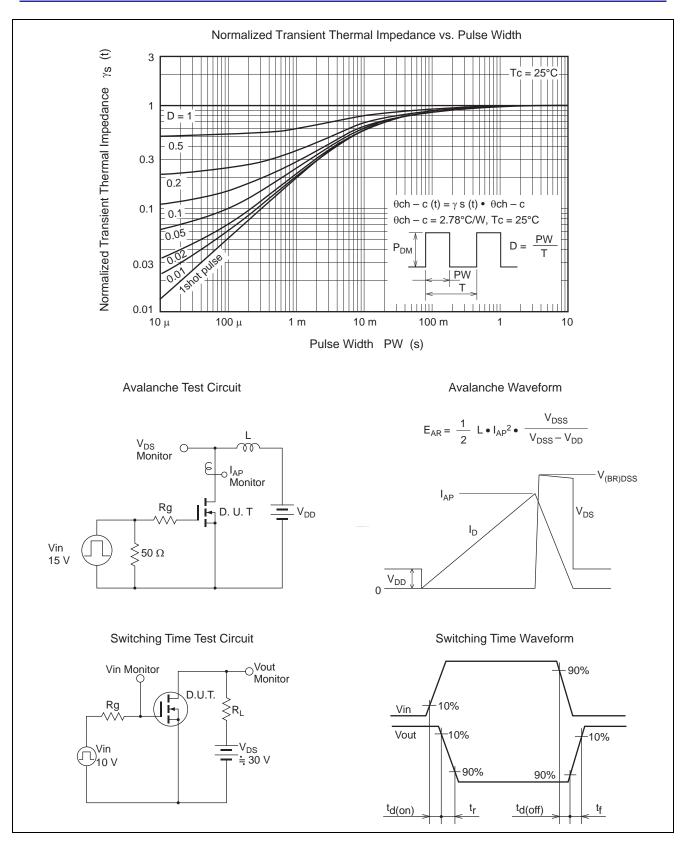


#### **Main Characteristics**



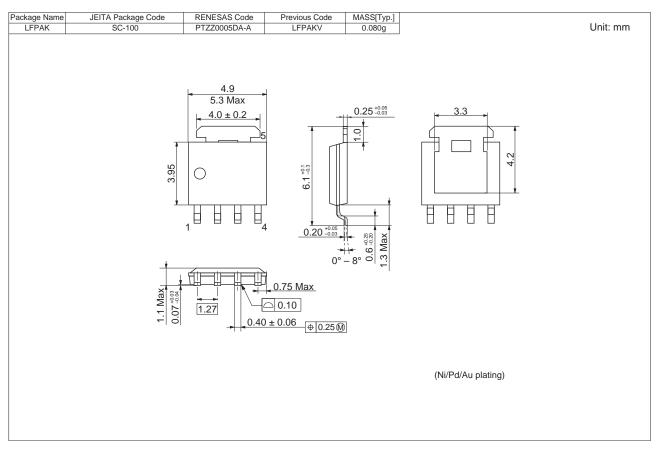








## **Package Dimensions**



### **Ordering Information**

Part No.	Quantity	Shipping Container
RJK1051DPB-00-J5	2500 pcs	Taping



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