

RJK03E1DNS

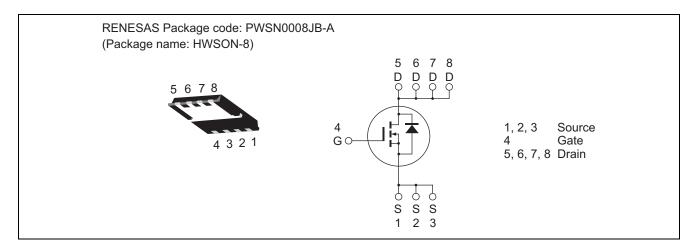
Silicon N Channel Power MOS FET Power Switching

REJ03G1903-0200 Rev.2.00 Apr 06, 2010

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 5.3 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V)}$
- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	25	A
Drain peak current	I _{D(pulse)} Note1	100	A
Body-drain diode reverse drain current	I _{DR}	25	А
Avalanche current	I _{AP} Note 2	12	A
Avalanche energy	E _{AR} Note 2	14.4	mJ
Channel dissipation	Pch Note3	15	W
Channel to case thermal impedance	θch-c Note3	8.33	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. Tc = 25°C



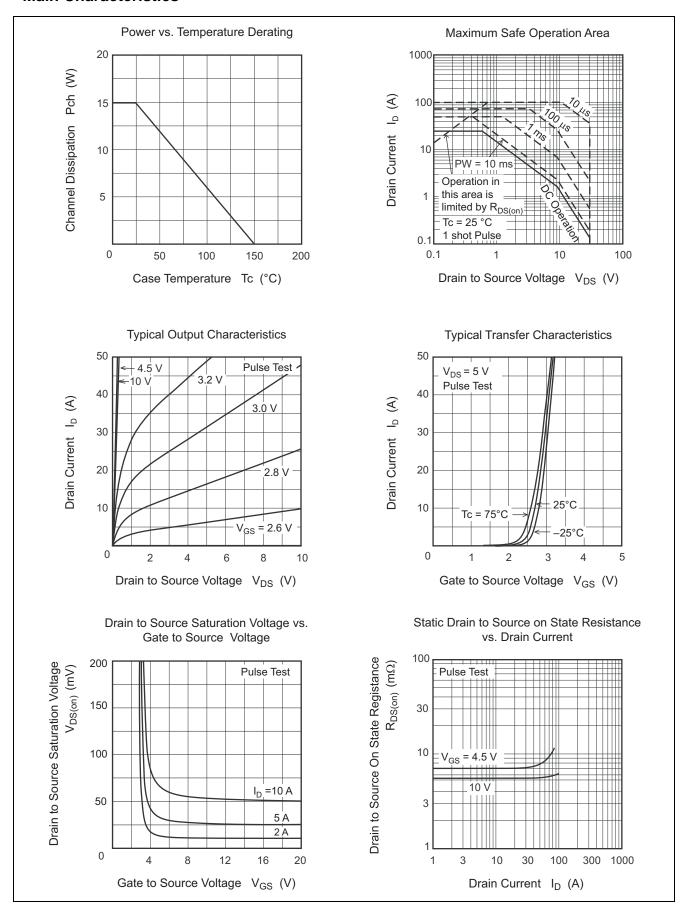
Electrical Characteristics

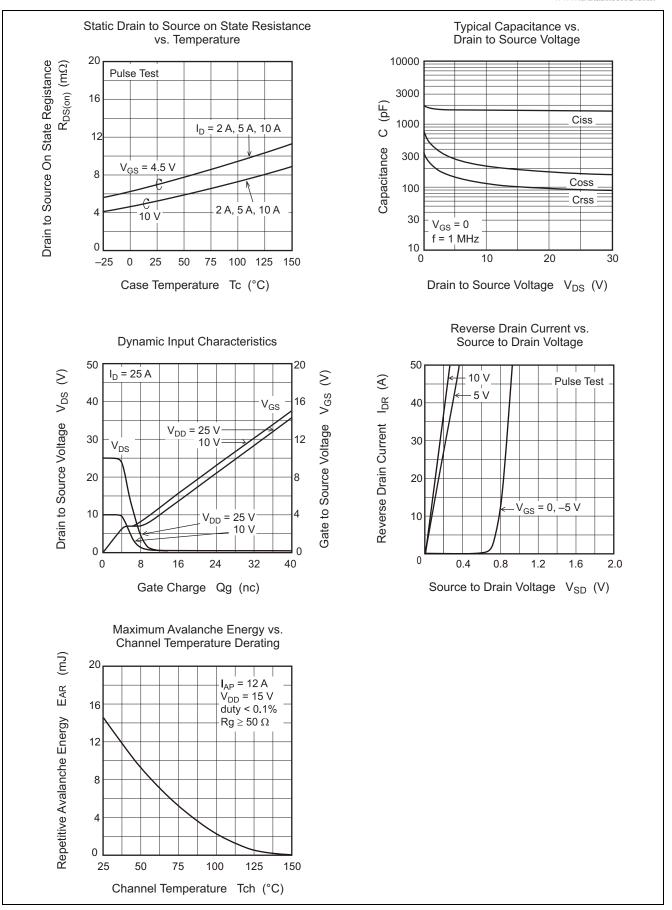
 $(Ta = 25^{\circ}C)$

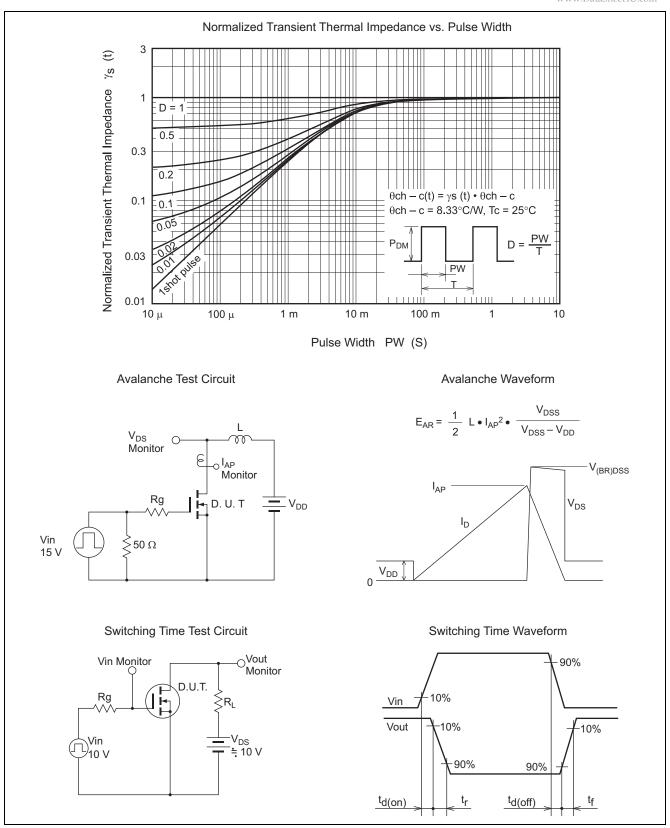
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	5.3	6.9	mΩ	$I_D = 12.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	7.0	9.8	mΩ	$I_D = 12.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}		48	_	S	$I_D = 12.5 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss		1640	2300	pF	V _{DS} = 10 V
Output capacitance	Coss		220	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		113	_	pF	f = 1 MHz
Gate Resistance	Rg		1.0	2.2	Ω	
Total gate charge	Qg	_	10.7	_	nC	$V_{DD} = 10 \text{ V}$
Gate to source charge	Qgs	_	4.9	_	nC	$V_{GS} = 4.5 \text{ V}$
Gate to drain charge	Qgd	_	2.5	_	nC	I _D = 25 A
Turn-on delay time	t _{d(on)}		9.4	_	ns	$V_{GS} = 10 \text{ V}, I_D = 12.5 \text{ A}$
Rise time	t _r		4.5	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t _{d(off)}		36	_	ns	$R_L = 0.8 \Omega$
Fall time	t _f		5.1	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}		0.84	1.10	V	$I_F = 25 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	15	_	ns	$I_F = 25 \text{ A}, V_{GS} = 0$ $di_F / dt = 100 \text{ A} / \mu \text{s}$

Notes: 4. Pulse test

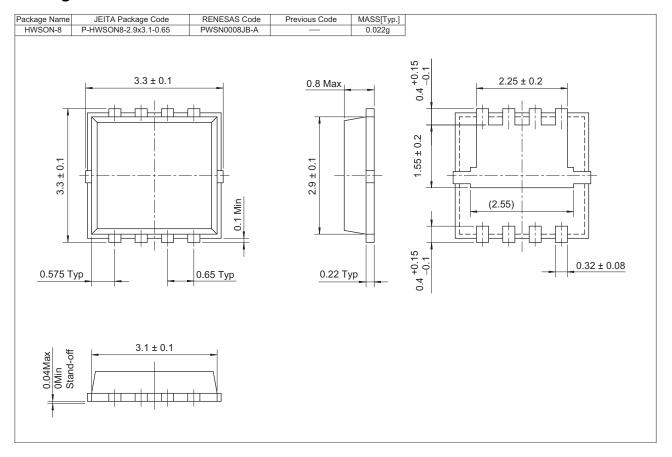
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK03E1DNS-00-J5	5000 pcs	Taping

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