

RJK03C0DPA

Silicon N Channel Power MOS FET Power Switching

REJ03G1822-0210 Rev.2.10 May 12, 2010

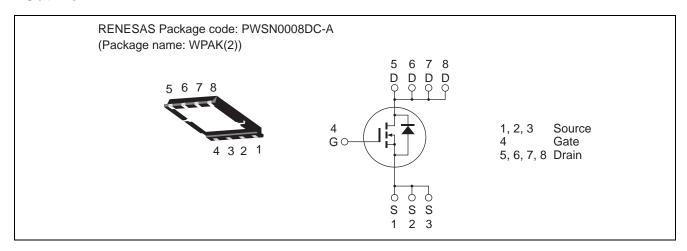
Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)} = 1.5 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V})$

- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	70	A
Drain peak current	I _{D(pulse)} Note1	280	A
Body-drain diode reverse drain current	I _{DR}	70	A
Avalanche current	I _{AP} Note 2	35	A
Avalanche energy	E _{AR} Note 2	122	mJ
Channel dissipation	Pch Note3	65	W
Channel to case thermal impedance	θch-c Note3	1.93	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. Tc = 25°C

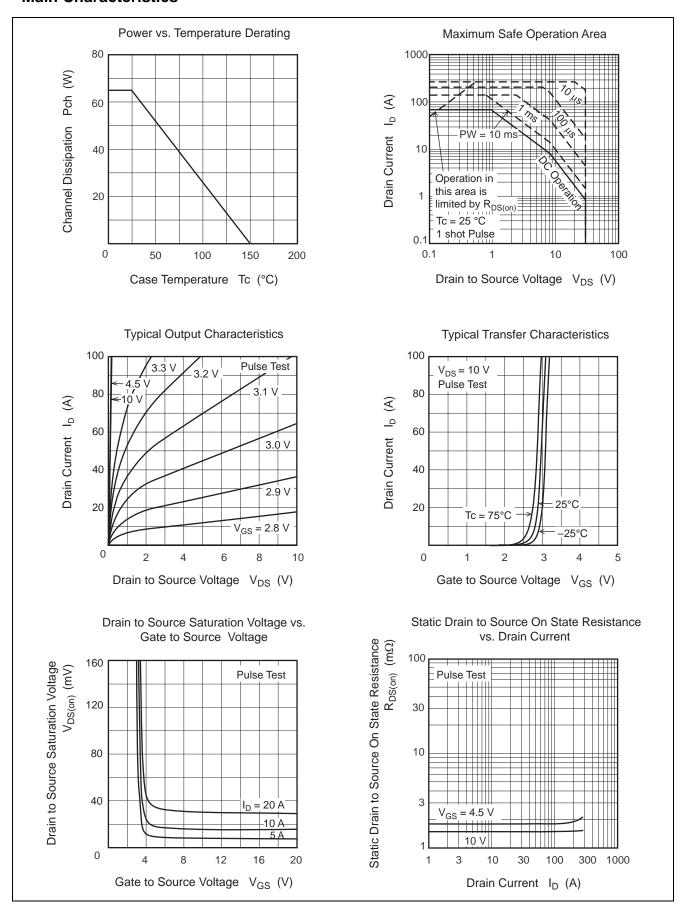
Electrical Characteristics

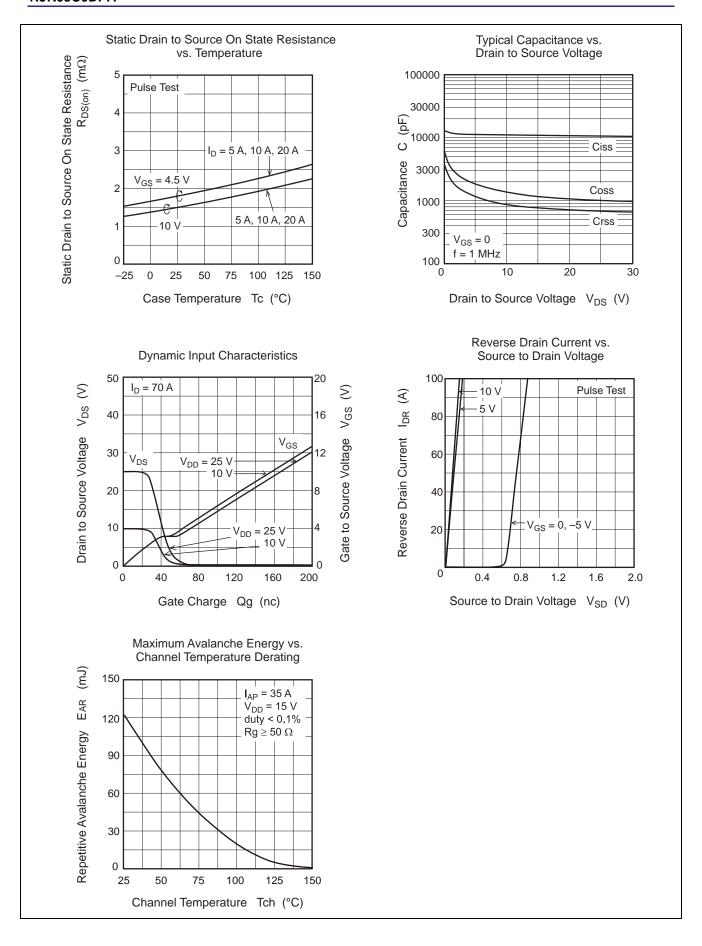
 $(Ta = 25^{\circ}C)$

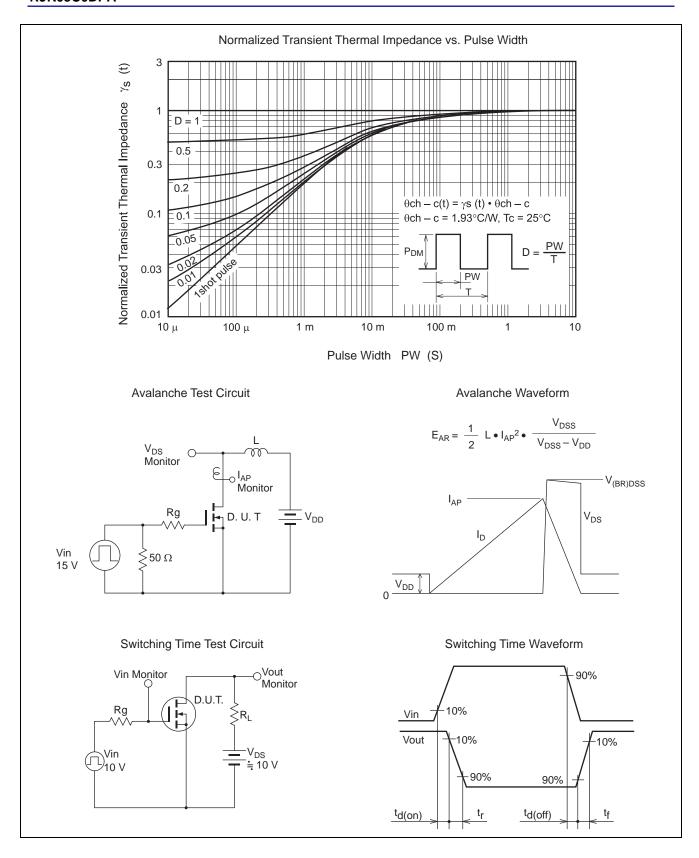
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 0.5	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	1.5	2.0	mΩ	$I_D = 35 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	1.8	2.5	mΩ	$I_D = 35 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	210	_	S	$I_D = 35 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	11000	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	1440	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	870	_	pF	
Gate Resistance	Rg	_	0.75	_	Ω	
Total gate charge	Qg	_	66	_	nC	$V_{DD} = 10 \text{ V}$
Gate to source charge	Qgs	_	42	_	nC	V _{GS} = 4.5 V
Gate to drain charge	Qgd	_	13.7	_	nC	I _D = 70 A
Turn-on delay time	t _{d(on)}	_	28	_	ns	$V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$
Rise time	t _r	_	14.2	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t _{d(off)}	_	102	_	ns	$R_L = 0.29 \Omega$
Fall time	t _f	_	40	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}	_	0.80	1.04	V	$I_F = 70 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery	t _{rr}	_	53	_	ns	$I_F = 70 \text{ A}, V_{GS} = 0$
time						$di_F/dt = 100 A/ \mu s$

Notes: 4. Pulse test

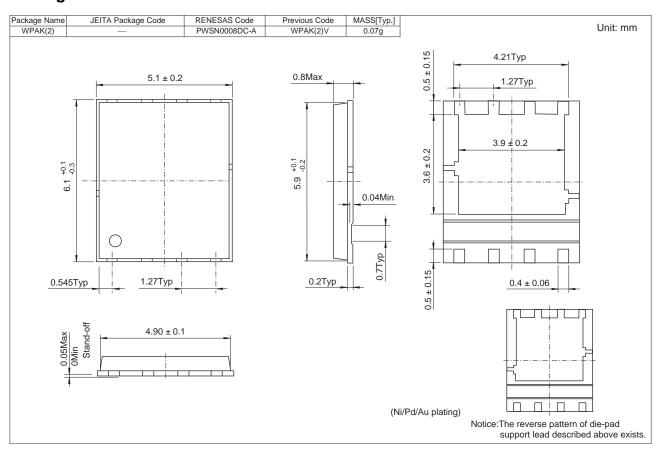
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK03C0DPA-00-J53	3000 pcs	Taping

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