

## RJK0304DPB

# Silicon N Channel Power MOS FET Power Switching

REJ03G1352-0600 Rev.6.00 Apr 19, 2006

#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS(on)} = 4.0 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$

#### **Outline**

RENESAS Package code: PTZZ0005DA-A (Package name: LFPAK)

5
D
1, 2, 3 Source
4 Gate
5 Drain

#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	$V_{GSS}$	+16/-12	V
Drain current	I <sub>D</sub>	35	A
Drain peak current	I <sub>D(pulse)</sub> Note1	140	A
Body-drain diode reverse drain current	I <sub>DR</sub>	35	A
Avalanche current	I <sub>AP</sub> Note 2	14	A
Avalanche energy	E <sub>AR</sub> Note 2	19	mJ
Channel dissipation	Pch Note3	50	W
Channel to Case Thermal Resistance	θch-C	2.5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

3.  $Tc = 25^{\circ}C$ 

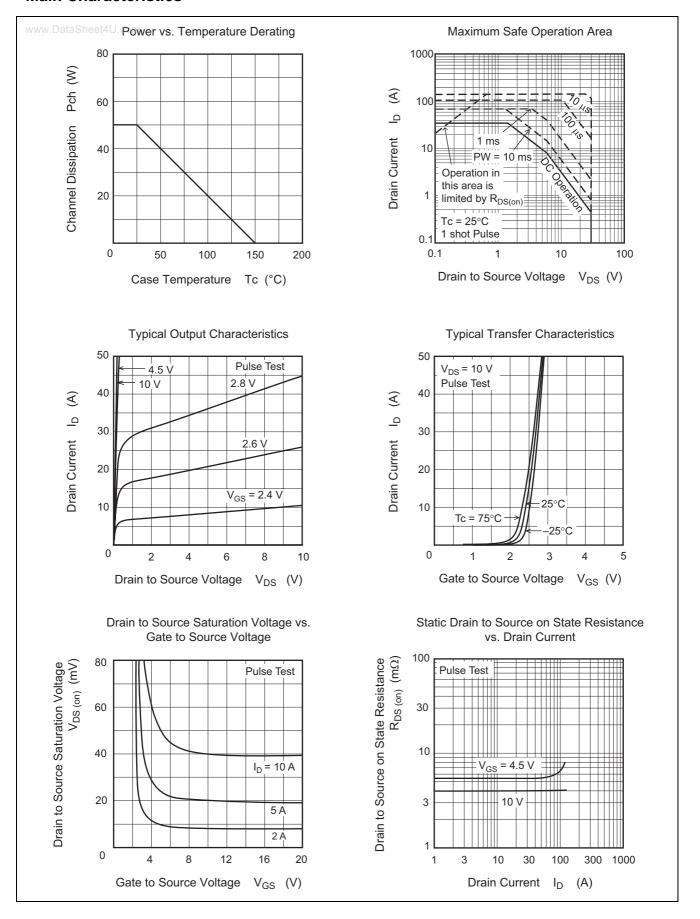
### **Electrical Characteristics**

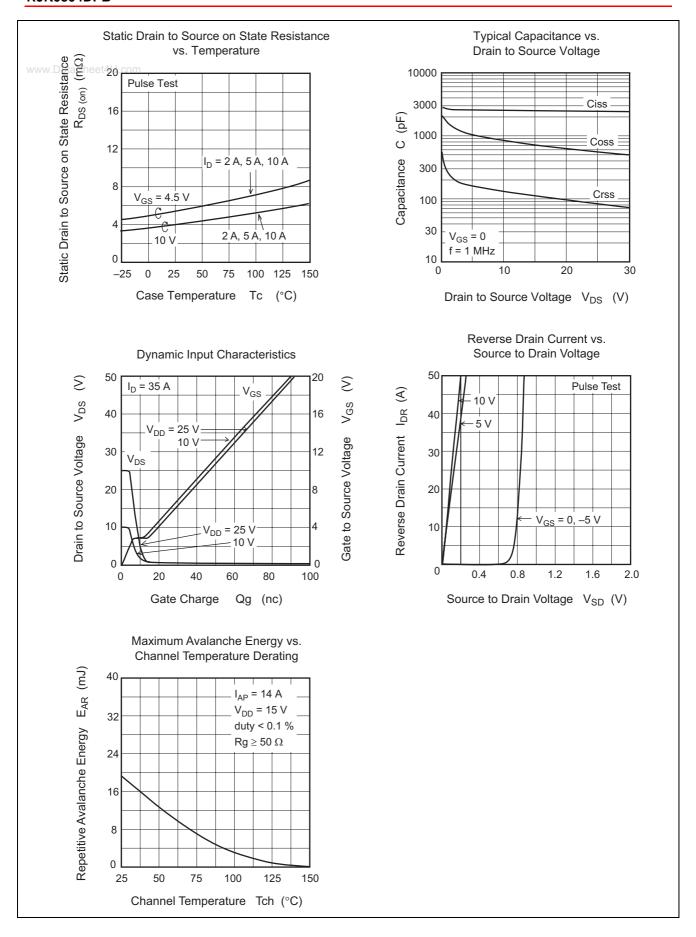
 $(Ta = 25^{\circ}C)$ 

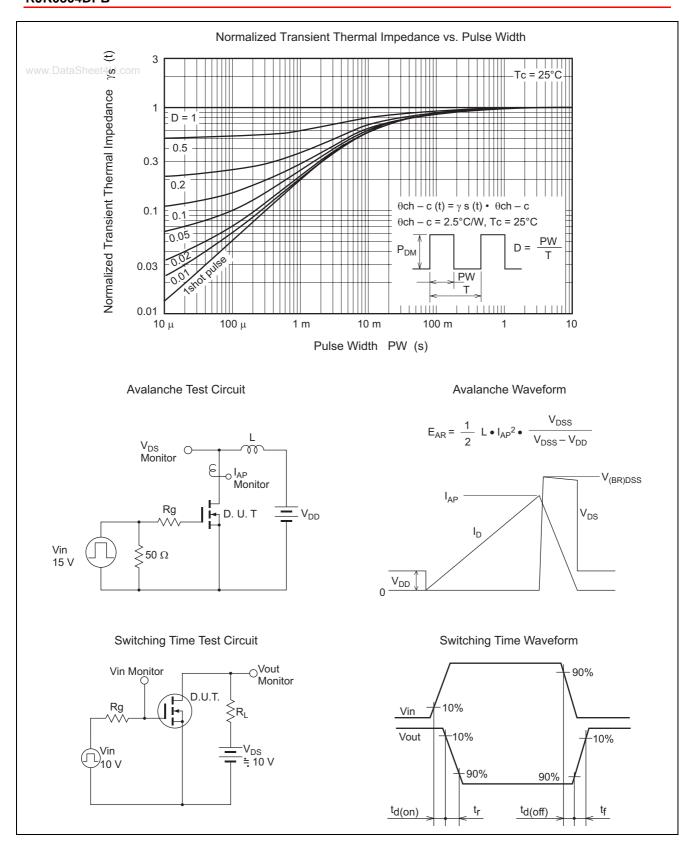
www.DataSheet4Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	± 0.1	μΑ	$V_{GS} = +16/-12 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	4.0	4.8	mΩ	$I_D = 17.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	5.5	7.2	mΩ	I <sub>D</sub> = 17.5 A, V <sub>GS</sub> = 4.5 V Note4
Forward transfer admittance	y <sub>fs</sub>	_	60	_	S	$I_D = 17.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	2500	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	850	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	130	_	pF	
Gate Resistance	Rg	_	0.5	_	Ω	
Total gate charge	Qg	_	17	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	6.7	_	nC	I <sub>D</sub> = 35 A
Gate to drain charge	Qgd	_	3.7	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	8.5	_	ns	$V_{GS} = 10 \text{ V}, I_D = 17.5 \text{ A},$
Rise time	t <sub>r</sub>	_	3.2	_	ns	$V_{DD} \cong 10 \text{ V,R}_L = 0.57 \Omega,$
Turn-off delay time	t <sub>d(off)</sub>	_	41	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	4.0	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.84	1.10	V	$IF = 35 A, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	35	_	ns	IF = 35 A, V <sub>GS</sub> = 0 di <sub>F</sub> / dt = 100 A/ μs

Notes: 4. Pulse test

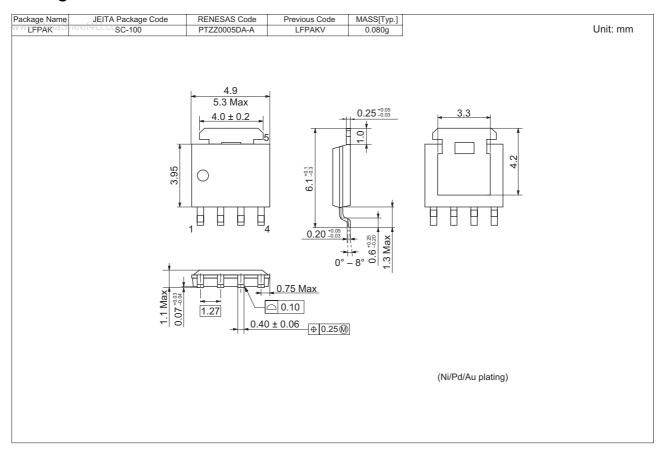
#### **Main Characteristics**







#### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
RJK0304DPB-00-J0	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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