

# RJE0605JPD

Silicon P Channel MOS FET Series Power Switching REJ03G1803-0100 Rev.1.00 Apr 01, 2010

Datasheet

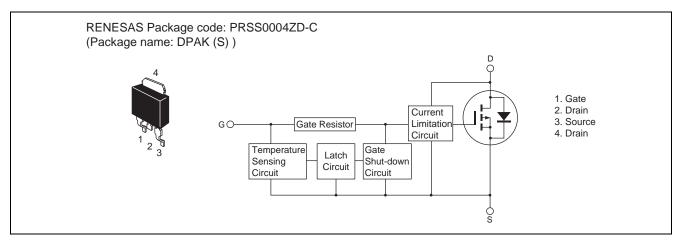
# Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

## Features

- Logic level operation (-6 V Gate drive).
- Built-in the over temperature shut-down circuit.
- High endurance capability against to the short circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- Low on-resistance  $R_{DS(on)}$ : 58 m $\Omega$  Typ, 75 m $\Omega$  Max ( $V_{GS} = -10$  V)

### Outline



# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	-16	V
Gate to source voltage	V <sub>GSS</sub>	2.5	V
Drain current	I <sub>D</sub> <sup>Note3</sup>	-10	А
Body-drain diode reverse drain current	I <sub>DR</sub>	-10	А
Avalanche current	I <sub>AP</sub> <sup>Note 2</sup>	-7	А
Avalanche energy	E <sub>AR</sub> <sup>Note 2</sup>	210	mJ
Channel dissipation	Pch Note 1	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 +150	°C

Notes: 1. Value at Tc = 25°C

2. Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

3. It provides by the current limitation lower bound value.



# **Typical Operation Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	VIH	-3.5	—	—	V	
	VIL	_	—	-1.2	V	
Input current	I <sub>IH1</sub>	_	_	-100	μΑ	$Vi = -8 V, V_{DS} = 0$
(Gate non shut down)	I <sub>IH2</sub>		—	-50	μA	$Vi = -3.5 V, V_{DS} = 0$
	IIL	_	_	-1	μΑ	$Vi = -1.2 V, V_{DS} = 0$
Input current	I <sub>IH(sd)1</sub>	_	-0.8	_	mA	$Vi = -8 V, V_{DS} = 0$
(Gate shut down)	I <sub>IH(sd)2</sub>	_	-0.35	_	mA	$Vi = -3.5 V, V_{DS} = 0$
Shut down temperature	Tsd	_	175	—	°C	Channel temperature
						(dv/dt $V_{GS} \ge 500 \text{ V/ms}$ )
Gate operation voltage	Vop	-3.5	_	-12	V	
Drain current	I <sub>D limt</sub>	-10	_	—	А	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{Note 4}$
(Current limitation value)						

Notes; 4. Pulse test

### **Electrical Characteristics**

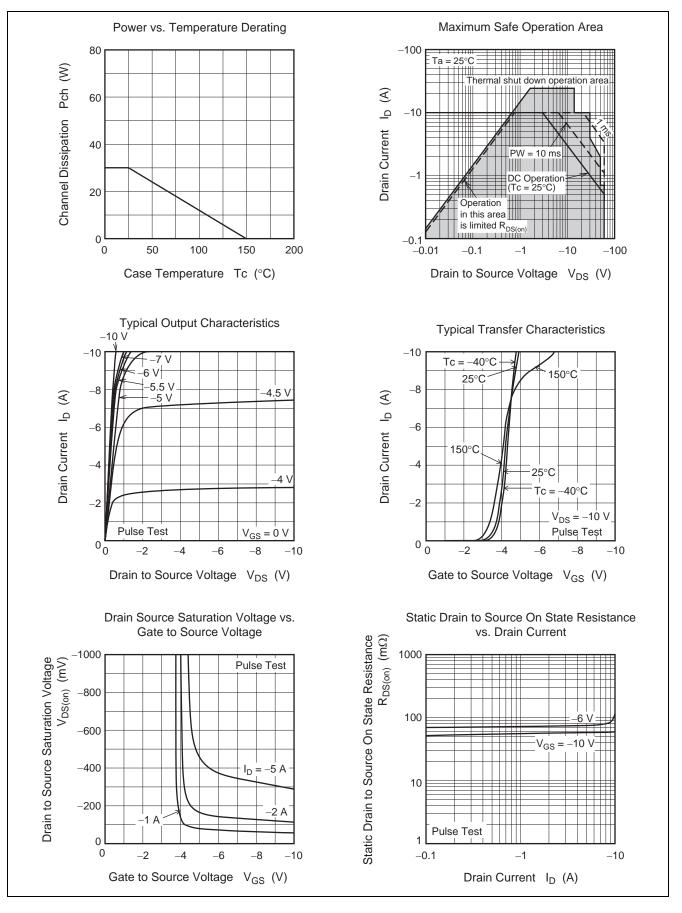
						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I <sub>D1</sub>	_	_	-4	А	$V_{GS} = -3.5 \text{ V}, V_{DS} = -10 \text{ V}$
	I <sub>D2</sub>	_	_	-10	mA	$V_{GS} = -1.2 \text{ V}, V_{DS} = -10 \text{ V}$
	I <sub>D3</sub>	-10	—	—	А	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{Note 5}$
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	-60	_	—	V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown	V <sub>(BR)GSS</sub>	-16	—	—	V	$I_G = -800 \ \mu A, \ V_{DS} = 0$
voltage	V <sub>(BR)GSS</sub>	2.5	—	—	V	$I_G = 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS1</sub>	—	—	-100	μA	$V_{GS} = -8 V, V_{DS} = 0$
	I <sub>GSS2</sub>	_	—	-50	μΑ	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
	I <sub>GSS3</sub>	—	—	-1	μA	$V_{GS} = -1.2 V, V_{DS} = 0$
	I <sub>GSS4</sub>	—	—	100	μA	$V_{GS} = 2.4 V, V_{DS} = 0$
Input current (shut down)	I <sub>GS(OP)1</sub>	—	-0.8	—	mA	$V_{GS} = -8 V, V_{DS} = 0$
	I <sub>GS(OP)2</sub>	—	-0.35	—	mA	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	—	-10	μΑ	$V_{DS} = -60 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	-2.2	—	-3.4	V	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$
Forward transfer admittance	y <sub>fs</sub>	4	8	—	S	$I_D = -5 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 5}}$
Static drain to source on state	R <sub>DS(on)</sub>	—	75	110	mΩ	$I_D = -5 \text{ A}, V_{GS} = -6 \text{ V}^{\text{Note 5}}$
resistance	R <sub>DS(on)</sub>	—	58	75	mΩ	$I_D = -5 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 5}}$
Output capacitance	Coss	—	355	—	pF	$V_{DS} = -10 V$ , $V_{GS} = 0$ , f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	—	4.5	—	μs	$V_{GS}$ = -10 V, $I_D$ = -5 A, $R_L$ = 6 $\Omega$
Rise time	tr	—	4.0	—	μs	
Turn-off delay time	t <sub>d(off)</sub>	—	1.8	—	μs	
Fall time	t <sub>f</sub>	—	1.3	—	μs	
Body-drain diode forward	V <sub>DF</sub>	_	0.87	_	V	$I_F = -10 \text{ A}, V_{GS} = 0$
voltage						
Body-drain diode reverse	t <sub>rr</sub>	—	209	—	ns	$I_F = -10 \text{ A}, V_{GS} = 0$
recovery time						$di_F/dt = 50 A/\mu s$
Over load shut down operation time Note 6	t <sub>os1</sub>	—	2.3	—	ms	$V_{GS} = -6 V, V_{DD} = -16 V$

Notes: 5. Pulse test

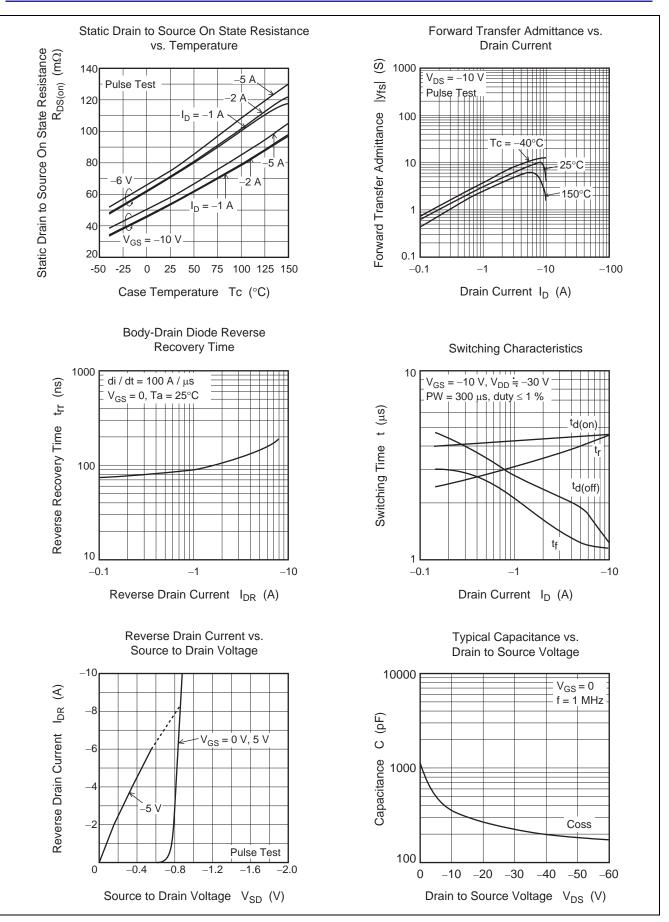
6. Including the junction temperature rise of the over loaded condition.



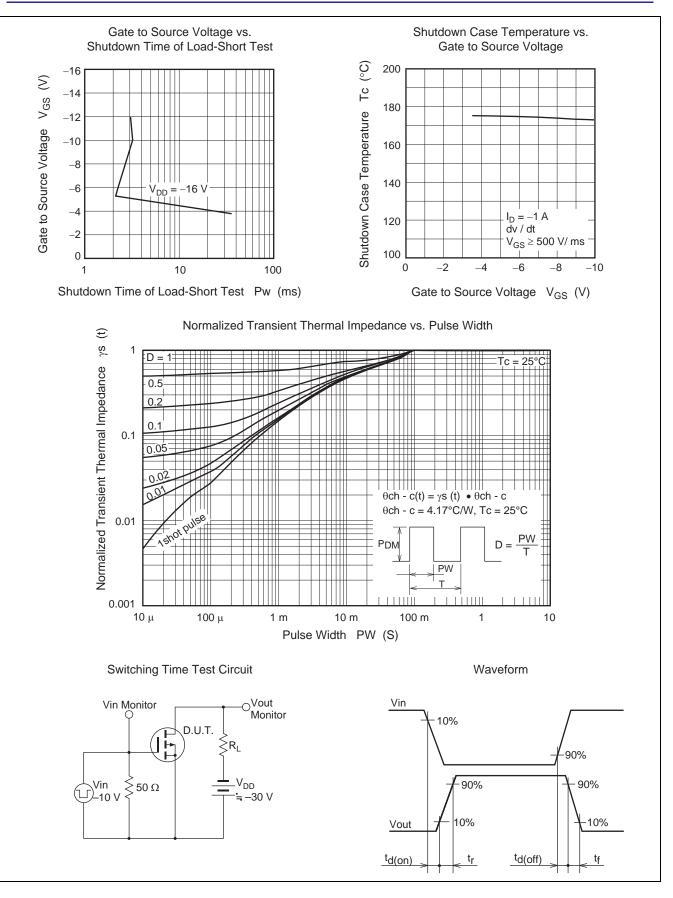
#### **Main Characteristics**





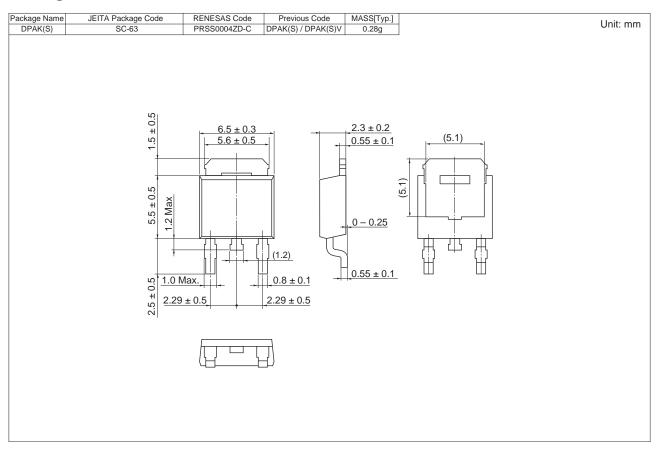








## **Package Dimensions**



# **Ordering Information**

Part No.	Quantity	Shipping Container
RJE0605JPD-00-J3	3000 pcs	Taping (Sinistrorse)



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