# Nch 60V 80A Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	7.7mΩ
I <sub>D</sub>	±80A
P <sub>D</sub>	96W

## Features

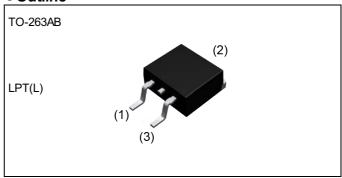
- 1) Low on resistance
- 2) High power small mold package
- 3) Pb-free lead plating; RoHS compliant
- 4) 100% Rg and UIS tested
- 5) Halogen free

# Application

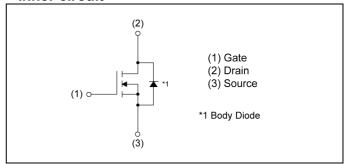
Switching

Power tool

## Outline



# Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	24
	Quantity (pcs)	1000
	Taping code	TLL
	Marking	RJ1L08CGN

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	60	V
Continuous drain current	V <sub>GS</sub> = 10V	I <sub>D</sub> *1	±80	Α
Pulsed drain current	l <sub>DP</sub> *2	±160	Α	
Gate - Source voltage		$V_{GSS}$	±20	V
Avalanche current, single pulse		I <sub>AS</sub> *3	37	Α
Avalanche energy, single pulse	E <sub>AS</sub> *3	52	mJ	
Power dissipation	P <sub>D</sub> *1	96	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage te	mperature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Parameter	Symbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	ı	1.3	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Symbol Conditions -			Values			
Parameter	Symbol	Symbol Conditions		Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	60	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	10	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 50\mu A$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta  V_{GS(th)}}{\Delta  T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-5.6	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 80A	-	5.3	7.7	mO.	
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 40A	-	7.4	10.7	mΩ	
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	1.8	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 40A	24	-	-	S	

<sup>\*1</sup> T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw≦10µs , Duty cycle≦1%

<sup>\*3</sup> L  $\simeq$  0.05mH, V<sub>DD</sub> = 30V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>i</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Symbol Conditions		Values			Unit
Parameter			Min.	Тур.	Max.	Urill
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2600	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V	-	510	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	130	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 30V, V_{GS} = 10V$	1	17	1	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 40A	1	32	1	no
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 0.75\Omega$		71	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	34	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

	\ α	,					
Parameter	Symbol	Conditions		Values			Unit
Parameter	Symbol			Min.	Тур.	Max.	UIIIL
Total gate above	O *4		V <sub>GS</sub> = 10V	-	55	-	
Total gate charge	$Q_g^{*4}$	$V_{DD} \approx 30V$ $I_D = 40A$		-	27	-	C
Gate - Source charge	Q <sub>gs</sub> *4		V <sub>GS</sub> = 4.5V	-	11	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4			-	9.8	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol Conditions		Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	80	Α	
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	-	-	160	Α	
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_{S} = 80A$	-	-	1.2	V	
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 50A, V <sub>GS</sub> =0V	-	42	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/μs	-	53	-	nC	

Fig.1 Power Dissipation Derating Curve

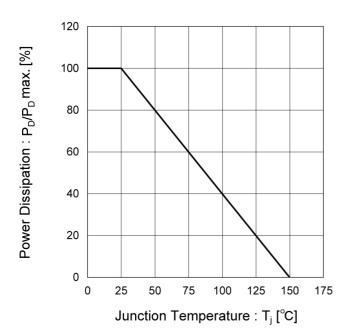
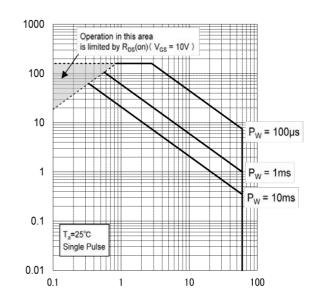


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

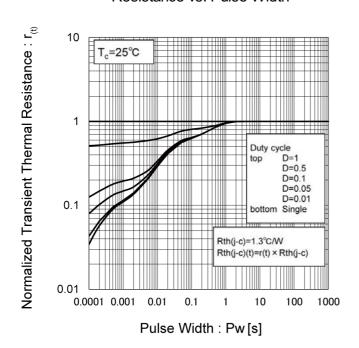


Fig.4 Single Pulse Maximum Power dissipation

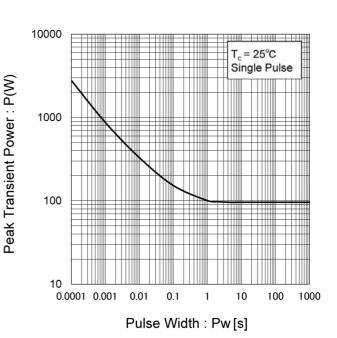
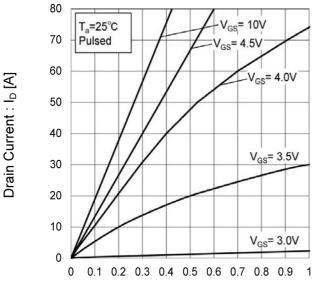
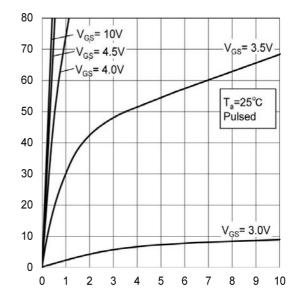


Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage :  $V_{DS}[V]$ 

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

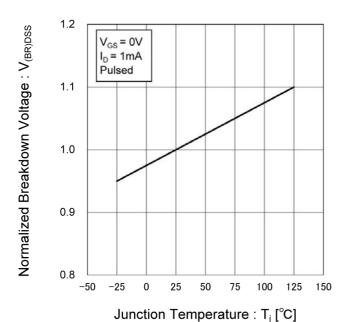


Fig.8 Typical Transfer Characteristics

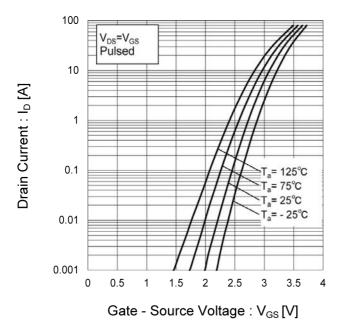
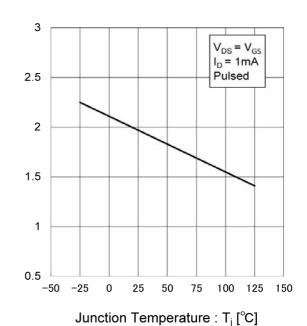
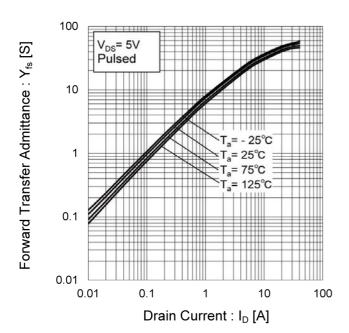


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: VGS(th) [V]

Fig.10 Forward Transfer Admittance vs.
Drain Current



6/11

Fig.11 Drain Current Derating Curve

120 Drain Current Dissipation : I<sub>D</sub>/I<sub>D</sub>max. [%] 100 80 60 40 20 0 50 75 150 0 25 100 125 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

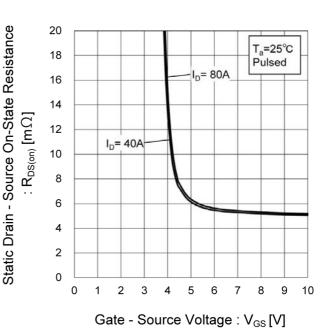


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

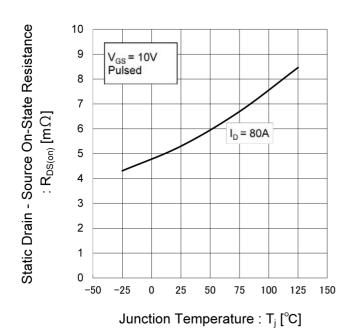


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

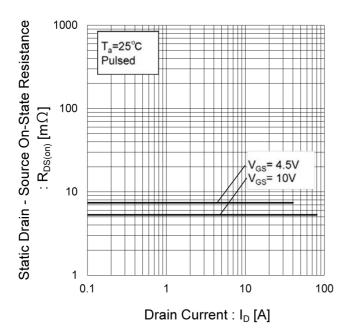


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

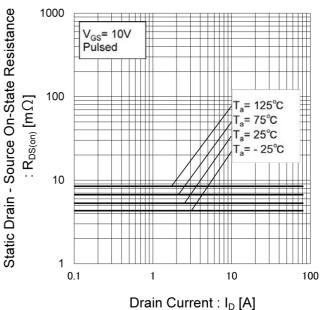
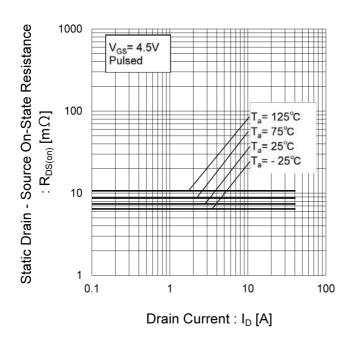


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



8/11

Fig.17 Typical Capacitance vs.

Drain - Source Voltage

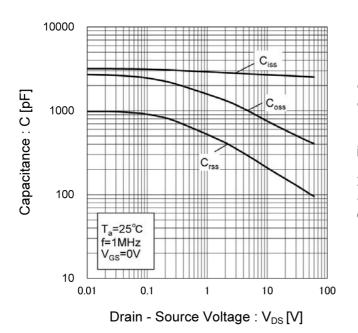


Fig.18 Switching Characteristics

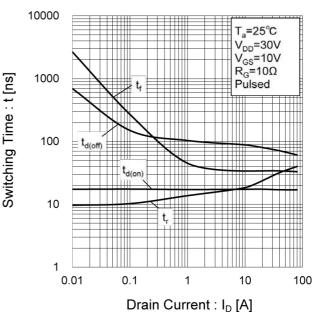


Fig.19 Dynamic Input Characteristics

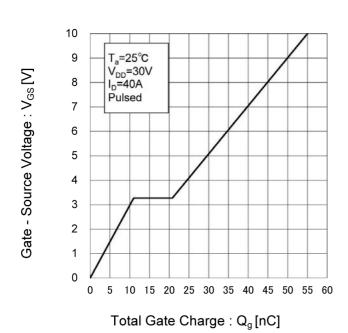
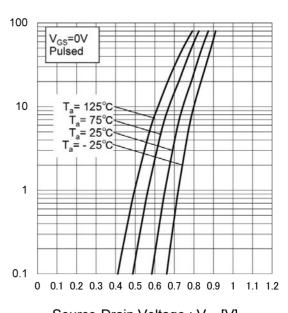


Fig.20 Source Current vs.
Source Drain Voltage



Source-Drain Voltage :  $V_{\text{SD}}[V]$ 

Source Current : Is [A]

# Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

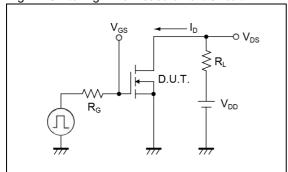


Fig.2-1 Gate Charge Measurement Circuit

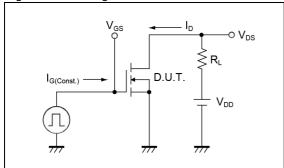


Fig.3-1 Avalanche Measurement Circuit

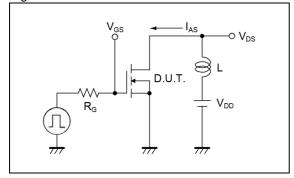


Fig.1-2 Switching Waveforms

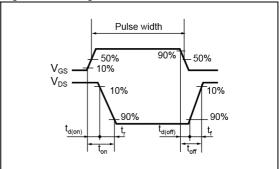


Fig.2-2 Gate Charge Waveform

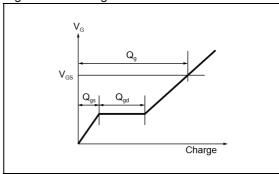
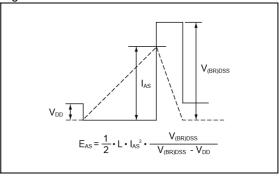
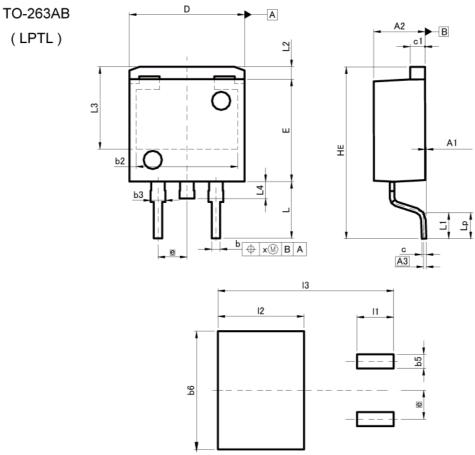


Fig.3-2 Avalanche Waveform



# Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.:	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.9	90	0.3	50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1,10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.1	00
HE	14.80	15.40	0.583	0.606
L	4.70	5.30	0.185	0.209
L1	2.10	2.70	0.083	0.106
L2	1,	10	0.0	43
L3	7.:	25	0.2	85
L4	1.5	50	0.0	159
Lp	2.60	2.00	0.102	0.079
х		0.25	-	0.010

DIM	MILIMETERS		INC	HES
DIM L	MIN	MAX	MIN	MAX
b5		1.23	æ	0.049
b6		10.40	<b>—</b>	0.409
- 11	-	3.20	-	0.126
12	2 <del>7</del> 2	7.55	2.72	0.297
13	5 <del>=</del>	15.40	-	0.606

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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