# RGW00TK65DGVC11

## 650V 50A Field Stop Trench IGBT

Datasheet

V <sub>CES</sub>	650V
I <sub>C (100°C)</sub>	26A
V <sub>CE(sat) (Typ.)</sub>	1.5V@I <sub>C</sub> =50A
P <sub>D</sub>	89W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

### Applications

**PFC** 

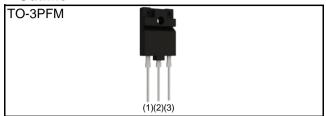
**UPS** 

Welding

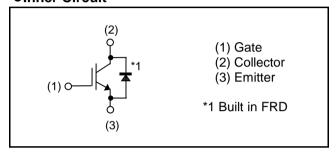
Solar Inverter

ΙH

#### Outline



#### ●Inner Circuit



Packaging Specifications

	gg opcomeanome	
	Packaging	Tube
	Reel Size (mm)	-
Tuno	Tape Width (mm)	-
Type	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGW00TK65D

## ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		V <sub>GES</sub>	±30	V
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	45	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	26	А
Pulsed Collector Current		I <sub>CP</sub> *1	200	А
Diode Forward Current	$T_C = 25^{\circ}C$	l <sub>F</sub>	34	А
	T <sub>C</sub> = 100°C	l <sub>F</sub>	19	А
Diode Pulsed Forward Current		I <sub>FP</sub> *1	200	А
Power Dissipation	$T_C = 25^{\circ}C$	P <sub>D</sub>	89	W
	T <sub>C</sub> = 100°C	P <sub>D</sub>	44	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>imax</sub>.

#### ●Thermal Resistance

Parameter	Symbol	Values			Unit
raiametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	1.67	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	2.29	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_C = 10 \mu A, V_{GE} = 0 V$	650	1	ı	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	ı	1	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, \ V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 33.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_{C} = 50A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Darameter	Cumbal	Conditions -	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	UTIIL
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	4200	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$	-	104	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	79	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 400V	-	141	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 50A	-	30	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	52	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 50A, V_{CC} = 400V$	-	52	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_G = 10\Omega$	-	21	-	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	180	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	33	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> includes diode	-	1.18	-	I
Turn - off Switching Loss	E <sub>off</sub>	reverse recovery	-	0.96	-	mJ
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 50A, V_{CC} = 400V$	-	49	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	23	-	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C	-	201	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	72	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> includes diode	-	1.18	-	- m l
Turn - off Switching Loss	$E_{off}$	reverse recovery	-	1.18	-	mJ
		$I_C = 200A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-
		$R_G = 100\Omega, T_j = 175^{\circ}C$				

# ●FRD Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
Diode Forward Voltage	$V_{F}$	$I_F = 30A$ $T_j = 25$ °C $T_j = 175$ °C	-	1.45 1.55	1.9 -	V
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 30A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	95	-	ns
Diode Peak Reverse Recovery Current	l <sub>rr</sub>		-	8.1	-	А
Diode Reverse Recovery Charge	$Q_{rr}$		-	0.42	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	19.3	-	μJ
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 30A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	1	155	-	ns
Diode Peak Reverse Recovery Current	l <sub>rr</sub>		,	10.4	•	А
Diode Reverse Recovery Charge	$Q_{rr}$		-	0.95	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	62.5	-	μJ

Fig.1 Power Dissipation vs. Case Temperature

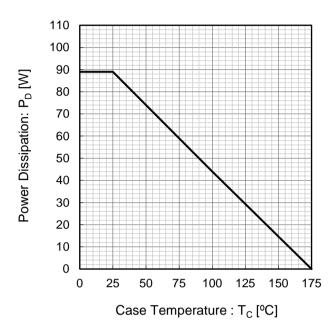


Fig.2 Collector Current vs. Case Temperature

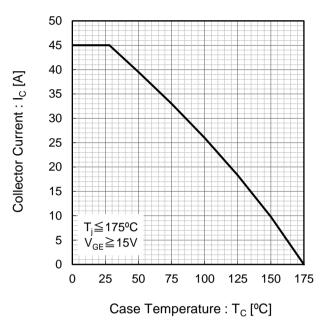


Fig.3 Forward Bias Safe Operating Area

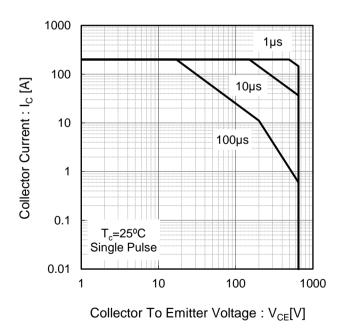


Fig.4 Reverse Bias Safe Operating Area

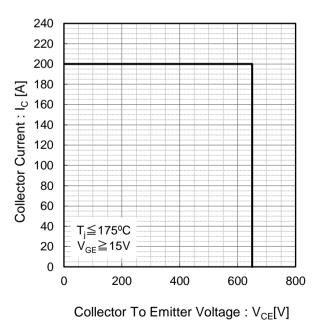


Fig.5 Typical Output Characteristics

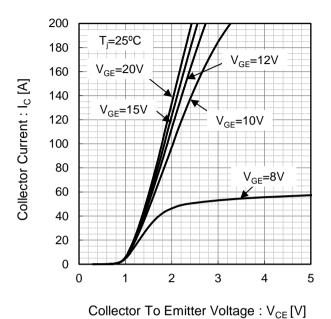
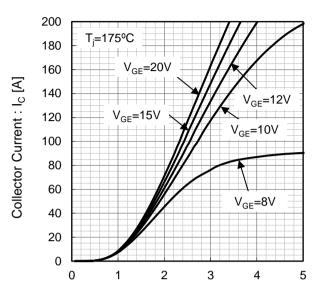


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V<sub>CE</sub>[V]

Fig.7 Typical Transfer Characteristics

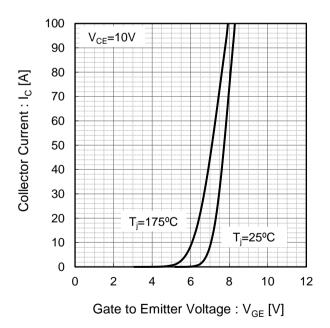


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

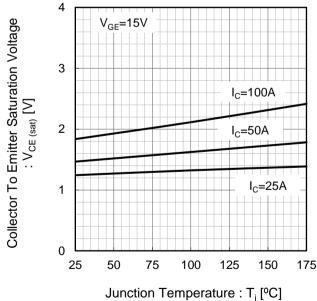


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

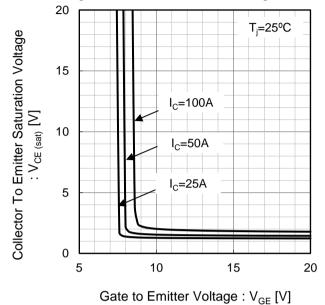
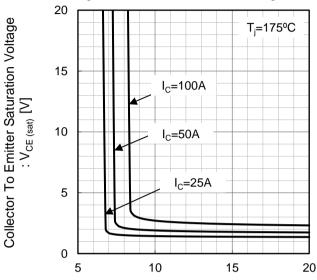


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate to Emitter Voltage : V<sub>GE</sub> [V]

Fig.11 Typical Switching Time vs. Collector Current

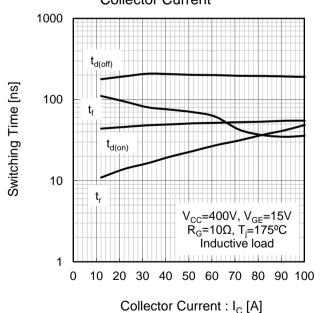
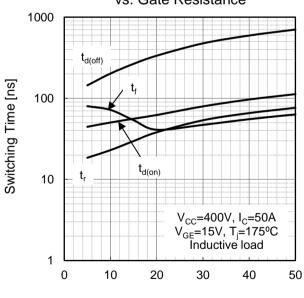


Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance :  $R_G [\Omega]$ 

Fig.13 Typical Switching Energy Losses vs. Collector Current

10  $E_{off}$   $V_{cc}=400V, V_{ge}=15V$   $R_{g}=10\Omega, T_{j}=175^{\circ}C$ Inductive load

0 10 20 30 40 50 60 70 80 90 100

Collector Current : I<sub>C</sub> [A]

Fig.14 Typical Switching Energy Losses vs.
Gate Resistance

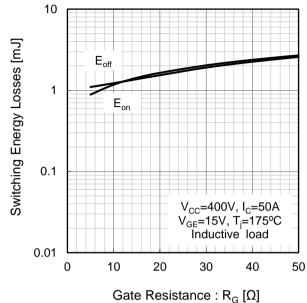


Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz  $V_{GE}=0V$ T<sub>i</sub>=25°C 0.1 0.01 1 10 100 Collector To Emitter Voltage :  $V_{CE}[V]$ 

0

1

#### **•**Electrical Characteristic Curves

Fig.17 Typical Diode Forward Current vs.
Forward Voltage

200
180
160
140
120
100
80
T<sub>j</sub>=25°C

40
20
0

2

Forward Voltage: V<sub>F</sub>[V]

3

4

5

Fig.18 Typical Diode Reverse Recovery Time
vs. Forward Current

400

EL

100

V<sub>CC</sub>=400V

di<sub>F</sub>/dt=200A/µs
Inductive load

0 10 20 30 40 50 60 70 80 90 100

Forward Current : I<sub>F</sub> [A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

20  $\overline{\{V\}}_{L}^{L}$ 115  $T_{j}=175^{\circ}\text{C}$   $T_{j}=25^{\circ}\text{C}$   $V_{CC}=400V$   $d_{i_{F}}/dt=200A/\mu s$   $d_{i_{F}}/dt=200A/\mu s$ 

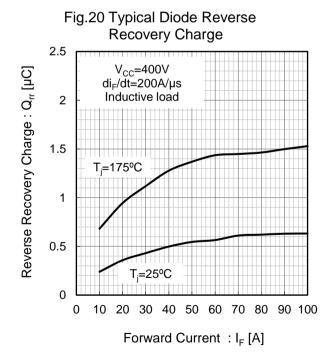
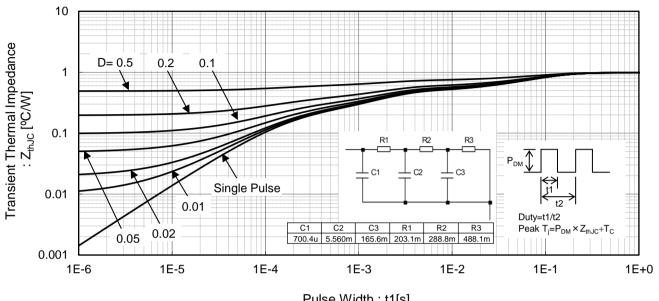
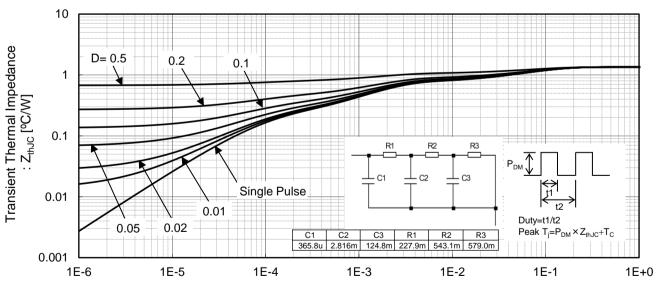


Fig.21 Typical IGBT Transient Thermal Impedance



Pulse Width: t1[s]

Fig.22 Typical Diode Transient Thermal Impedance



Pulse Width: t1[s]

## ●Inductive Load Switching Circuit and Waveform

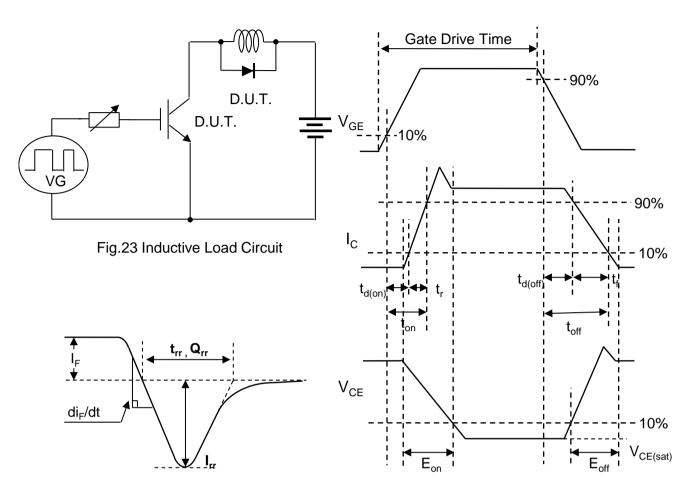


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

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