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E-mail: sales@raystar-optronics.com
Tel:886-4-2565-0761 Fax: 886-4-2565-0760

# RG12864B-FHW-V

# SPECIFICATION

CUSTOMER:

| | |
|---|---|
| APPROVED BY | |
| PCB VERSION | |
| DATE | |

FOR CUSTOMER USE ONLY

| SALES BY | APPROVED BY | CHECKED BY | PREPARED BY |
|---|---|---|---|
| | | | |

ISSUED DATE:

# Contents

# 1. Revision History

| DATE | VERSION | REVISED PAGE NO. | Note |
|---|---|---|---|
| 2008/11/21 | 1 | | First issue |

# 2. General Specification

The Features is described as follow:

- Module dimension: 75.0 x 52.7 x 8.9 (max.) $mm^3$
- View area: 60.0 x 32.6 $mm^2$
- Active area: 55.0 x 27.48 $mm^2$
- Number of dots: 128 x 64
- Dot size: 0.39 x 0.39 $mm^2$
- Dot pitch: 0.43 x 0.43 $mm^2$
- LCD type: FSTN Positive Transflective
- Duty: 1/64
- View direction: 6 o'clock
- Backlight Type: LED White

# 3. Module Coding System

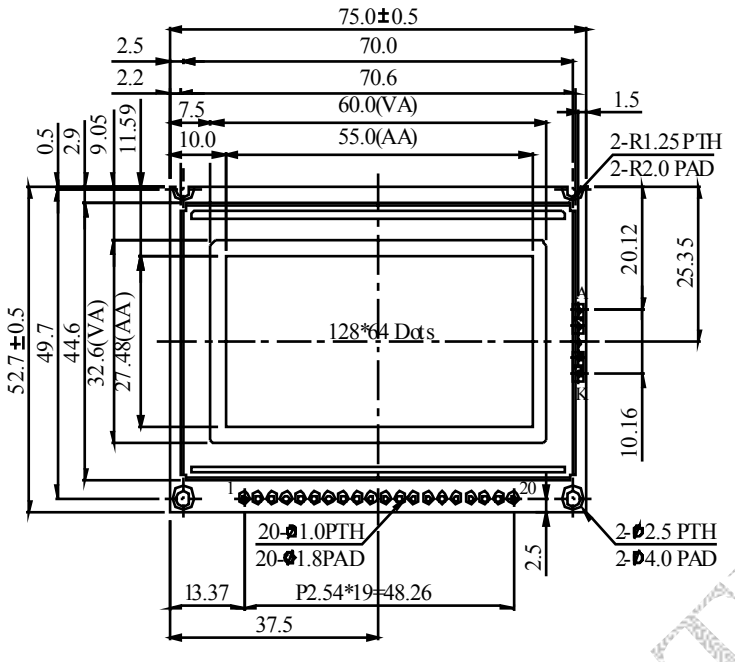
| R | G | 12864 | B | - | F | H | W | - | V |
|---|---|---|---|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | - | 5 | 6 | 7 | - | 8 |

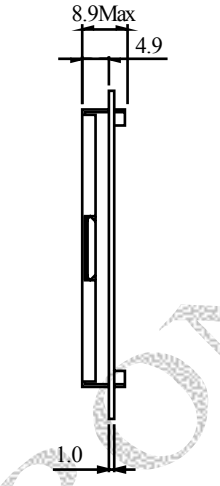
| Item | Description | | |
|---|---|---|---|
| 1 | **R：Raystar Optronics Inc.** | | |
| 2 | Display | C：Character Type, | |
| | | **G： Graphic Type** | |
| 3 | Number of dots：**128 x64 Dots** | | |
| 4 | Serials code | | |
| 5 | LCD | P：TN Positive, Gray | |
| | | N：TN Negative, | |
| | | G：STN Positive, Gray | |
| | | Y：STN Positive, Yellow Green | |
| | | B：STN Negative, Blue | |
| | | **F： FSTN Positive** | |
| | | T：FSTN Negative | |
| 6 | Polarizer Type, Temperature range, View direction | A：Reflective, N.T, 6:00 | K：Transflective, W.T,12:00 |
| | | D：Reflective, N.T, 12:00 | 1：Transflective, U.T,6:00 |
| | | G：Reflective, W. T, 6:00 | 4：Transflective, U.T.12:00 |
| | | J：Reflective, W. T, 12:00 | C：Transmissive, N.T,6:00 |
| | | 0：Reflective, U. T, 6:00 | F：Transmissive, N.T,12:00 |
| | | 3：Reflective, U. T, 12:00 | I：Transmissive, W. T, 6:00 |
| | | B：Transflective, N.T,6:00 | L：Transmissive, W.T,12:00 |
| | | E：Transflective, N.T.12:00 | 2：Transmissive, U. T, 6:00 |
| | | **H: Transflective, W.T,6:00** | 5：Transmissive, U.T,12:00 |
| 7 | Backlight | N：Without backlight | Y：LED, Yellow Green |
| | | P：EL, Blue green | A：LED, Amber |
| | | T：EL, Green | **W： LED, White** |
| | | D：EL, White | O：LED, Orange |
| | | F：CCFL, White | G：LED, Green |
| 8 | Special code | V: Built-in Negative Voltage | |

# 4. Interface Pin Function

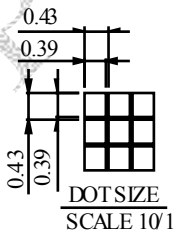
| Pin # | Symbol | Level | Description |
|---|---|---|---|
| 1 | VDD | 5.0V | Supply voltage for logic |
| 2 | $V_{SS}$ | 0V | Ground |
| 3 | $V_o$ | (Variable) | Contrast Adjustment |
| 4 | DB0 | H/L | Data bus line |
| 5 | DB1 | H/L | Data bus line |
| 6 | DB2 | H/L | Data bus line |
| 7 | DB3 | H/L | Data bus line |
| 8 | DB4 | H/L | Data bus line |
| 9 | DB5 | H/L | Data bus line |
| 10 | DB6 | H/L | Data bus line |
| 11 | DB7 | H/L | Data bus line |
| 12 | CS1 | L | Select Column 1~ Column 64 |
| 13 | CS2 | L | Select Column 65~ Column 128 |
| 14 | RST | L | Reset signal |
| 15 | R/W | H/L | H: Read (MPU←Module) , L: Write (MPU→Module) |
| 16 | D/I | H/L | H: Data , L : Instruction |
| 17 | E | H | Enable signal |
| 18 | Vee | — | Negative Voltage output |
| 19 | A | — | LED + |
| 20 | K | — | LED - |

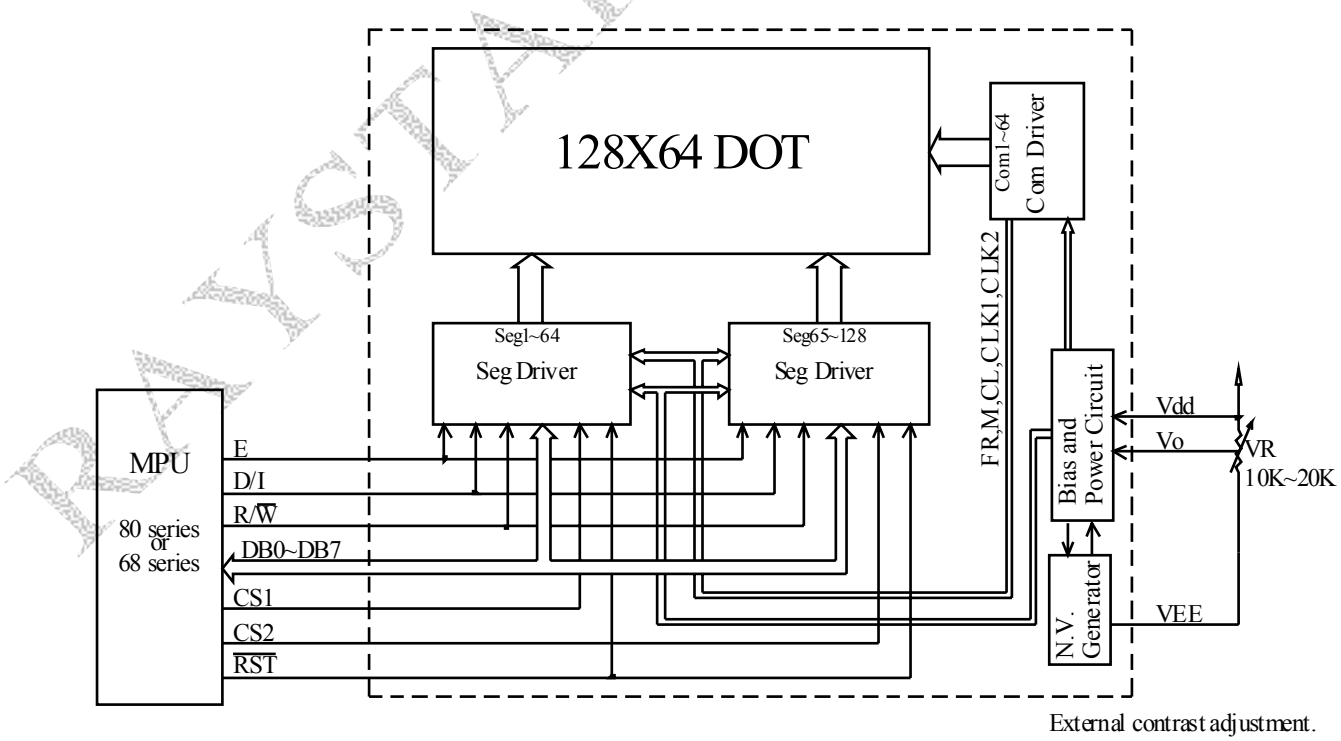
# 5. Outline Dimension & Block Diagram




| PIN NO | SYMBOL |
|---|---|
| 1 | Vdd |
| 2 | Vss |
| 3 | Vo |
| 4 | DB0 |
| 5 | DB1 |
| 6 | DB2 |
| 7 | DB3 |
| 8 | DB4 |
| 9 | DB5 |
| 10 | DB6 |
| 11 | DB7 |
| 12 | CS1 |
| 13 | CS2 |
| 14 | $\overline{RST}$ |
| 15 | R/$\overline{W}$ |
| 16 | D/$\overline{I}$ |
| 17 | E |
| 18 | Vee |
| 19 | A |
| 20 | K |




External contrast adjustment.

# 6. Display Control Instruction

The internal state of NT7108 is defined by Display Control Instruction, sent by MPU, shown in the table below.

| Instruction | RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 | Function |
|---|---|---|---|---|---|---|---|---|---|---|---|
| Display on/off | L | L | L | L | H | H | H | H | H | L/H | Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON |
| Set address (Y address) | L | L | L | H | Y address (0-63) | | | | | | Sets the Y address in the Y address counter. |
| Set page (X address) | L | L | H | L | H | H | H | Page (0-7) | | | Sets the X address at the X address register. |
| Display Start line (Z address) | L | L | H | H | Display start line (0-63) | | | | | | Indicates the display data RAM displayed at the top of the screen. |
| Status read | L | H | Busy | L | On/ Off | Reset | L | L | L | L | Read status.<br>BUSY L: Ready<br>H: In operation<br>ON/OFF L: Display ON<br>H: Display OFF<br>RESET L: Normal<br>H: Reset |
| Write display data | H | L | Write data | | | | | | | | Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically. |
| Read display data | H | H | Read data | | | | | | | | Reads data (DB0: 7) from display data RAM to the data bus. |

## Instruction

### Display ON/OFF

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | D |

**D gives the value of DB0. The Display Data appears as D is 1, and disappears as D is 0. The Display Data still remains** in Display Data RAM as D is 0, though the data is not shown on the screen. The Display Data can reappear by switching D from 0 to 1.

## SET ADDRESS (Y ADDRESS)

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 0 | 0 | 0 | 1 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

The Y addresses (AC0-AC5) of Display Data RAM are set in the Y address counter. Each address is set by instruction and incremented by 1 automatically by read or write operations.

## SET PAGE (X ADDRESS)

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 0 | 0 | 1 | 0 | 1 | 1 | 1 | AC2 | AC1 | AC0 |

The X address (AC0-AC2) of Display Data RAM is set in the X address counter. Writing or reading to or from MPU is executed in this specified page until the next page is set.

## DISPLAY START LINE (Z ADDRESS)

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 0 | 0 | 1 | 1 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

Z addresses (AC0-AC5) of the Display Data RAM are set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen are displayed from the line specified by display start line instruction.

## STATUS READ

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 0 | 1 | BUSY | 0 | ON/OFF | RESET | 0 | 0 | 0 | 0 |

• BUSY

As BUSY is 1, the Chip is executing internal operation and can not accept any instruction.

As BUSY is 0, the Chip is ready to accept any instruction.

• ON/OFF

As ON/OFF is 1, the display is OFF.

As ON/OFF is 0, the display is ON.

• RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions can be accepted except for the Status Read instruction.

As RESET is 0, initializing has finished and the system is in its normal operation condition.

## WRITE DISPLAY DATA

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 1 | 0 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |

The Write Display Data (D0-D7) are written into the Display Data RAM. Completing the writing instruction, Y address is increased by 1 automatically.

## READ DISPLAY DATA

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|---|---|---|---|---|---|---|---|---|---|
| 1 | 1 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |

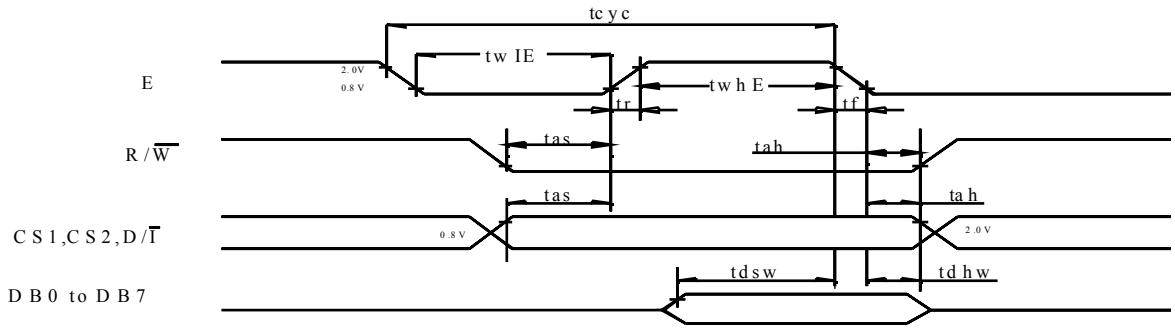
The Read Display Data (D0-D7) are read from the display data RAM. Completing the reading instruction, Y address is increased by 1 automatically.

# 7. Timing Characteristics

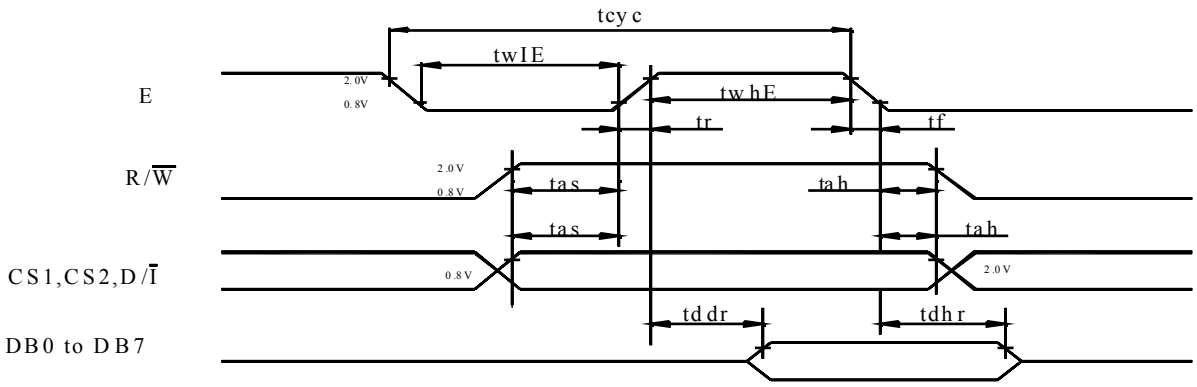
MPU Interface

(T=25℃, VDD=+5.0V±0.5)

| Characteristic | Symbol | Min | Type | Max | Unit |
|---|---|---|---|---|---|
| E cycle | tcyc | 1000 | — | — | ns |
| E high level width | twh E | 450 | — | — | ns |
| E low level width | tw lE | 450 | — | — | ns |
| E rise time | tr | — | — | 25 | ns |
| E tall time | tf | — | — | 25 | ns |
| Address set-up time | tas | 140 | — | — | ns |
| Address hold time | tah | 10 | — | — | ns |
| Data set-up time | tdsw | 200 | — | — | ns |
| Data delay time | tddr | — | — | 320 | ns |
| Data hold time (write) | tdhw | 10 | — | — | ns |
| Data hold time (read) | tdhr | 20 | — | — | ns |




MPU Write Timing



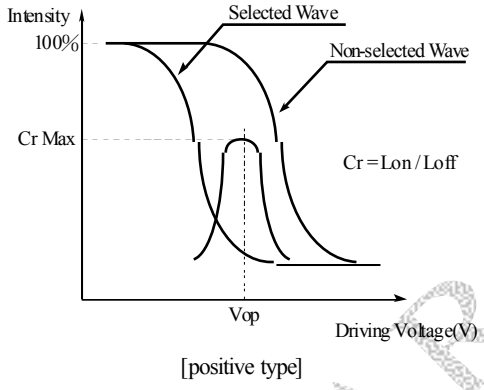

MPU Read Timing

# 8. Optical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| View Angle | (V)θ | CR≧2 | 30 | — | 60 | deg |
| | (H)φ | CR≧2 | -45 | — | 45 | deg |
| Contrast Ratio | CR | — | — | 5 | | — |
| Response Time | T rise | — | — | 150 | 200 | ms |
| | T fall | — | — | 150 | 200 | ms |

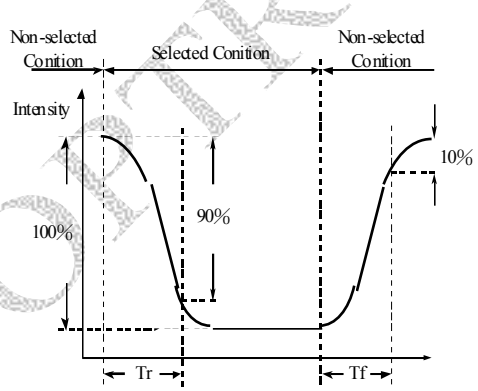
MPU Read Timing

**Definition of Operation Voltage, Vop.**




[positive type]

**Definition of Response Time, Tr and Tf.**



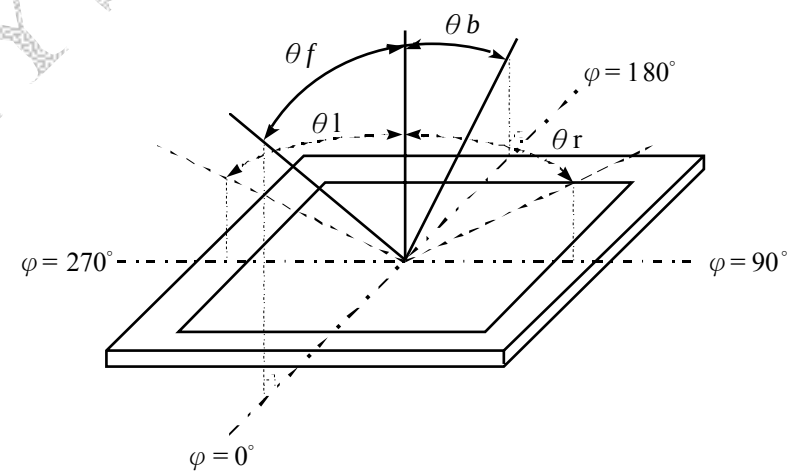

[positive type]

**Conditions:**

Operating Voltage : Vop　　Viewing Angle(θ，φ) : 0°， 0°

Frame Frequency: 64 HZ　　Driving Waveform: 1/N duty, 1/a bias

## Definition of viewing angle (CR≧2)



# 9. Absolute Maximum Ratings

| Item | Symbol | Min | Typ | Max | Unit |
|---|---|---|---|---|---|
| Operating Temperature | $T_{OP}$ | -20 | – | +70 | ℃ |
| Storage Temperature | $T_{ST}$ | -30 | – | +80 | ℃ |
| Input Voltage | $V_I$ | 0 | – | $V_{DD}$ | V |
| Supply Voltage For Logic | $V_{DD}$ | 0 | – | 6.7 | V |
| Supply Voltage For LCD | $V_{DD}$-$V_{LCD}$ | 0 | – | 16.7 | V |

# 10. Electrical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| Supply Voltage For Logic | $V_{DD}$-$V_{SS}$ | – | 4.5 | 5.0 | 5.5 | V |
| Supply Voltage For LCD | $V_{DD}$-$V_0$ | Ta=-20℃ | – | – | 9.6 | V |
| | | Ta=25℃ | – | 8.0 | – | V |
| | | Ta=+70℃ | 7.6 | – | – | V |
| Input High Volt. | $V_{IH}$ | – | 2.0 | – | $V_{DD}$ | V |
| Input Low Volt. | $V_{IL}$ | – | 0 | – | 0.8 | V |
| Output High Volt. | $V_{OH}$ | – | 2.4 | – | $V_{DD}$ | V |
| Output Low Volt. | $V_{OL}$ | – | – | – | 0.4 | V |
| Supply Current | $I_{DD}$ | – | 3.0 | 4.0 | 5.0 | mA |

# 11. Backlight Information

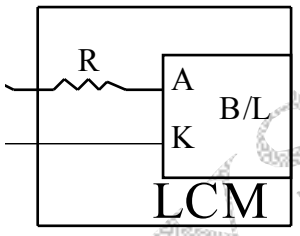
## Specification

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | TEST CONDITION |
|---|---|---|---|---|---|---|
| Supply Current | $I_{LED}$ | 40 | 48 | 60 | mA | V=3.5V |
| Supply Voltage | V | 3.4 | 3.5 | 3.6 | V | — |
| Reverse Voltage | VR | — | — | 5 | V | — |
| Luminous Intensity | IV | 150 | 200 | — | $cd/m^2$ | $I^{LED}$=60mA |
| Wave Length | λp | — | | — | nm | $I_{LED}$=60mA |
| Life Time | — | — | 10K | — | hr. | $I_{LED}$=60mA |
| Color | White | | | | | |

**Note:**

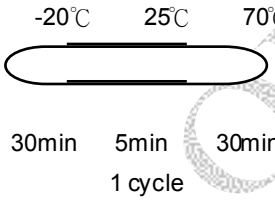
**The LED of B/L is drive by current only; drive voltage is for reference only. Drive voltage has to make driving current under safety area (current between minimum and maximum).**

.Drive from pin19,pin20



# 12. Reliability

**Content of Reliability Test (wide temperature, -20℃~70℃)**

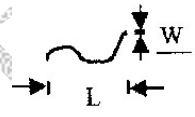
| Environmental Test | | | |
|---|---|---|---|
| **Test Item** | **Content of Test** | **Condition** | **Note** |
| High Temperature storage | Endurance test applying the high storage temperature for a long time. | 80℃<br>200hrs | 2 |
| Low Temperature storage | Endurance test applying the high storage temperature for a long time. | -30℃<br>200hrs | 1,2 |
| High Temperature Operation | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time. | 70℃<br>200hrs | - |
| Low Temperature Operation | Endurance test applying the electric stress under low temperature for a long time. | -20℃<br>200hrs | 1 |
| High Temperature/<br>Humidity Operation | The module should be allowed to stand at 60℃,90%RH max<br>For 96hrs under no-load condition excluding the polarizer,<br>Then taking it out and drying it at normal temperature. | 60℃,90%RH<br>96hrs | 1,2 |
| Thermal shock resistance | The sample should be allowed stand the following 10 cycles of operation<br>  <br> | -20℃/70℃<br>10 cycles | - |
| Vibration test | Endurance test applying the vibration during transportation and using. | fixed amplitude: 15mm<br>Vibration. Frequency: 10~55Hz.<br>One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes | 3 |
| Static electricity test | Endurance test applying the electric stress to the terminal. | VS=800V,RS=1.5kΩ<br>CS=100pF<br>1 time | —— |

**Note1: No dew condensation to be observed.**

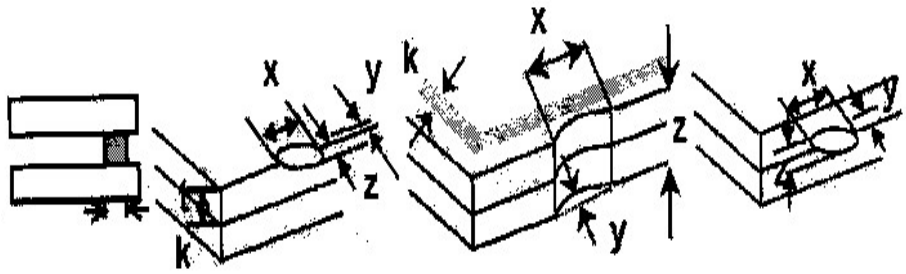
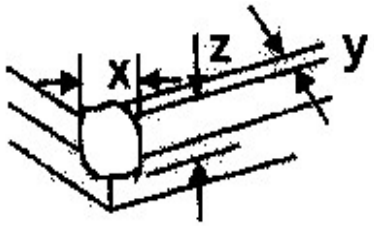
**Note2: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after remove from the test chamber.**

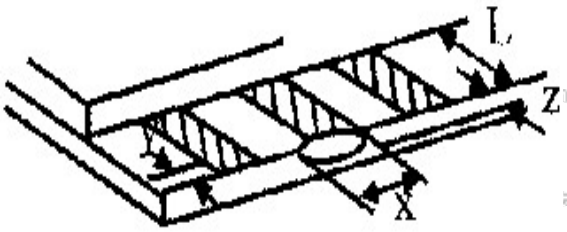
**Note3: Vibration test will be conducted to the product itself without putting it in a container.**

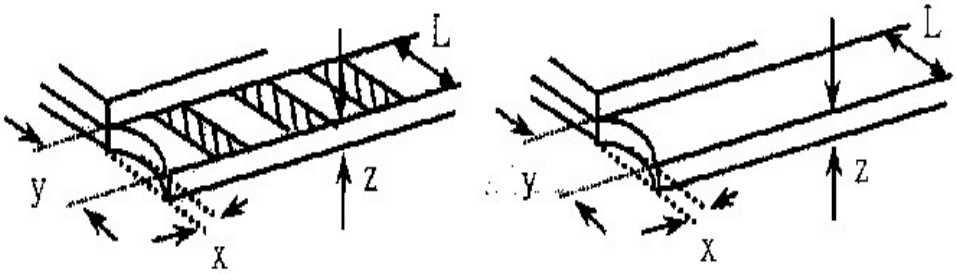
# 13. Inspection specification

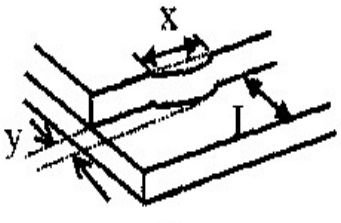
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>01</td><td>Electrical Testing</td><td>1.1 Missing vertical, horizontal segment, segment contrast defect.<br>1.2 Missing character, dot or icon.<br>1.3 Display malfunction.<br>1.4 No function or no display.<br>1.5 Current consumption exceeds product specifications.<br>1.6 LCD viewing angle defect.<br>1.7 Mixed product types.<br>1.8 Contrast defect.</td><td>0.65</td></tr>
<tr><td>02</td><td>Black or white spots on LCD (display only)</td><td>2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present.<br>2.2 Densely spaced: No more than two spots or lines within 3mm</td><td>2.5</td></tr>
<tr><td rowspan="2">03</td><td rowspan="2">LCD black spots, white spots, contamination (non-display)</td><td>3.1 Round type : As following drawing<br>Φ=( x + y ) / 2</td><td>2.5</td></tr>
<tr><td>3.2 Line type : (As following drawing)<br> 

<table>
<tr><th>Length</th><th>Width</th><th>Acceptable Q TY</th></tr>
<tr><td>---</td><td>W≦0.02</td><td>Accept no dense</td></tr>
<tr><td>L≦3.0</td><td>0.02＜W≦0.03</td><td rowspan="2">2</td></tr>
<tr><td>L≦2.5</td><td>0.03＜W≦0.05</td></tr>
<tr><td>---</td><td>0.05＜W</td><td>As round type</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>04</td><td>Polarizer bubbles</td><td>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.
<table>
<tr><th>Size Φ</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.20</td><td>Accept no dense</td></tr>
<tr><td>0.20＜Φ≦0.50</td><td>3</td></tr>
<tr><td>0.50＜Φ≦1.00</td><td>2</td></tr>
<tr><td>1.00＜Φ</td><td>0</td></tr>
<tr><td>Total Q TY</td><td>3</td></tr>
</table></td><td>2.5</td></tr>
</table>

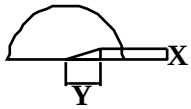
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>05</td><td>Scratches</td><td>Follow NO.3 LCD black spots, white spots, contamination</td><td></td></tr>
<tr><td>06</td><td>Chipped glass</td><td>Symbols Define:<br>x: Chip length y: Chip width z: Chip thickness<br>k: Seal width t: Glass thickness a: LCD side length<br>L: Electrode pad length:<br><br>6.1 General glass chip :<br>6.1.1 Chip on panel surface and crack between panels:<br>  <br><br><table><tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙If there are 2 or more chips, x is total length of each chip.<br><br>6.1.2 Corner crack:<br>  <br><br><table><tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙If there are 2 or more chips, x is the total length of each chip.</td><td>2.5</td></tr>
</table>

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>06</td><td>Glass crack</td><td>
Symbols :<br>
x: Chip length y: Chip width z: Chip thickness<br>
k: Seal width t: Glass thickness a: LCD side length<br>
L: Electrode pad length<br>
6.2 Protrusion over terminal :<br>
6.2.1 Chip on electrode pad :<br>


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦0.5mm</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
6.2.2 Non-conductive portion:<br>


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦ L</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.<br>
⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.<br>
6.2.3 Substrate protuberance and internal crack.<br>


<table>
<tr><th>y: width</th><th>x: length</th></tr>
<tr><td>y≦1/3L</td><td>x ≦ a</td></tr>
</table>
</td><td>2.5</td></tr>
</table>

| NO | Item | Criterion | AQL |
|---|---|---|---|
| 07 | Cracked glass | The LCD with extensive crack is not acceptable. | 2.5 |
| 08 | Backlight elements | 8.1 Illumination source flickers when lit.<br>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.<br>8.3 Backlight doesn't light or color wrong. | 0.65<br>2.5<br>0.65 |
| 09 | Bezel | 9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.<br>9.2 Bezel must comply with job specifications. | 2.5<br>0.65 |
| 10 | PCB、COB | 10.1 COB seal may not have pinholes larger than 0.2mm or contamination.<br>10.2 COB seal surface may not have pinholes through to the IC.<br>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.<br>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.<br>10.5 No oxidation or contamination PCB terminals.<br>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.<br>10.7 The jumper on the PCB should conform to the product characteristic chart.<br>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.<br>10.9 The Scraping testing standard for Copper Coating of PCB<br>  <br>**X * Y<=2mm²** | 2.5<br>2.5<br>0.65<br>2.5<br>2.5<br>0.65<br>0.65<br>2.5<br>2.5 |
| 11 | Soldering | 11.1 No un-melted solder paste may be present on the PCB.<br>11.2 No cold solder joints, missing solder connections, oxidation or icide.<br>11.3 No residue or solder balls on PCB.<br>11.4 No short circuits in components on PCB. | 2.5<br>2.5<br>2.5<br>0.65 |

| NO | Item | Criterion | AQL |
|---|---|---|---|
| 12 | General appearance | 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.<br>12.2 No cracks on interface pin (OLB) of TCP.<br>12.3 No contamination, solder residue or solder balls on product.<br>12.4 The IC on the TCP may not be damaged, circuits.<br>12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.<br>12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.<br>12.7 Sealant on top of the ITO circuit has not hardened.<br>12.8 Pin type must match type in specification sheet.<br>12.9 LCD pin loose or missing pins.<br>12.10 Product packaging must the same as specified on packaging specification sheet.<br>12.11 Product dimension and structure must conform to product specification sheet. | 2.5<br>0.65<br>2.5<br>2.5<br>2.5<br>2.5<br>2.5<br>0.65<br>0.65<br>0.65<br>0.65 |

# 14. Precautions in use of LCD Modules

1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
2. Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
3. Don't disassemble the LCM.
4. Don't operate it above the absolute maximum rating.
5. Don't drop, bend or twist LCM.
6. Soldering: only to the I/O terminals.
7. Storage: please storage in anti-static electricity container and clean environment.

# 15. Material List of Components for RoHs

1. RAYSTAR Optronics Co., Ltd. hereby declares that all of or part of products, including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

   Exhibit A：The Harmful Material List

| Material | (Cd) | (Pb) | (Hg) | (Cr6+) | PBBs | PBDEs |
|---|---|---|---|---|---|---|
| Limited Value | 100 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm |
| Above limited value is set up according to RoHS. | | | | | | |

2. Process for RoHS requirement：
   (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
   (2) Heat-resistance temp.：
   Reflow：250℃, 30 seconds Max.；
   Connector soldering wave or hand soldering：320℃, 10 seconds max.
   (3) Temp. curve of reflow, max. Temp.：235±5℃；
   Recommended customer's soldering temp. of connector：280℃, 3 seconds.

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## LCM Sample Estimate Feedback Sheet

**Module Number：** _____

| | | |
|---|---|---|
| **1、Panel Specification：** | | |
| 1. Panel Type： | □ Pass | □ NG ,_____ |
| 2. View Direction： | □ Pass | □ NG ,_____ |
| 3. Numbers of Dots： | □ Pass | □ NG ,_____ |
| 4. View Area： | □ Pass | □ NG ,_____ |
| 5. Active Area： | □ Pass | □ NG ,_____ |
| 6.Operating Temperature： | □ Pass | □ NG ,_____ |
| 7.Storage Temperature： | □ Pass | □ NG ,_____ |
| 8.Others：_____ | | |
| **2、Mechanical Specification：** | | |
| 1. PCB Size： | □ Pass | □ NG ,_____ |
| 2.Frame Size： | □ Pass | □ NG ,_____ |
| 3.Materal of Frame： | □ Pass | □ NG ,_____ |
| 4.Connector Position： | □ Pass | □ NG ,_____ |
| 5.Fix Hole Position： | □ Pass | □ NG ,_____ |
| 6.Backlight Position： | □ Pass | □ NG ,_____ |
| 7. Thickness of PCB： | □ Pass | □ NG ,_____ |
| 8. Height of Frame to PCB： | □ Pass | □ NG ,_____ |
| 9.Height of Module： | □ Pass | □ NG ,_____ |
| 10.Others： | □ Pass | □ NG ,_____ |
| **3、Relative Hole Size：** | | |
| 1.Pitch of Connector： | □ Pass | □ NG ,_____ |
| 2.Hole size of Connector： | □ Pass | □ NG ,_____ |
| 3.Mounting Hole size： | □ Pass | □ NG ,_____ |
| 4.Mounting Hole Type： | □ Pass | □ NG ,_____ |
| 5.Others： | □ Pass | □ NG ,_____ |
| **4、Backlight Specification：** | | |
| 1.B/L Type： | □ Pass | □ NG ,_____ |
| 2.B/L Color： | □ Pass | □ NG ,_____ |
| 3.B/L Driving Voltage (Reference for LED Type)：□ Pass □ NG ,_____ | | |
| 4.B/L Driving Current： | □ Pass | □ NG ,_____ |
| 5.Brightness of B/L： | □ Pass | □ NG ,_____ |
| 6.B/L Solder Method： | □ Pass | □ NG ,_____ |
| 7.Others： | □ Pass | □ NG ,_____ |

>> Go to page 2 <<

**Module Number**：_____

**5**、**Electronic Characteristics of Module**：

| | | |
|---|---|---|
| 1.Input Voltage： | □ Pass | □ NG ,_____ |
| 2.Supply Current： | □ Pass | □ NG ,_____ |
| 3.Driving Voltage for LCD： | □ Pass | □ NG ,_____ |
| 4.Contrast for LCD： | □ Pass | □ NG ,_____ |
| 5.B/L Driving Method： | □ Pass | □ NG ,_____ |
| 6.Negative Voltage Output： | □ Pass | □ NG ,_____ |
| 7.Interface Function： | □ Pass | □ NG ,_____ |
| 8.LCD Unifomity： | □ Pass | □ NG ,_____ |
| 9.ESD test： | □ Pass | □ NG ,_____ |
| 10.Others： | □ Pass | □ NG ,_____ |

**6**、**Summary**：

**Sales signature**：_____

**Customer Signature**：_____ **Date**：__/__/__