



#### 3.3 V, SWITCH AND LNA FRONT END SOLUTION

Package Style: QFN, 12-pin, 2mmx2mmx0.5mm



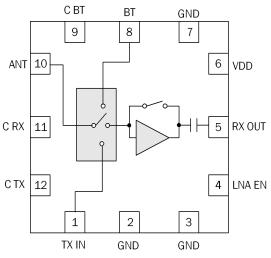


#### **Features**

- Single Supply Voltage 3.0V to 4.5V
- Integrated SP3T Switch and LNA with Bypass
- Typical Gain is 11dB and 2.0dB NF in RX Mode Pin-to-Pin
- SP3T Switch Control Voltage is 2.1V to 5V (3.0V Typical)

### **Applications**

- IEEE802.11b/g/n WiFi Applications
- Portable Battery-Powered Equipment
- WiFi/Bluetooth®
   Combination Devices



Functional Block Diagram

### **Product Description**

The RF5501 is designed specifically for high-performance WiFi applications in the 2.4GHz to 2.5GHz ISM band, including Personal Media Players (PMPs), digital cameras, and WiFi enabled handsets.

The RF5501 integrates the LNA with bypass and an SP3T switch of a Front-End solution for WiFi and Bluetooth® combination systems. The integrated input and output match reduces the number of external components, keeping cost down and utilizing minimum layout area for implementation. The RF5501 is provided in a 2mmx2mmx0.5mm, 12-pin QFN package. This LNA + Switch front-end solution meets or exceeds the specification requirements of IEEE 802.11 b/g/n WiFi RF systems.

#### **Ordering Information**

RF5501 Standard 25 piece bag
RF5501SR Standard 100 piece reel
RF5501TR7 Standard 2500 piece reel
RF5501PCK-410 Fully Assembled Evaluation Board

## **RF5501**



#### **Absolute Maximum Ratings**

Parameter	Rating	Unit
DC Supply Voltage	5.5	V
Operating Temperature Range	-40 to +85	°C
Storage Temperature	-40 to +150	°C
Antenna Port Nominal Impedance	50	Ω
Stability Output VSWR	5:1	
LNA Input Power (no damage)	5	dBm
Moisture Sensitivity	MSL2	



Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified spical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EU Directive 2011/65/EU (at time of this document revision).

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Parameter	Specification			Unit	Condition	
Parameter	Min.	Тур.	Max.	Unit	Condition	
Compliance					IEEE802.11b, IEEE802.11g FCC CFR 15.247,.205,.209, V <sub>DD</sub> =3.3V, LNA EN=3.0V, Temp=-10°C to +75°C, Freq=2.4GHz to 2.5GHz, unless noted otherwise.	
Operating Frequency	2.4		2.5	GHz		
LNA Voltage Supply (V <sub>DD</sub> )	3.0	3.3	4.5	V		
LNA Enable Voltage (LNA_EN)	2.7	3.0	4.5	V	LNA ON	
			0.2	V	LNA OFF; Bypass mode ON	
Switch Control Voltage "HIGH"	2.4	3.0	4.5	V	C_RX, C_TX, C_BT	
Switch Control Voltage "LOW"			0.2	V	C_RX, C_TX, C_BT	
LNA Bypass Switch	2.7	3.0	4.5	V	LNA_EN high; Bypass mode OFF	
			0.2	V	LNA_EN low; Bypass mode ON	
Current Consumption						
LNA V <sub>DD</sub>		7	10.5	mA	LNA ON	
			10	μΑ	LNA OFF	
LNA_EN high		1	1.5	mA		
LNA_EN low			10	uA		
Switch Controls			10	uA	1-3 uA per control line	
Gain						
WiFi Receive	8	11	14	dB	C RX <sub>HI</sub> , C TX <sub>LO</sub> , C BT <sub>LO</sub> , LNA EN <sub>HI</sub>	
WiFi RX Bypass	-5	-3.5		dB	LNA EN<0.2V	
Simultaneous WiFi/ BT Receive (note 2)	7	8		dB	Measured at RX OUT (LNA EN $_{\rm HI}$ , C RX $_{\rm HI}$ , C BT $_{\rm HI}$ , C TX $_{\rm LO}$ )	
	-5.0	-4		dB	Measured at BT Port (LNA $\mathrm{EN_{HI}}$ , $\mathrm{C}$ $\mathrm{RX_{HI}}$ , $\mathrm{C}$ $\mathrm{BT_{HI}}$ , $\mathrm{C}$ $\mathrm{TX_{LO}}$ )	



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Parameter	Min.	Тур.	Max.	Unit	Condition	
Insertion Loss						
WiFi Bypass (ANT to RX_OUT)		3.5	5.0	dB	C RX <sub>HI</sub> , C TX <sub>LO</sub> , C BT <sub>LO</sub> , LNA EN <sub>LO</sub>	
BT (ANT to BT)		0.8	1.2	dB	C BT <sub>HI</sub> , C RX <sub>LO</sub> , C TX <sub>LO</sub> , LNA EN <sub>X</sub>	
TX (TX_IN to ANT)		0.7	1.2	dB	C_TX <sub>HI</sub> , C_RX <sub>LO</sub> , C_BT <sub>LO</sub> , LNA_EN <sub>X</sub>	
Simultaneous WiFi/ BT Bypass (note 2)		6	7	dB	Measured at RX OUT (LNA EN $_{\rm LO}$ , C RX $_{\rm HI}$ , C BT $_{\rm HI}$ , C TX $_{\rm LO}$ )	
		4	5.0	dB	$\begin{array}{c} \text{Measured at BT Port (LNA EN}_{\text{LO}}, \text{C RX}_{\text{HI}}, \text{C BT}_{\text{HI}}, \\ \text{C TX}_{\text{LO}}) \end{array}$	
Noise Figure						
WiFi Rx Mode		2.0	3.0	dB	Including switch, LNA EN <sub>HI</sub>	
ВТ		0.8	1.2	dB		
Simultaneous WiFi/ BT RX (note 2)		4	5.0	dB	Measured at RX OUT (LNA EN $_{\rm HI}$ , C RX $_{\rm HI}$ , C BT $_{\rm HI}$ , C TX $_{\rm LO}$ )	
Input IP3	+1	+4		dBm	LNA ON (Highh Gain Mode)	
Return Loss						
WiFi RX Mode	7.5	15		dB	Measured at RX OUT	
BT	10	15		dB	Measured at BT Port	
Transmit Port	10	15		dB	Measured at TX IN	
Antenna Port (WiFi RX Mode)	7.5	11		dB	Measured at ANT Port under load conditions	
Other Parameters						
Input/Output Impedance		50		Ω	All RF Ports (note 2)	
Passband Ripple	-0.2		+0.2	dB	All modes	
TX Output Power	21	23		dBm	C_TX>3.0V; 1% composite EVM (note1)	
Switch P1dB		28		dBm		
Isolation						
TX to BT	25	29		dB	Measured ANT-BT in TX mode	
TX to RX	20	23		dB	Measured ANT-RX in TX mode	
Switch Control Speed		50		ns		
ESD Human Body Model (HBM)	500			V	Class 1B; JESD22-A114	

Note 1: Assumes system EVM < 0.5% for input signal.

Note 2: The FEM can be placed in receive WiFi and Bluetooth modes simultaneously with increased insertion loss.

#### **Switch Control Logic**

ESD Charge Device Model (CDM)

	Switch Controls			
MODE	C BT	C RX	C TX	LNA EN
WL RX	LOW	HIGH	LOW	HIGH
WiFi RX Bypass	LOW	HIGH	LOW	LOW
BT	HIGH	LOW	LOW	LOW
TX	LOW	LOW	HIGH	LOW
Simultaneous WL/BT RX	HIGH	HIGH	LOW	HIGH

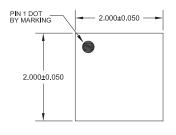
650

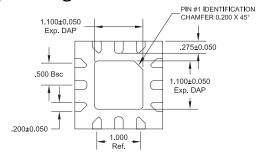
Class III; JESD22-C101



Pin	Function	Description	
1	TX IN	RF input for the 802.11b/g/n PA. Input is matched to $50\Omega$ .	
2	GND	Ground.	
3	GND	Ground.	
4	LNA EN	A logic HIGH enables the LNA.	
5	RX OUT	Receive port for 802.11b/g/n. Internally matched to 50Ω. DC block provided internally.	
6	VDD	Supply voltage to the LNA.	
7	GND	Ground.	
8	BT	RF bidirectional port for Bluetooth $^{\otimes}$ . Input is matched to 50 $\Omega$ .	
9	C BT	Bluetooth® mode control voltage. See switch truth table for proper level.	
10	ANT	This is the common port (antenna). It is matched at $50\Omega$ .	
11	C RX	Receive mode control voltage. See switch truth table for proper level.	
12	C TX	Transmit mode control voltage. See switch truth table for proper level.	

## **Package Drawing**



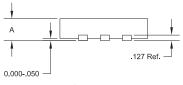


TOP VIEW

		ETSLP
	MAX.	0.500
Α	NOM.	0.450
	MIN.	0.400

Notes: 1) Pin 1 Shaded Area

**BOTTOM VIEW** 

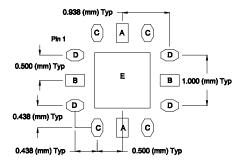


SIDE VIEW



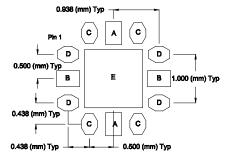
#### **PCB Metal Land Pattern**

A = 0.230 x 0.378 (mm) Typ B = 0.378 x 0.230 (mm) Typ C = 0.230 x 0.378 (mm) Typ Octgaon D = 0.378 x 0.230 (mm) Typ Octgaon E = 1.100 (mm) Sq



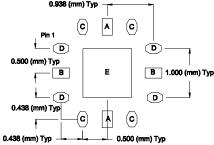
#### **PCB Solder Mask Pattern**

A = 0.330 x 0.478 (mm) Typ B = 0.478 x 0.330 (mm) Typ C = 0.330 x 0.478 (mm) Typ Octagon D = 0.478 x 0.330 (mm) Typ Octagon F = 1.200 (mm) Sq



#### **PCB Stencil Pattern**

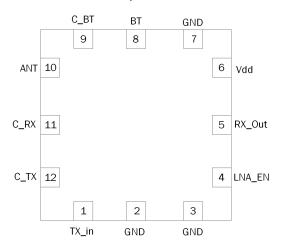
A = 0.207 x 0.340 (mm) Typ B = 0.340 x 0.207 (mm) Typ C = 0.207 x 0.340 (mm) TypOctagon D = 0.340 x 0.207 (mm) Typ Octagon E = 0.990 (mm) Sq



## **RF5501**

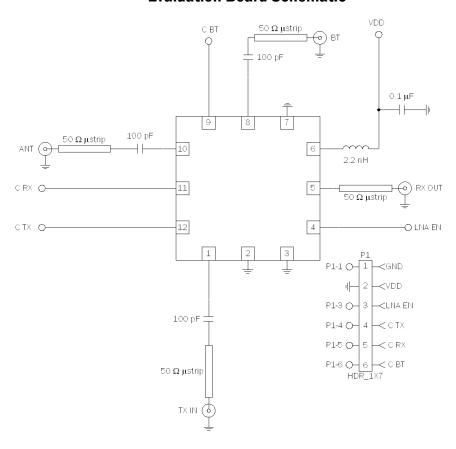


# **Pin Out** Top View





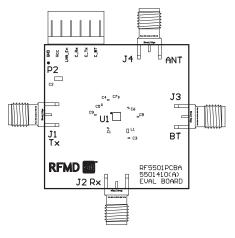
## **Evaluation Board Schematic**

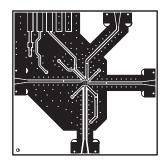




# Evaluation Board Layout Board Size 1.5" x 1.5"

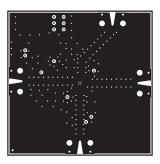
## Board Thickness 0.032", Board Material FR-4, Multi-layer



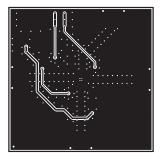


**TOP SILK** 



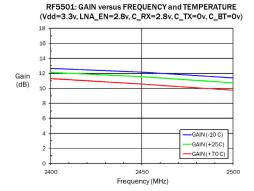


MID-1

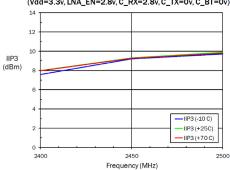


BOTTOM

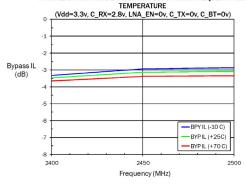




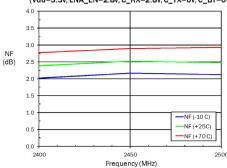
RF5501: INPUT IP3 versus FREQUENCY and TEMPERATURE (Vdd=3.3v, LNA\_EN=2.8v, C\_RX=2.8v, C\_TX=0v, C\_BT=0v)



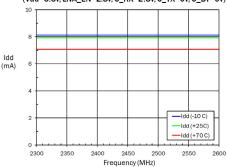
RF5501: BYPASS MODE INSERTION LOSS versus FREQUENCY and



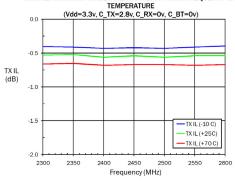
RF5501: NF versus FREQUENCY and TEMPERATURE (Vdd=3.3v, LNA\_EN=2.8v, C\_RX=2.8v, C\_TX=0v, C\_BT=0v)



RF5501: CURRENT versus FREQUENCY and TEMPERATURE (Vdd=3.3v, LNA\_EN=2.8v, C\_RX=2.8v, C\_TX=0v, C\_BT=0v)



RF5501: TRANSMIT MODE INSERTION LOSS versus FREQUENCY and



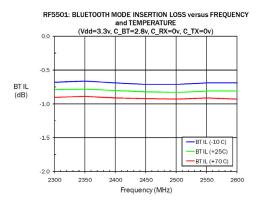
## **RF5501**

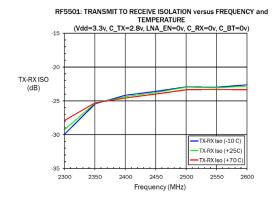
-40 <del>| -</del> 2300

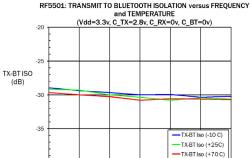
2350

2400









2450

Frequency (MHz)

2500

2550

2600