GENERAL PURPOSE AMPLIFIER

RoHS Compliant & Pb-Free Product

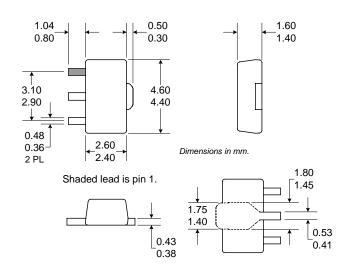
Typical Applications

- Basestation Applications
- Broadband, Low-Noise Gain Blocks
- IF or RF Buffer Amplifiers

- Driver Stage for Power Amplifiers
- Final PA for Low-Power Applications
- High Reliability Applications

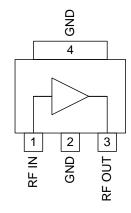
Product Description

The RF3375 is a general purpose, low-cost RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily-cascadable 50Ω gain block. Applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to $6000\,\text{MHz}.$ The device is self-contained with 50Ω input and output impedances and requires only two external DC-biasing elements to operate as specified.



Optimum Technology Matching® Applied

☐ Si BJT ☐ GaAs MESFET☐ Si Bi-CMOS☐ SiGe HBT☐ Si CMOS☐ InGaP/HBT☐ GaN HEMT☐ SiGe Bi-CMOS☐



Functional Block Diagram

Package Style: SOT89

Features

- DC to >6000MHz Operation
- Internally Matched Input and Output
- 13.2dB Small Signal Gain
- +28dBm Output IP3
- +16.0dBm Output P1dB

Ordering Information

RF3375 General Purpose Amplifier RF337XPCBA-41XFully Assembled Evaluation Board

RF Micro Devices, Inc. 7628 Thorndike Road Greensboro, NC 27409, USA Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com

Absolute Maximum Ratings

| Parameter | Rating | Unit |
|-------------------------------|-------------|------|
| Input RF Power | +13 | dBm |
| Operating Ambient Temperature | -40 to +85 | °C |
| Storage Temperature | -60 to +150 | °C |
| I _{CC} | 80 | mA |



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| Doromotor | Specification | | l lm!4 | Condition | | |
|--|---------------|-------------|--------|-----------|--|--|
| Parameter | Min. | Тур. | Max. | Unit | Condition | |
| Overall | | | | | T=25 °C, I _{CC} =65 mA (See Note 1.) | |
| Frequency Range | | DC to >6000 | | MHz | | |
| 3dB Bandwidth | | 6 | | GHz | | |
| Gain | 12.5 | 13.5 | | dB | Freq=500MHz | |
| | 12.5 | 13.5 | | dB | Freq=1000MHz | |
| | 12.2 | 13.2 | | dB | Freq=2000MHz | |
| | 12.2 | 13.2 | | dB | Freq=3000MHz | |
| | 12.0 | 13.0 | | | Freq=4000MHz | |
| | 10.0 | 12.4 | | | Freq=6000MHz | |
| Noise Figure | | 4.6 | | dB | Freq=2000MHz | |
| Input VSWR | | <1.9:1 | | | In a 50Ω system, DC to 6000MHz | |
| Output VSWR | | <2.0:1 | | | In a 50Ω system, DC to $500 MHz$ | |
| | | <1.7:1 | | | In a 50Ω system, 500MHz to 6000MHz | |
| Output IP ₃ | +31.0 | +33.9 | | dBm | Freq=1000MHz | |
| | +28.0 | +30.0 | | dBm | Freq=2000MHz | |
| Output P _{1dB} | +17.0 | +18.5 | | dBm | Freq=1000MHz | |
| | +14.5 | +16.0 | | dBm | Freq=2000MHz | |
| Reverse Isolation | | -18.0 | | dB | Freq=2000MHz | |
| Thermal | | | | | I _{CC} =65mA, P _{DISS} =313mW. (See Note 3.) | |
| Theta _{JC} | | 175 | | °C/W | V _{PIN} =4.81 V | |
| Maximum Measured Junction | | 139 | | °C | T _{CASE} =+85°C | |
| Temperature at DC Bias Con- ditions | | | | | | |
| Mean Time to Failures | | 1500 | | years | T _{CASE} =+85°C | |
| Power Supply | | | | | With 22Ω bias resistor, T=+25°C | |
| Device Operating Voltage | | 5.18 | 5.36 | V | At pin 8 with I _{CC} =65mA | |
| | | 6.6 | 7.0 | V | At Evaluation Board Connector I _{CC} =65mA | |
| Operating Current | | 65 | 80 | mA | See Note 2. | |

Note 1: All specification and characterization data has been gathered on standard FR-4 evaluation boards. These evaluation boards are not optimized for frequencies above 2.5GHz. Performance above 2.5GHz may improve if a high performance PCB is used.

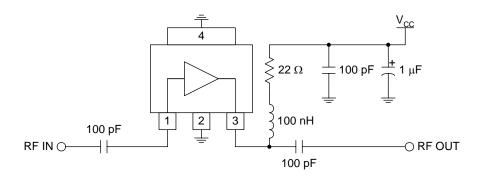
Note 3: Because of process variations from part to part, the current resulting from a fixed bias voltage will vary. As a result, caution should be used in designing fixed voltage bias circuits to ensure the worst case bias current does not exceed 80 mA over all intended operating conditions.

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Note 2: The RF3375 must be operated at or below 80 mA in order to achieve the thermal performance listed above. While the RF3375 may be operated at higher bias currents, 65 mA is the recommended bias to ensure the highest possible reliability and electrical performance.

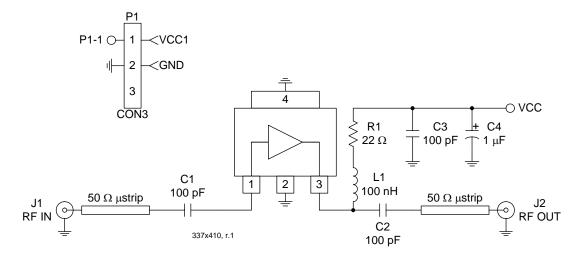
| Pin | Function | Description | Interface Schematic |
|-----|----------|---|---------------------|
| 1 | RF IN | RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability. | |
| 2 | GND | Ground connection. | |
| 3 | RF OUT | RF output and bias pin. Biasing is accomplished with an external series resistor and choke inductor to V_{CC} . The resistor is selected to set the DC current into this pin to a desired level. The resistor value is determined by the following equation: $R = \frac{(V_{SUPPLY} - V_{DEVICE})}{I_{CC}}$ Care should also be taken in the resistor selection to ensure that the current into the part never exceeds 80 mA over the planned operating temperature . This means that a resistor between the supply and this pin is always required, even if a supply near 5.0V is available, to provide DC feedback to prevent thermal runaway. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. The supply side of the bias network should also be well bypassed. | RF IN O |
| 4 | GND | Ground connection. | |

Application Schematic



Evaluation Board Schematic

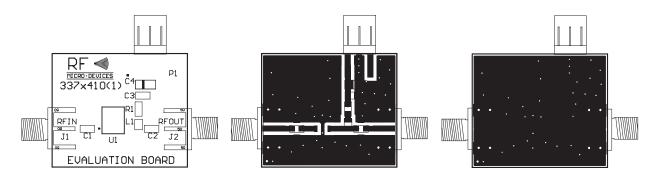
(Download Bill of Materials from www.rfmd.com.)

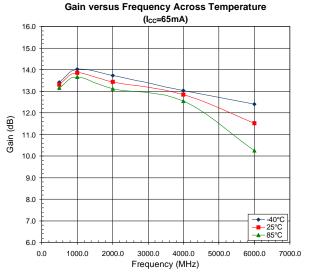


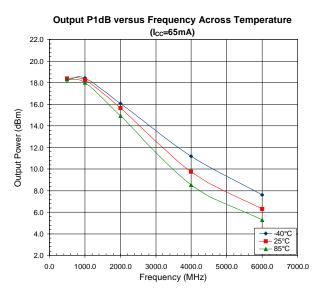
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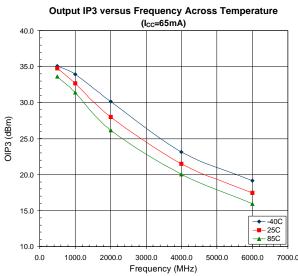
Evaluation Board Layout Board Size 1.195" x 1.000"

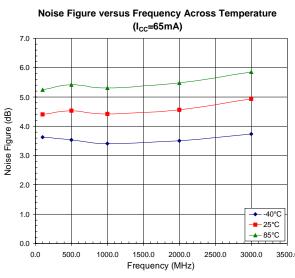
Board Thickness 0.033", Board Material FR-4

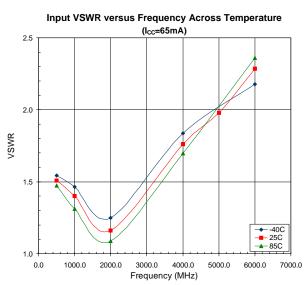


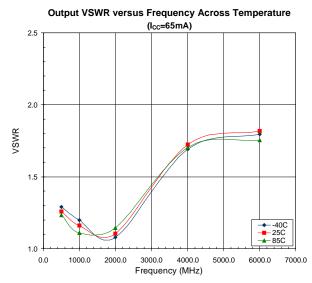




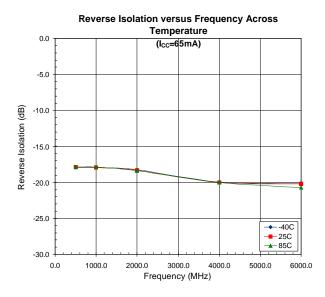


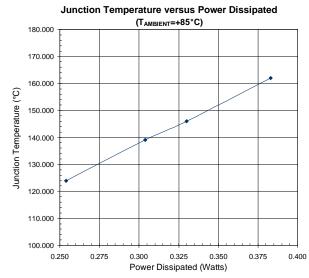


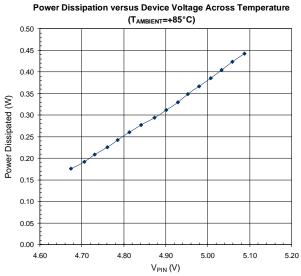


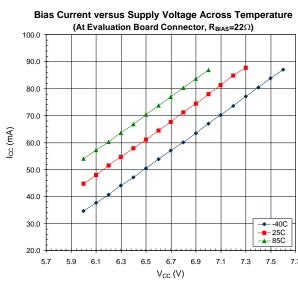


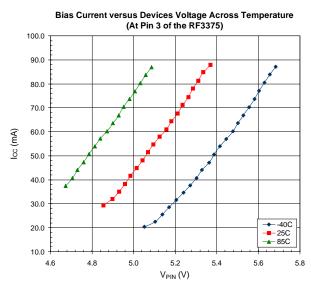
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