



Power Transistors

RCA31/SDH RCA31B/SDH
RCA31A/SDH RCA31C/SDH

Hometaxial-Base, Silicon N-P-N VERSAWATT Transistors

For Medium-Power Switching and
 Amplifier Applications

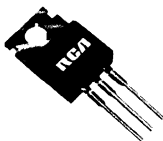
Features:

- 50 W at 25°C case temperature
- Low saturation voltage
- Maximum safe-area-of-operation curves
- Thermal-cycle ratings curves

TERMINAL CONNECTIONS

Terminal No. 1 — Base
 Terminal No. 2 — Collector
 Terminal No. 3 — Emitter

Mounting Flange Terminal No. 4 — Collector



JEDEC TO-220 AB

H-1535

RCA31/SDH, RCA31A/SDH, RCA31B/SDH, and RCA31C/SDH are single-diffused hometaxial-base, silicon n-p-n transistors. These types are essentially hometaxial-base versions of the RCA31, RCA31A, RCA31B, and RCA31C epitaxial-base types, respectively. • They are intended for a wide variety of switching and amplifier applications, such as series and shunt

regulators and driver and output stages of high-fidelity amplifiers. These new plastic power-transistors differ from each other in voltage ratings and in the currents at which the parameters are controlled.

• RCA31-series types are described in RCA data bulletin File No. 585.

MAXIMUM RATINGS, Absolute-Maximum Values:

COLLECTOR-TO-BASE VOLTAGE V_{CBO}
 COLLECTOR-TO-EMITTER VOLTAGE:

With base open V_{CEO}

EMITTER-TO-BASE VOLTAGE V_{EBO}

* CONTINUOUS COLLECTOR CURRENT I_C

CONTINUOUS BASE CURRENT I_B

TRANSISTOR DISSIPATION: P_T

At case temperatures up to 25°C

At ambient temperatures up to 25°C

At case temperatures above 25°C

At ambient temperatures above 25°C Derate linearly

TEMPERATURE RANGE:

Storage and Operating (Junction)

TERMINAL TEMPERATURE (During Soldering):

At distance 1/8 in. (3.17 mm) from case for 10 s max.

RCA31/SDH	RCA31A/SDH	RCA31B/SDH	RCA31C/SDH	
40	60	80	100	V
40	60	80	100	V
5	5	5	5	V
4	4	4	2.5	A
1	1	1	1	A
36	36	36	50	W
1.8	1.8	1.8	1.8	W
See Fig. 2				
0.0144				W/°C
-65 to 150				°C
235				°C

*Differs from RCA31 Series.

ELECTRICAL CHARACTERISTICS, At Case Temperature (T_C) = 25°C

CHARACTERISTIC	SYMBOL	TEST CONDITIONS					LIMITS				UNITS
		DC VOLTAGE (V)			DC CURRENT (A)		RCA31/SDH		RCA31A/SDH		
		V _{CE}	V _{EB}	V _{BE}	I _C	I _B	MIN.	MAX.	MIN.	MAX.	
Collector-Cutoff Current: With base open	I _{CEO}	30				0	—	0.3	—	0.3	mA
With base-emitter junction short-circuited	I _{CES}	40 60		0 0			— —	0.2 —	— —	— 0.2	
Emitter-Cutoff Current	I _{EBO}		5		0		—	1	—	1	mA
Collector-to-Emitter Breakdown Voltage: With base open	V _{BR(CEO)}				0.03 ^a	0	40	—	60	—	V
DC Forward-Current Transfer Ratio	h _{FE}	4 4			1 ^a 3 ^a		25 10	— —	25 10	— —	
Base-to-Emitter Voltage	V _{BE}	4			3 ^a		—	1.8	—	1.8	V
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}				3 ^a	0.375	—	1.2	—	1.2	V
Common-Emitter, Small-Signal, Short, Circuit, Forward- Current Transfer Ratio (f = 1 kHz)	h _{fe}	10			0.5		20	—	20	—	
Magnitude of Common- Emitter, Small-Signal, Short-Circuit, For- ward-Current Transfer Ratio (f = 1 MHz)	h _{fe}	10			0.5		0.8	—	0.8	—	
Unclamped Inductive Load Energy ^b (L = 20 mH) See Fig. 8		(V _{CC}) 10					—	32	—	32	mJ
Saturated Switching Time: (R _L = 30 Ω) See Figs. 10 and 11 Turn-on-time (t _d + t _r)	t _{on}	(V _{CC}) 30			1	0.1 ^c	2.3 (typ.)	5	2.3 (typ.)	5	μs
Turn-off time (t _s + t _f)	t _{off}	(V _{CC}) 30			1	0.1 ^c	6 (typ.)	15	6 (typ.)	15	
Thermal Resistance Junction-to-Case	R _{θJC}						—	3.5	—	3.5	°C/W
Junction-to-Ambient	R _{θJA}						—	70	—	70	

*Differs from RCA31 Series.

•Pulsed: Pulse duration = 300 μs, duty factor = 2%.

^bBased upon ability of device to perform in circuit shown in Fig. 8.^cI_{B1} = I_{B2} = value shown.

ELECTRICAL CHARACTERISTICS, At Case Temperature (T_C) = 25°C (cont'd)

CHARACTERISTIC	SYMBOL	TEST CONDITIONS					LIMITS				UNITS
		DC VOLTAGE (V)			DC CURRENT (A)		RCA31B/SDH		RCA31C/SDH		
		V _{CE}	V _{EB}	V _{BE}	I _C	I _B	MIN.	MAX.	MIN.	MAX.	
Collector-Cutoff Current: With base open	I _{CEO}	60				0	—	0.3	—	0.3	mA
With base-emitter junction short-circuited	I _{CES}	80 100		0 0			— —	0.2 —	— —	— 0.2	
Emitter-Cutoff Current	I _{EBO}		5		0		—	1	—	1	mA
Collector-to-Emitter Breakdown Voltage: With base open	V _{BR(CEO)}				0.03 ^a	0	80	—	100	—	V
DC Forward-Current Transfer Ratio	h _{FE}	4 4			1 ^a 3 ^a		25 10	— —	25 10	— —	
Base-to-Emitter Voltage	V _{BE}	4			3 ^a		—	1.8	—	1.8	V
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}				3 ^a	0.375	—	1.2	—	1.2	V
Common-Emitter, Small-Signal, Short-Circuit, Forward-Current Transfer Ratio (f = 1 kHz)	h _{fe}	10			0.5		20	—	20	—	
Magnitude of Common-Emitter, Small-Signal, Short-Circuit, Forward-Current Transfer Ratio (f = 1 MHz)	h _{fe}	10			0.5		0.8	—	0.8	—	
Unclamped Inductive Load Energy ^b (L = 20 mH) See Fig. 8		(V _{CC}) 10					—	32	—	32	mJ
Saturated Switching Time: (R _L = 30 Ω) See Figs. 10 and 11 Turn-on-time (t _d + t _r)	t _{on}	(V _{CC}) 30			1	0.1 ^c	2.3 (typ.)	5	2.3 (typ.)	5	μs
Turn-off time (t _s + t _f)	t _{off}	(V _{CC}) 30			1	0.1 ^c	6 (typ.)	15	6 (typ.)	15	
Thermal Resistance Junction-to-Case	R _{θJC}						—	3.5	—	2.5	°C/W
Junction-to-Ambient	R _{θJA}						—	70	—	70	

*Differs from RCA31 Series.

^aPulsed: Pulse duration = 300 μs, duty factor = 2%.^bBased upon ability of device to perform in circuit shown in Fig. 8.^cI_{B1} = I_{B2} = value shown.

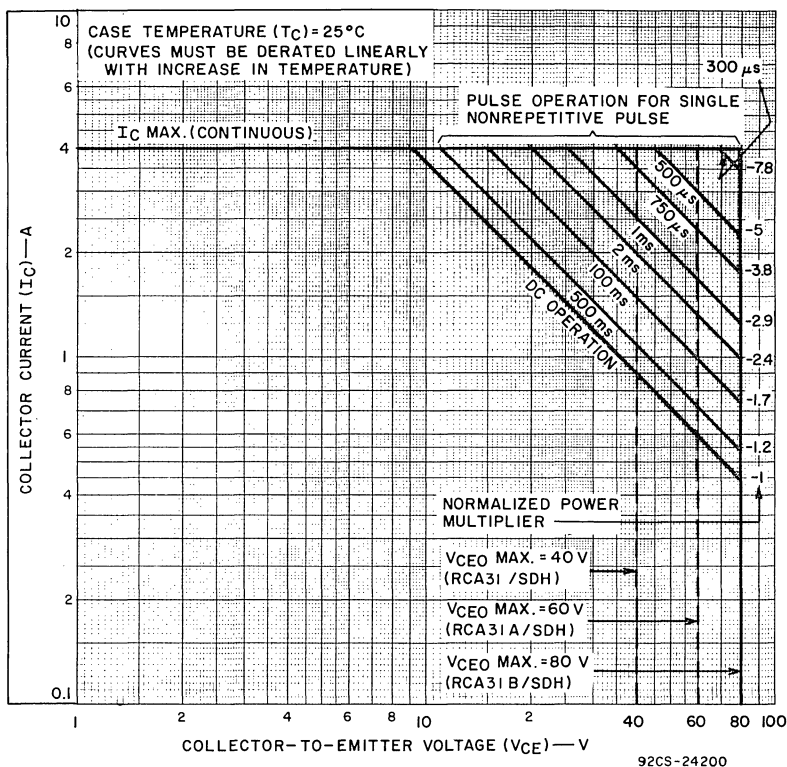


Fig. 1 — Mounting operating areas for RCA31/SDH, RCA31A/SDH, and RCA31B/SDH.

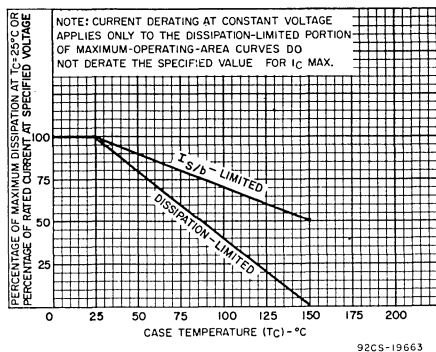
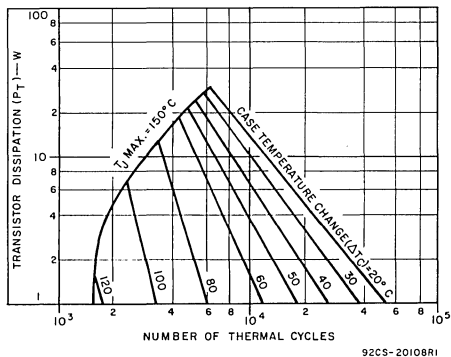
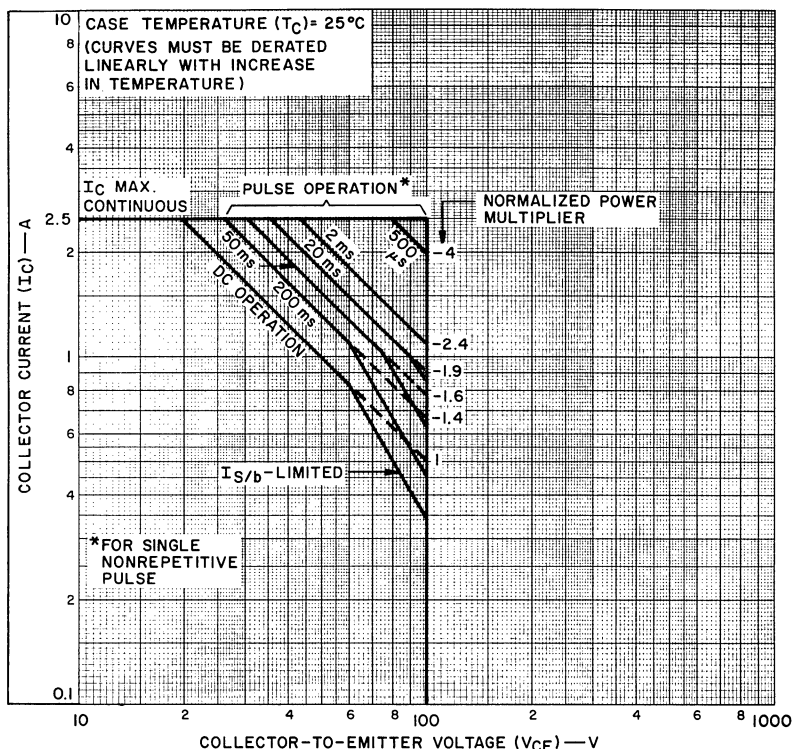
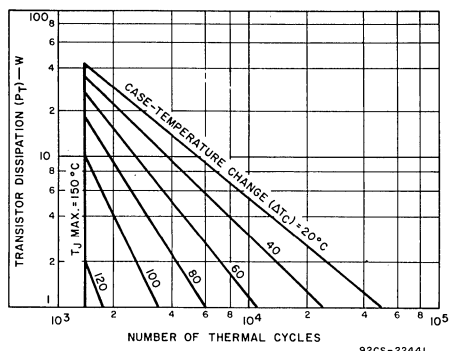
Fig. 2 — Dissipation and $I_{S/b}$ derating curves for all types.

Fig. 3 — Thermal-cycling rating chart for RCA31/SDH, RCA31A/SDH, and RCA31B/SDH.



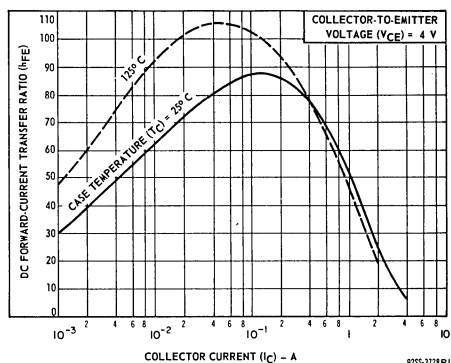
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Fig. 4 — Maximum operating areas for RCA31C/SDH.



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Fig. 5 — Thermal-cycling rating chart for RCA31C/SDH.



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Fig. 6 — Typical dc beta characteristics for RCA31/SDH, RCA31A/SDH, and RCA31B/SDH.

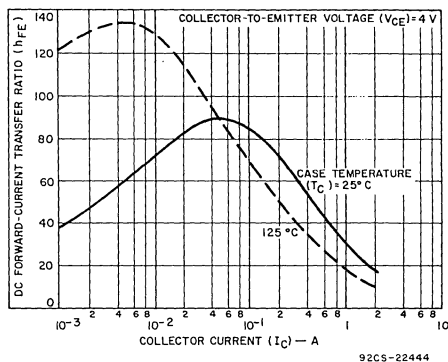
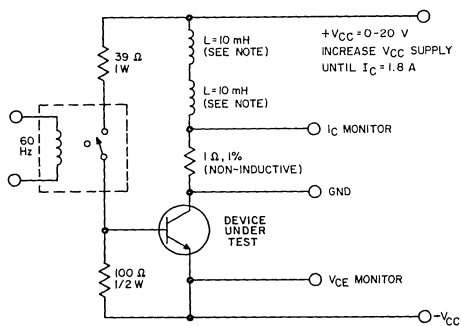


Fig. 7 — Typical dc beta characteristics for RCA31C/SDH.



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Fig. 8 — Circuit for measuring inductive-load switching for all types.

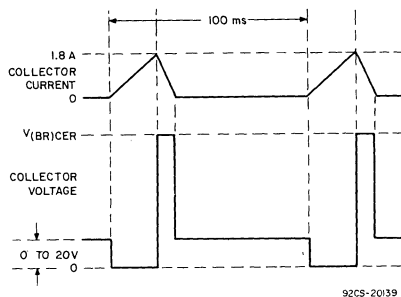
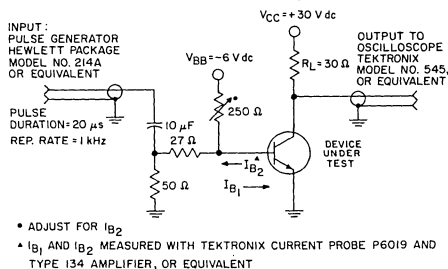


Fig. 9 — Inductive-load voltage and current waveforms (test circuit shown in Fig. 8).



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Fig. 10 — Circuit used to measure switching times for all types.

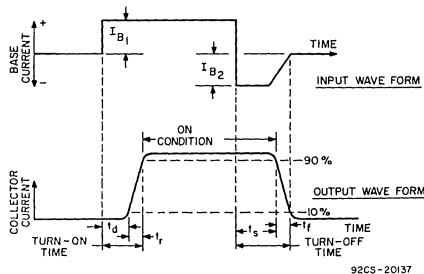


Fig. 11 — Phase relationship between input current and output voltage showing reference points for specification of switching times (test circuit shown in Fig. 10).