

# PUMX2-Q

NPN/NPN general-purpose double transistor

21 November 2024

**Product data sheet** 

### 1. General description

NPN/NPN general-purpose double transistors in a small SOT363 (SC-88) Surface Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

· General-purpose switching and amplification

### 4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transis	tor		·				·
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	50	V
I <sub>C</sub>	collector current			-	-	150	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 6 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C		120	250	560	

### 5. Pinning information

#### **Table 2. Pinning information** Pin Symbol Description Simplified outline Graphic symbol 1 E1 emitter TR1 C1 B1 C2 2 E2 emitter TR2 3 base TR2 TR1 B2 4 C2 collector TR2 ||1 ||2 ||3 É1 E2 B2 5 base TR1 Β1 006aaa653 TSSOP6 (SOT363) 6 C1 collector TR1



### 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PUMX2-Q		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>			

### 7. Marking

Table 4. Marking codes				
Type number	Marking code[1]			
PUMX2-Q	Z1%			

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or		I			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	7	V
I <sub>C</sub>	collector current			-	150	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	180	mW
Per device			L. L.			
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

### 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transis	tor						
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	694	K/W
Per device							
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

### **10. Characteristics**

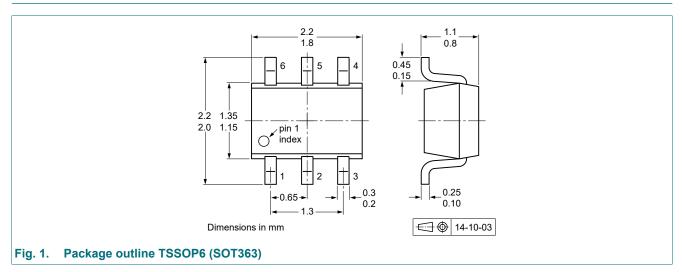
Table 7. Cha	racteristics					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per transist	or		·	·		·
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 60 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 60 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	50	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 7 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 6 \text{ V}; \text{ I}_{C} = 1 \text{ mA}; \text{ T}_{amb} = 25 ^{\circ}\text{C}$	120	250	560	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C}$ = 50 mA; $I_{B}$ = 5 mA; $T_{amb}$ = 25 °C	-	-	250	mV
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 12 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	3	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 12 V; I <sub>C</sub> = 2 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	-	-	MHz

### **11. Test information**

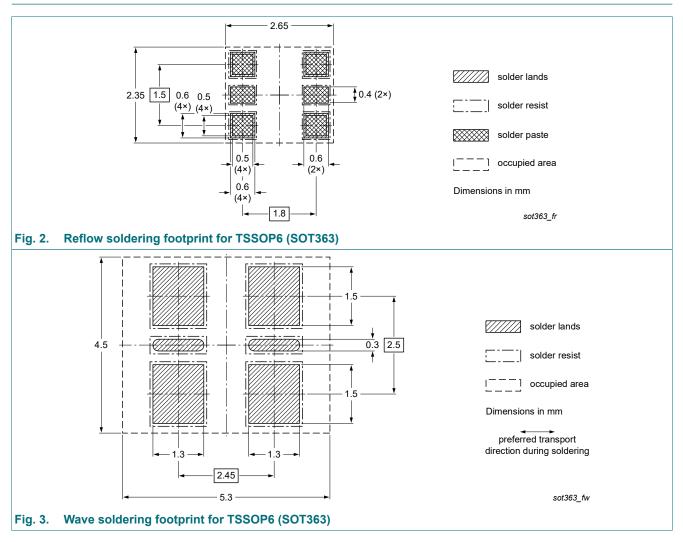
### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 12. Package outline



### 13. Soldering



## 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PUMX2-Q v.1	20241121	Product data sheet	-	-		

# 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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