

Bi-directional 4.5V High Capacitance TVS Protector

Description

The PTVSHC3D4V5B2 transient voltage suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC3D4V5B2 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC3D4V5B2 is available in a SOD-323 package with working voltages of 4.5 volt.



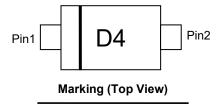
Feature

- \triangleright 2000W Peak pulse power per line (t_P = 8/20 μ s)
- SOD-323 package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD) ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

Pin1 Pin2 Circuit Diagram

Applications

- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP3 players

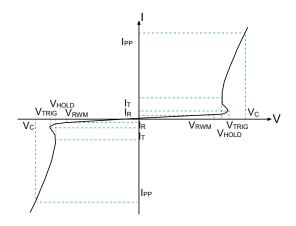


Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260°C
- Pure tin plating: 7 ~ 17 um
- ▶ Pin flatness:≤3mil

Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V_{BR}	Breakdown Voltage @ I⊤		
lτ	Test Current		
I _{PP}	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ I _{PP}		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		



Electrical characteristics per line@25℃(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Zener Voltage	Vz	I _{ZT} = 5mA		5.1		V
Reverse Working Voltage ⁽¹⁾	V_{RWM}				4.5	٧
Breakdown Voltage(Pin1~Pin2)	V_{BR}	I _t =1mA	4.6			V
Reverse Leakage Current(Pin1~Pin2)	I _R	V _{RWM} =4.5V			2	μA
Clamping Voltage(Pin1 to Pin2)	Vc	I _{PP} =20A t _P = 8/20μs		7.5	8.5	V
Clamping Voltage(Pin1~Pin2)	Vc	I _{PP} =45A t _P = 8/20μs		8.8	10	V
Clamping Voltage(Pin1~Pin2)	Vc	I _{PP} =90A t _P = 8/20μs		11	12.5	V
Clamping Voltage(Pin1~Pin2)	Vc	I _{PP} =130A t _P = 8/20μs		14	16	V
Junction Capacitance	Cj	V _R =0V f = 1MHz		310	350	pF

Note 1: V_{RWM} is the maximum reverse working voltage, or reverse stand-off voltage. ESD can protect signal line properly within its rated voltage. If the signal line's voltage is over V_{RWM} , ESD will change to other state.

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _P = 8/20µS)	P _{pp}	2000	W
Total Device Dissipation FR-5 Board	P _D	500	mW
Lead Soldering Temperature	T∟	260 (10 sec)	$^{\circ}$ C
Operating Temperature	TJ	-55 to 125	$^{\circ}$ C
Storage Temperature	T _{STG}	-55 to 150	$^{\circ}$

Typical Characteristics

16

14

12 10

8

6 4

2

Vc-Clamping Voltage (V)

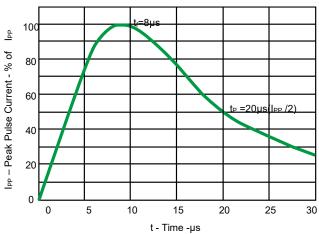


Fig 1.Pulse Waveform

Pulse waveform: tp=8/20us

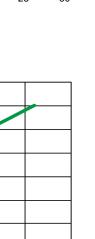


Fig 3. Clamping voltage vs. Peak pulse current

I_{PP}-Peak pulse current (A)

75

100

125

150

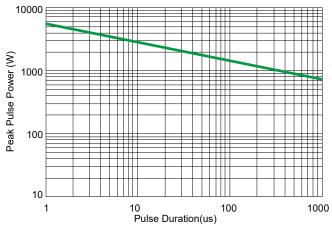


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time

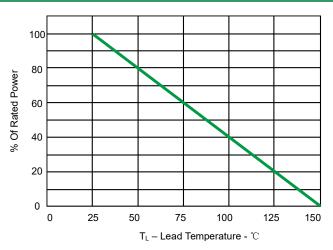


Fig 2.Power Derating Curve

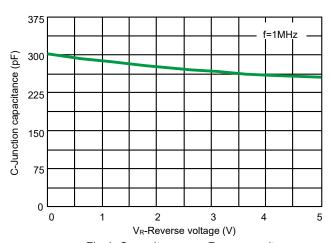
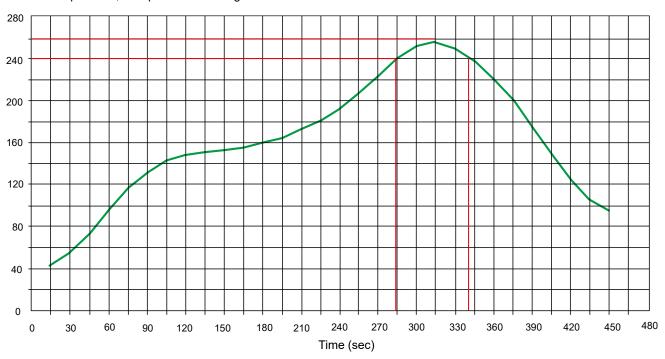


Fig 4. Capacitance vs. Reveres voltage

Solder Reflow Recommendation

Peak Temp=257℃, Ramp Rate=0.802deg. ℃/sec

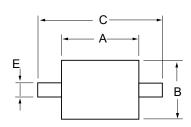


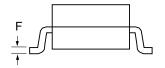
PCB Design

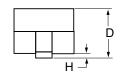
For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

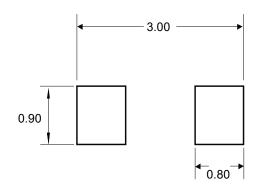
Product dimension (SOD-323)







Dim	Incl	nes	Millimeters		
	MIN	MAX	MIN	MAX	
Α	0.063	0.075	1.60	1.90	
В	0.045	0.057	1.15	1.45	
С	0.090	0.106	2.30	2.70	
D	0.031	0.043	0.80	1.00	
Е	0.010	0.01	0.25	0.40	
F	0.004	0.007	0.09	0.18	
Н	0.000	0.004	0.00	0.10	



Suggested PCB Layout

Unit:mm

Ordering information

Device	Package	Reel	Shipping
PTVSHC3D4V5B2	SOD-323 (Pb-Free)	7"	3000 / Tape & Reel

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