



PSMN3R9-60PS

N-channel 60 V, 3.9 mΩ standard level MOSFET in SOT78

1 February 2013

Product data sheet

1. General description

Standard level N-channel MOSFET in SOT78 using TrenchMOS technology. Product design and manufacture has been optimized for use in battery operated power tools.

2. Features and benefits

- High efficiency due to low switching & conduction losses
- Robust construction for demanding applications
- Standard level gate

3. Applications

- Battery-powered tools
- Load switching
- Motor control
- Uninterruptible power supplies

4. Quick reference data

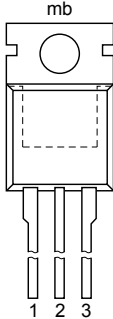
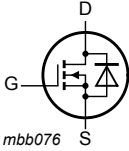
Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	-	60	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; Fig. 1	[1]	-	-	130	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; Fig. 2		-	-	263	W
Static characteristics							
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; Fig. 11		-	2.94	3.9	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 48 V; V _{GS} = 10 V; Fig. 13 ; Fig. 14		-	103	-	nC
Q _{GD}	gate-drain charge			-	33	-	nC
Avalanche ruggedness							
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I _D = 130 A; V _{sup} ≤ 60 V; R _{GS} = 50 Ω; V _{GS} = 10 V; T _{j(init)} = 25 °C; unclamped; Fig. 3		-	-	283	mJ

[1] Continuous current is limited by package.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 <p>TO-220AB (SOT78)</p>	
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN3R9-60PS	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

7. Marking

Table 4. Marking codes

Type number	Marking code
PSMN3R9-60PS	PSMN3R9-60PS

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage	$T_j \geq 25\text{ °C}$; $T_j \leq 175\text{ °C}$		-	60	V
V_{DGR}	drain-gate voltage	$R_{GS} = 20\text{ k}\Omega$		-	60	V
V_{GS}	gate-source voltage			-20	20	V
I_D	drain current	$T_{mb} = 25\text{ °C}$; $V_{GS} = 10\text{ V}$; Fig. 1	[1]	-	130	A
		$T_{mb} = 100\text{ °C}$; $V_{GS} = 10\text{ V}$; Fig. 1		-	127	A
I_{DM}	peak drain current	$T_{mb} = 25\text{ °C}$; pulsed; $t_p \leq 10\text{ }\mu\text{s}$; Fig. 4		-	705	A

Symbol	Parameter	Conditions		Min	Max	Unit
P_{tot}	total power dissipation	$T_{\text{mb}} = 25\text{ °C}$; Fig. 2		-	263	W
T_{stg}	storage temperature			-55	175	°C
T_{j}	junction temperature			-55	175	°C
$T_{\text{sld(M)}}$	peak soldering temperature			-	260	°C
Source-drain diode						
I_{S}	source current	$T_{\text{mb}} = 25\text{ °C}$	[1]	-	130	A
I_{SM}	peak source current	pulsed; $t_{\text{p}} \leq 10\text{ }\mu\text{s}$; $T_{\text{mb}} = 25\text{ °C}$		-	705	A
Avalanche ruggedness						
$E_{\text{DS(AL)S}}$	non-repetitive drain-source avalanche energy	$I_{\text{D}} = 130\text{ A}$; $V_{\text{sup}} \leq 60\text{ V}$; $R_{\text{GS}} = 50\text{ }\Omega$; $V_{\text{GS}} = 10\text{ V}$; $T_{\text{j(init)}} = 25\text{ °C}$; unclamped; Fig. 3		-	283	mJ

[1] Continuous current is limited by package.

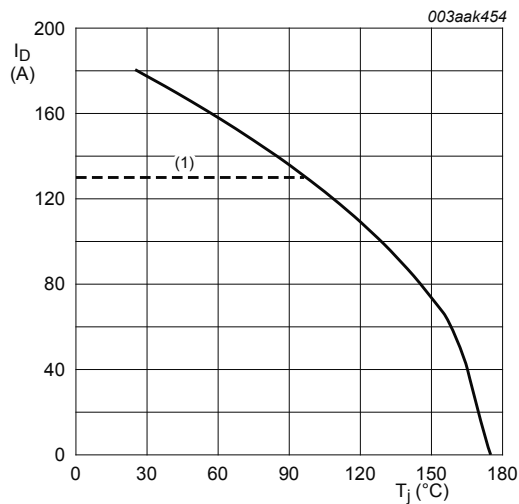


Fig. 1. Continuous drain current as a function of mounting base temperature

$$V_{\text{GS}} \geq 10\text{ V}$$

(1) Capped at 130 A due to package.

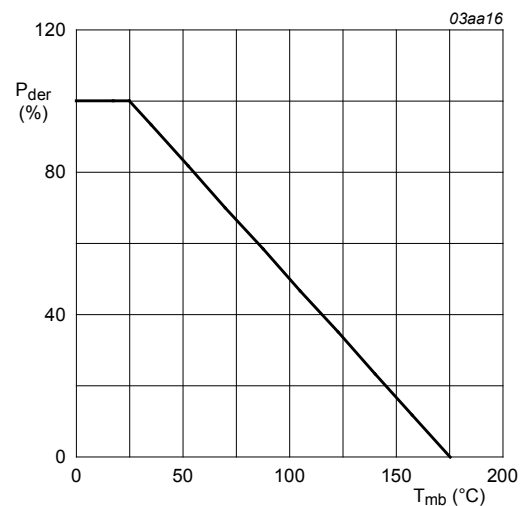


Fig. 2. Normalized total power dissipation as a function of mounting base temperature

$$P_{\text{der}} = \frac{P_{\text{tot}}}{P_{\text{tot}(25\text{ °C})}} \times 100\%$$

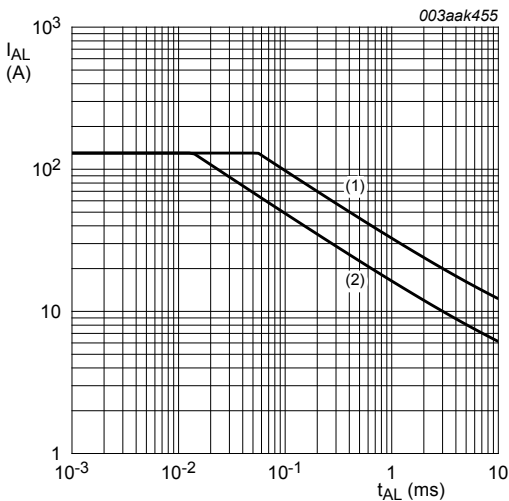


Fig. 3. Single pulse avalanche rating; avalanche current as a function of avalanche time

(1) $T_j (int) = 25^\circ C$; (2) $T_j (int) = 100^\circ C$

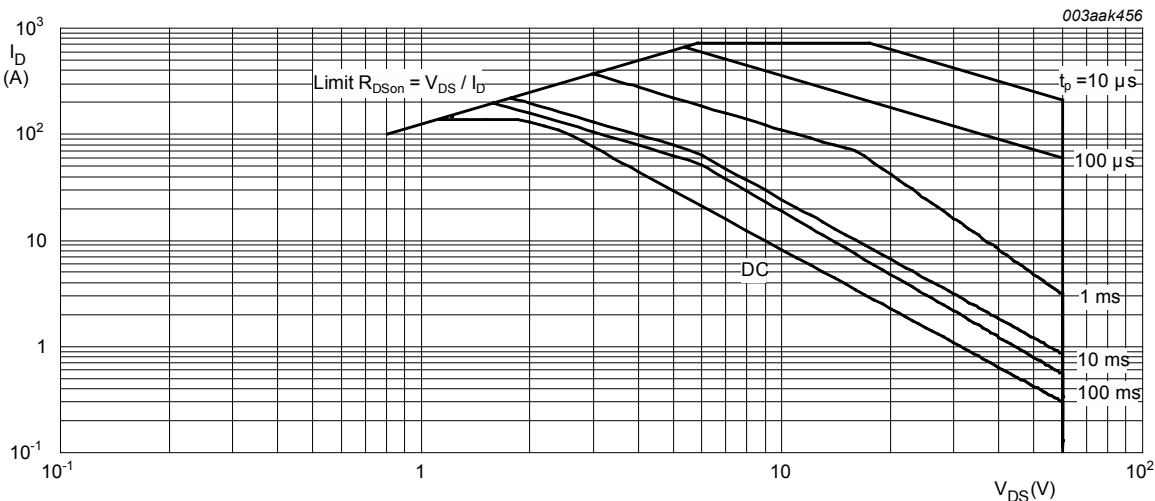


Fig. 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

$T_{mb} = 25^\circ C$; I_{DM} is a single pulse

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 5	-	0.49	0.57	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in still air	-	60	-	K/W

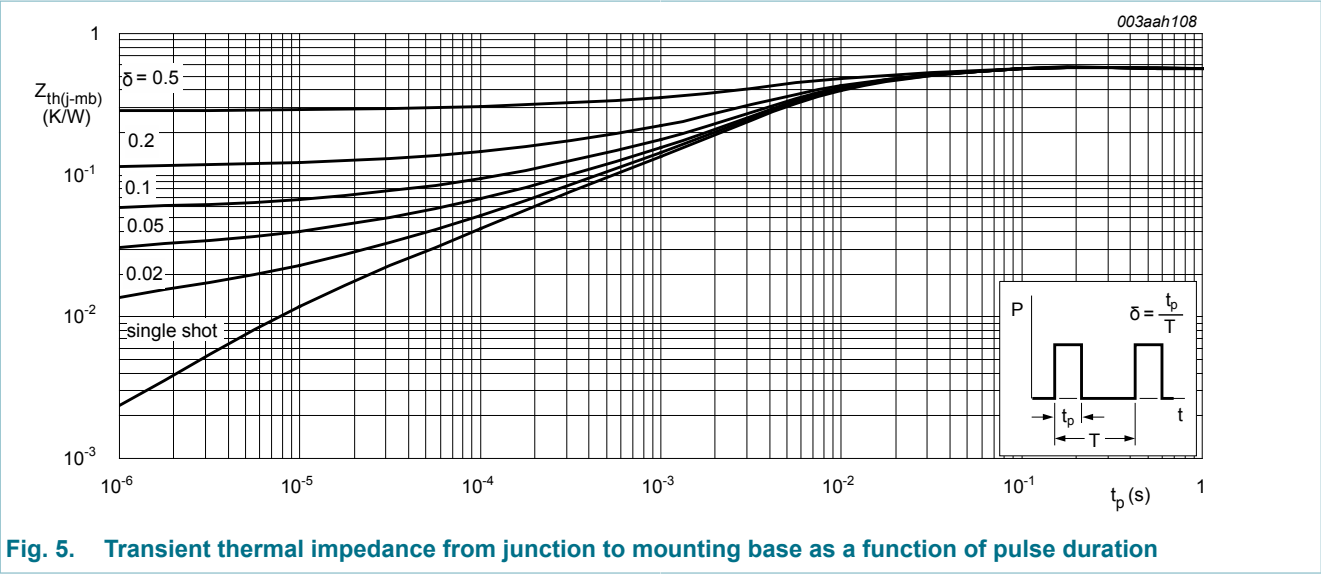


Fig. 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V; T _J = 25 °C	60	-	-	V
		I _D = 250 μA; V _{GS} = 0 V; T _J = -55 °C	54	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 1 mA; V _{DS} = V _{GS} ; T _J = 25 °C; Fig. 9; Fig. 10	2.4	3	4	V
		I _D = 1 mA; V _{DS} = V _{GS} ; T _J = 175 °C; Fig. 9	1	-	-	V
		I _D = 1 mA; V _{DS} = V _{GS} ; T _J = -55 °C; Fig. 9	-	-	4.5	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _J = 175 °C	-	-	500	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _J = 25 °C	-	0.07	1	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _J = 25 °C	-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _J = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _J = 25 °C; Fig. 11	-	2.94	3.9	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _J = 175 °C; Fig. 11; Fig. 12	-	-	8.5	mΩ
R _G	gate resistance	f = 1 MHz	0.35	0.7	1.4	Ω
Dynamic characteristics						
Q _{G(tot)}	total gate charge	I _D = 25 A; V _{DS} = 48 V; V _{GS} = 10 V; Fig. 13; Fig. 14	-	103	-	nC
Q _{GS}	gate-source charge		-	25.1	-	nC

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Q _{GD}	gate-drain charge			-	33	-	nC
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz; T _j = 25 °C; Fig. 15		-	5600	-	pF
C _{oss}	output capacitance			-	740	-	pF
C _{rss}	reverse transfer capacitance			-	460	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 45 V; R _L = 1.8 Ω; V _{GS} = 10 V; R _{G(ext)} = 5 Ω		-	25.3	-	ns
t _r	rise time			-	41.4	-	ns
t _{d(off)}	turn-off delay time			-	62.7	-	ns
t _f	fall time			-	45	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _j = 25 °C; Fig. 16		-	0.8	1.2	V
t _{rr}	reverse recovery time	I _S = 20 A; dI _S /dt = -100 A/μs; V _{GS} = 0 V; V _{DS} = 25 V		-	39	-	ns
Q _r	recovered charge			-	51	-	nC

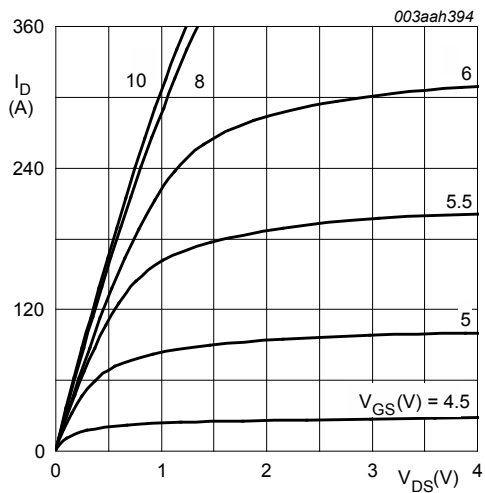


Fig. 6. Output characteristics; drain current as a function of drain-source voltage; typical values

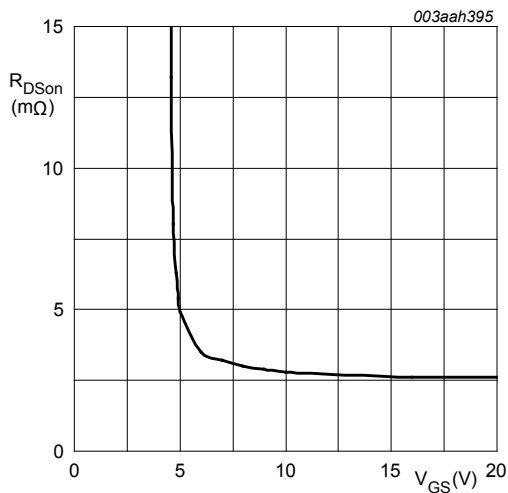


Fig. 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

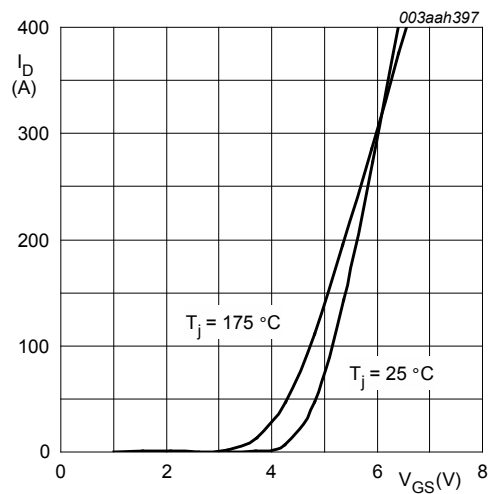


Fig. 8. Transfer characteristics; drain current as a function of gate-source voltage; typical values

$V_{DS} = 10V$

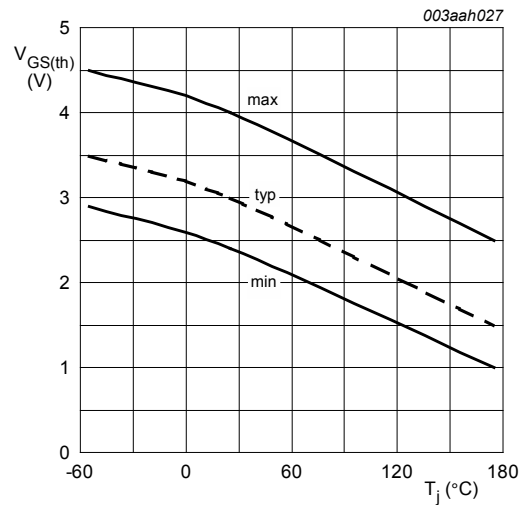


Fig. 9. Gate-source threshold voltage as a function of junction temperature

$I_D = 1\text{ mA}; V_{DS} = V_{GS}$

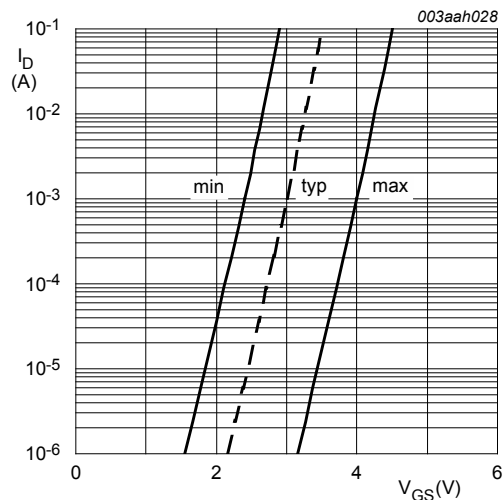


Fig. 10. Sub-threshold drain current as a function of gate-source voltage

$T_j = 25^\circ\text{C}; V_{DS} = 5V$

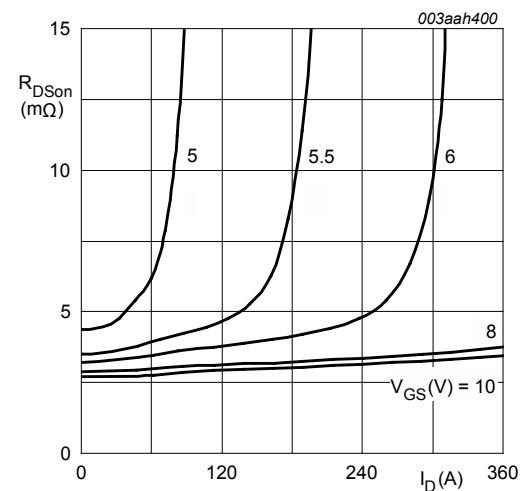


Fig. 11. Drain-source on-state resistance as a function of drain current; typical values

$T_j = 25^\circ\text{C}; t_p = 300\text{ }\mu\text{s}$

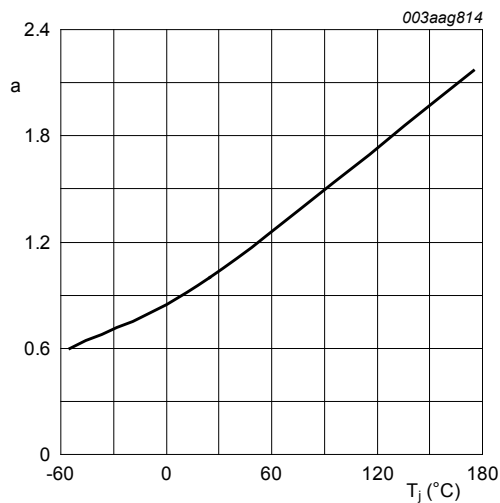


Fig. 12. Normalized drain-source on-state resistance factor as a function of junction temperature

$$a = \frac{R_{DSon}}{R_{DSon(25\text{ °C})}}$$

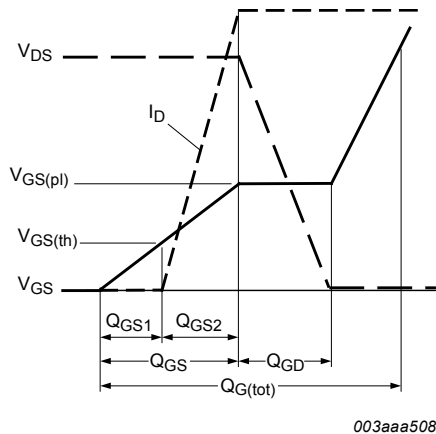


Fig. 13. Gate charge waveform definitions

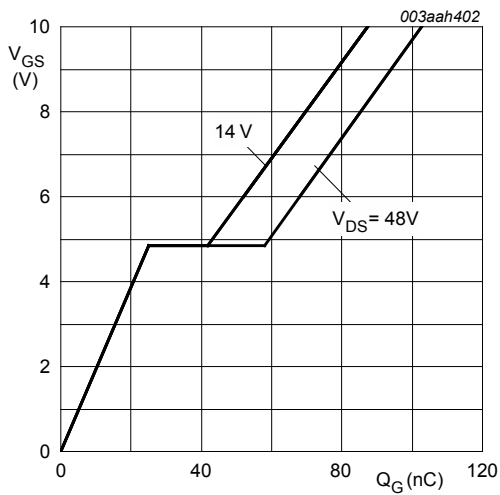


Fig. 14. Gate-source voltage as a function of gate charge; typical values

$$T_j = 25\text{ °C}; I_D = 25\text{ A}$$

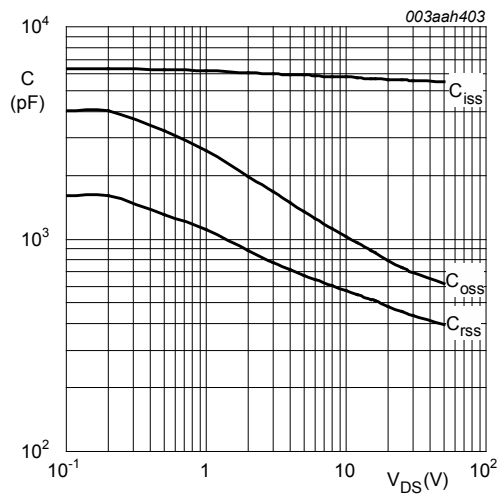


Fig. 15. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

$$V_{GS} = 0\text{ V}; f = 1\text{ MHz}$$

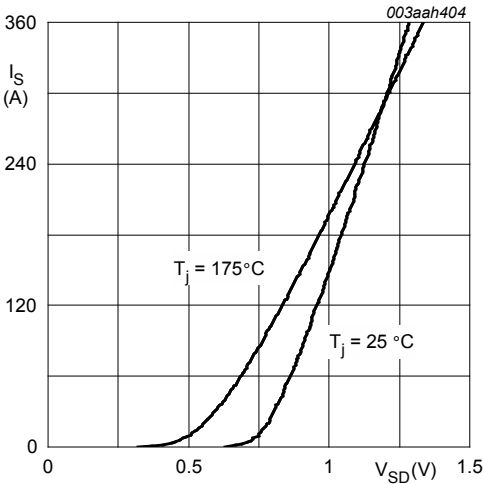


Fig. 16. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

$V_{GS} = 0V$

11. Package outline

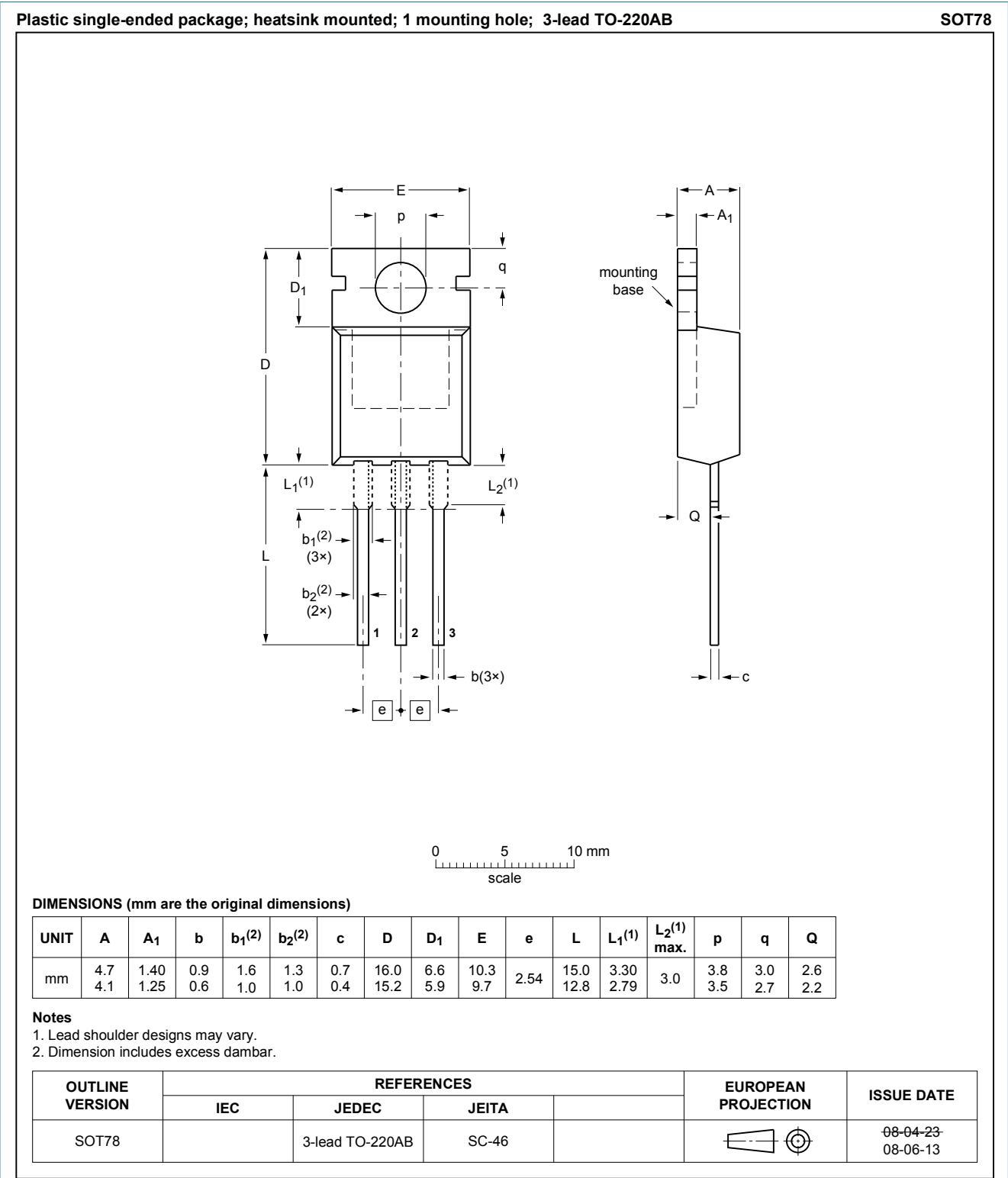


Fig. 17. Package outline TO-220AB (SOT78)

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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