

650 V, 10 A SiC Schottky diode in D2PAK R2P for automotive applications

7 March 2025

Product data sheet

1. General description

Nexperia introduces leading edge Silicon Carbide (SiC) Schottky diode for ultra-high performance, low loss, high efficiency power conversion applications. The SiC Schottky diode encapsulated in a Real-2-Pin D2PAK R2P (TO-263-2) Surface-Mounted Device (SMD) power plastic package offers temperature independent capacitive turn-off, zero recovery switching behavior combined with an outstanding figure-of-merit ($Q_C \times V_F$). The Merged PiN Schottky (MPS) diode improves the robustness expressed in a high I_{FSM} .

2. Features and benefits

- · Reduced system costs
- · Temperature independent fast and smooth switching performance
- Outstanding figure-of-merit (Q_c x V_F)
- High I_{FSM} capability
- · High power density
- · System miniaturization
- Reduced EMI
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Traction inverter
- DC-DC converter
- · Onboard charger

4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------|-------------------------|--|-----|-----|-----|------|
| V_{DC} | DC blocking voltage | | 650 | - | - | V |
| I _F | forward current | T _c ≤ 122 °C; δ = 1 | - | - | 10 | Α |
| Q _C | total capacitive charge | $V_R = 400 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 10 \text{ A}; $ $T_j = 25 ^{\circ}\text{C}$ | - | 22 | - | nC |



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5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------------------------------|---------------------|----------------|
| 1 | K | cathode | mb | |
| 2 | A | anode | | |
| mb | К | mounting base; connected to cathode | D2PAK R2P (SOT8018) | K K; mb |

6. Ordering information

Table 3. Ordering information

| Type number Package | | | | | |
|---------------------|-----------|--|---------|--|--|
| | Name | Description | Version | | |
| PSC1065J-Q | D2PAK R2P | Plastic, single-ended surface-mounted package (D2PAK R2P); Real-2-Pin configuration; 5.08 mm pitch; 8.8 mm x 10.35 mm x 4.46 mm body | SOT8018 | | |

7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PSC1065J-Q | PSC1065JQ |

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|--------------------|---------------------------------|---|-----|-----|------|
| V_{RRM} | repetitive peak reverse voltage | | - | 650 | V |
| dv/dt | diode dv/dt ruggedness | 0 V ≤ V _R ≤ 480 V | - | 100 | V/ns |
| I _F | forward current | T _c ≤ 122 °C; δ = 1 | - | 10 | Α |
| I _{FSM} | non-repetitive peak | t _p = 10 μs; square wave; T _c = 25 °C | - | 440 | Α |
| | forward current | t _p = 10 ms; half sine-wave; T _c = 25 °C | - | 52 | Α |
| | | t _p = 10 ms; half sine-wave; T _c = 150 °C | - | 42 | Α |
| ∫i ² dt | i ² t value | t _p = 10 ms; T _c = 25 °C | - | 14 | A²s |
| | | t _p = 10 ms; T _c = 150 °C | - | 9 | A²s |
| P _{tot} | total power dissipation | T _c ≤ 25 °C | - | 60 | W |
| T _j | junction temperature | | - | 175 | °C |
| T _{amb} | ambient temperature | | -55 | 175 | °C |
| T _{stg} | storage temperature | | -65 | 175 | °C |

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9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|---------|--|------------|-----|-----|-----|------|
| 11(1-0) | thermal resistance from junction to case | | - | 1.9 | 2.5 | K/W |

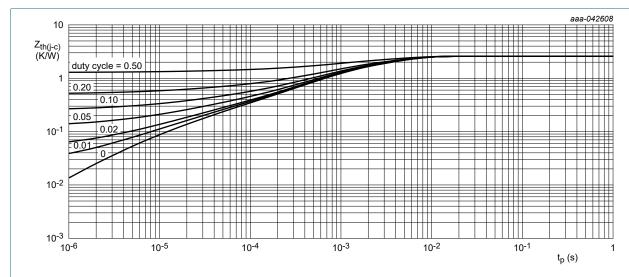


Fig. 1. Transient thermal impedance as a function of pulse duration; maximum values

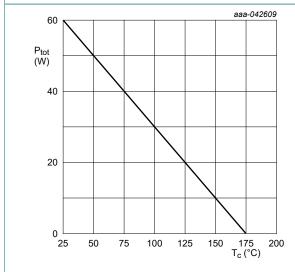


Fig. 2. Power dissipation; maximum values

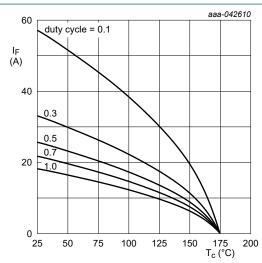


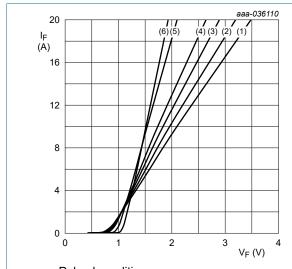
Fig. 3. Forward current as a function of case temperature; maximum values

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10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------|-------------------------|---|-----|------|-----|------|
| V_{DC} | DC blocking voltage | | 650 | - | - | V |
| V _F | forward voltage | I _F = 10 A; T _j = 25 °C | - | 1.5 | 1.8 | V |
| | | I _F = 10 A; T _j = 150 °C | - | 1.95 | 2.6 | V |
| I _R | reverse current | V _R = 650 V; T _j = 25 °C | - | 1 | 60 | μΑ |
| | | V _R = 650 V; T _j = 150 °C | - | 10 | 120 | μΑ |
| C _d | diode capacitance | V _R = 1 V; f = 1 MHz; T _j = 25 °C | - | 340 | - | pF |
| | | V _R = 400 V; f = 1 MHz; T _j = 25 °C | - | 36 | - | pF |
| Q _C | total capacitive charge | $V_R = 400 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 10 \text{ A}; $ $T_j = 25 \text{ °C}$ | - | 22 | - | nC |





(1) $T_j = 175 \,^{\circ}\text{C}$

 $(2) T_i = 150 °C$

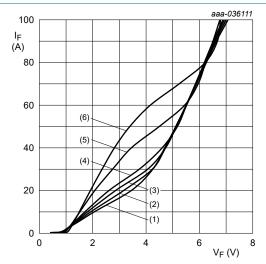
(3) $T_i = 125 \,^{\circ}\text{C}$

 $(4) T_j = 100 °C$

(5) $T_i = 25 °C$

 $(6) T_i = -55 ^{\circ}C$

Fig. 4. Forward current as a function of forward voltage; typical values



Pulsed condition:

(1) $T_j = 175 \, ^{\circ}C$

(2) $T_i = 150 °C$

(3) $T_i = 125 \,^{\circ}\text{C}$

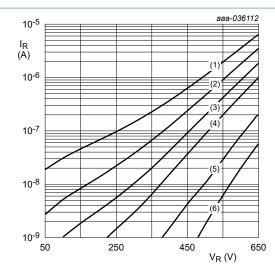
 $(4) T_j = 100 °C$

(5) $T_j = 25$ °C

(6) $T_i = -55 \,^{\circ}\text{C}$

Fig. 5. Forward characteristics in surge current as a function of forward voltage; typical values

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Pulsed condition:

(1) $T_j = 175 \, ^{\circ}C$

(2) $T_j = 150 \,^{\circ}\text{C}$ (3) $T_j = 125 \,^{\circ}\text{C}$ (4) $T_j = 100 \,^{\circ}\text{C}$

(5) $T_i = 25 °C$

(6) $T_j = -55 \,^{\circ}\text{C}$

Fig. 6. Reverse current as a function of reverse voltage; typical values

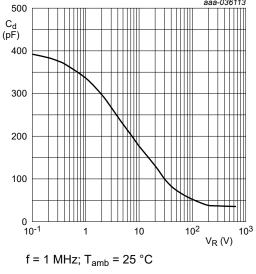


Fig. 7. Diode capacitance as a function of reverse voltage; typical values

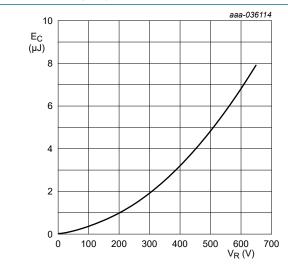
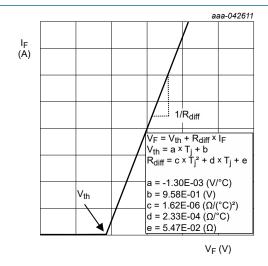


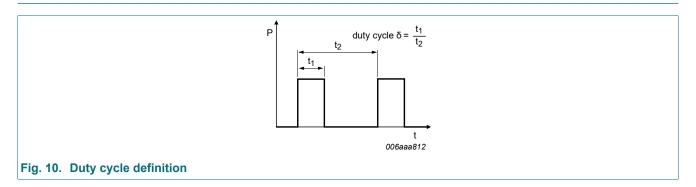
Fig. 8. Capacitance stored energy as a function of reverse voltage; typical values



Simplified forward characteristics mode

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11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline

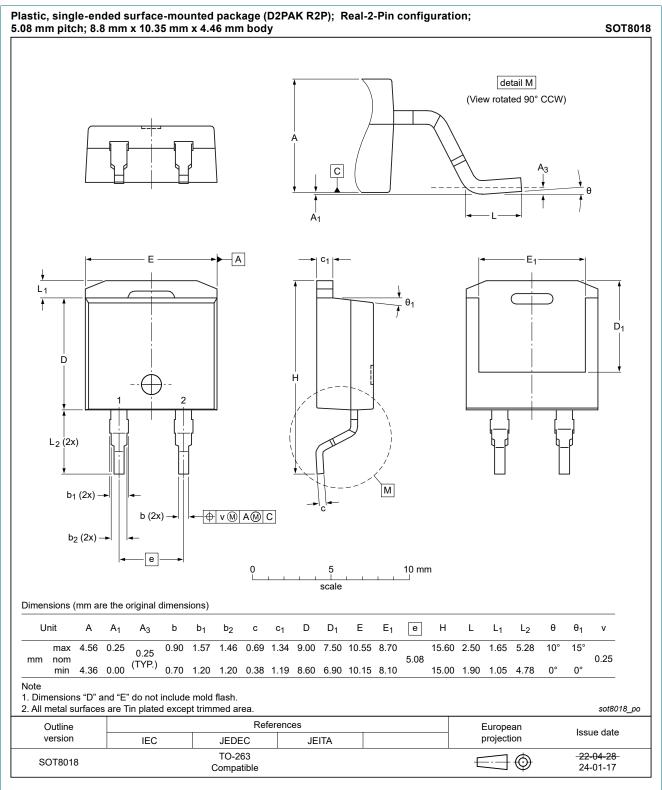
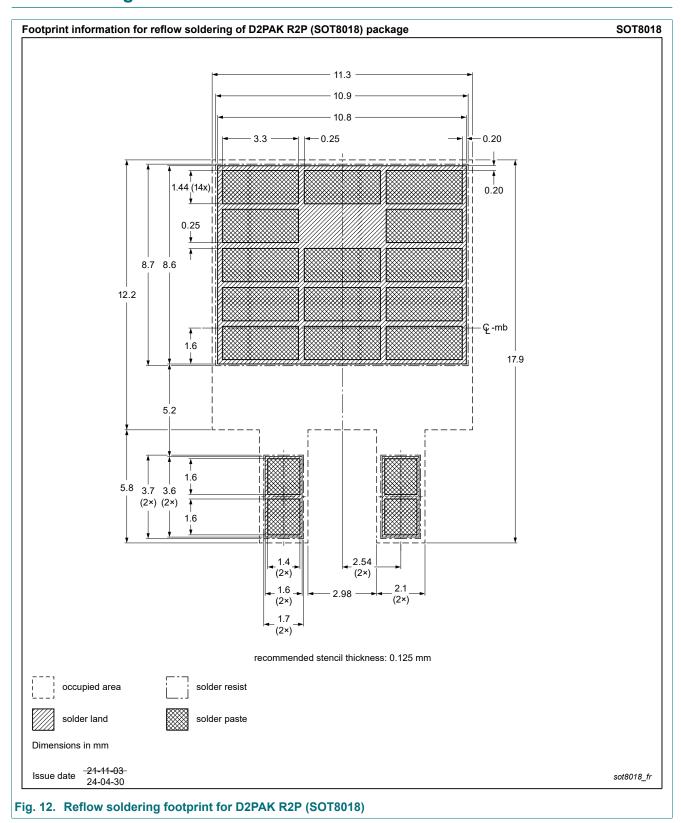


Fig. 11. Package outline D2PAK R2P (SOT8018)

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13. Soldering



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14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|----------------|--------------|--------------------|---------------|------------|
| PSC1065J-Q v.1 | 20250307 | Product data sheet | - | - |

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15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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PSC1065J-Q

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