

PSC1065B1-Q

650 V, 10 A SiC Schottky diode in bare die for automotive applications

14 June 2024

Product data sheet

1. General description

Nexperia introduces leading edge Silicon Carbide (SiC) Schottky diode for ultra-high performance, low loss, high efficiency power conversion applications. The Merged PiN Schottky (MPS) diode delivered as bare die in Tape and Reel (T & R) offers temperature independent capacitive turnoff, zero recovery switching behavior combined with an outstanding figure-of-merit ($Q_C \times V_F$) and improves the robustness expressed in a high I_{FSM} .

2. Features and benefits

- Zero forward and reverse recovery
- · Temperature independent fast and smooth switching performance
- Outstanding figure-of-merit (Q_c x V_F)
- High I_{FSM} capability
- · High power density
- · Reduced system costs
- System miniaturization
- Reduced EMI
- Qualified according to AEC-Q101 in SMD package with epoxy mold compound and recommended for use in automotive applications

3. Applications

- Traction inverter
- DC-DC converter
- Onboard charger

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DC}	DC blocking voltage		[1]	650	-	-	V
l _F	forward current	δ = 1; T _c ≤ 122 °C	[2]	-	-	10	Α
Q_C	total capacitive charge	$V_R = 400 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 10 \text{ A}; $ $T_j = 25 ^{\circ}\text{C}$	[2]	-	22	-	nC

- [1] Parameters 100% tested.
- [2] Validation performed on DPAK with mold compound.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode (back side)		
2	A	anode (top side)	Transparent top view PSC1065B1-Q	A ─├∑ K aaa-0038726

6. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PSC1065B1-Q	PSC1065B1-Q	Bare die product; 1.45 mm × 1.45 mm × 0.11 mm die size	PSC1065B1-Q				

7. Marking

Table 4. Marking codes

Type number	Marking code
PSC1065B1-Q	PSC1065B1-Q

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{RRM}	repetitive peak reverse voltage			-	650	V
dv/dt	diode dv/dt ruggedness	$0 \le V_R \le 480 \text{ V}$		-	100	V/ns
l _F	forward current	δ = 1; T _c ≤ 122 °C	[1]	-	10	Α
I _{FSM}	non-repetitive peak forward current	t _p = 10 μs; square wave; T _c = 25 °C	[1]	-	440	Α
		t _p = 10 ms; half sine-wave; T _c = 25 °C	[1]	-	52	Α
		t _p = 10 ms; half sine-wave; T _c = 150 °C	[1]	-	42	Α
∫i ² dt	i ² t value	$t_p = 10 \text{ ms}; T_c = 25 \text{ °C}$	[1]	-	14	A²s
		t _p = 10 ms; T _c = 150 °C	[1]	-	9	A²s
P _{tot}	total power dissipation	T _c = 25 °C	[1]	-	58	W
T _j	junction temperature		[1]	-	175	°C
T _{amb}	ambient temperature		[1]	-55	175	°C
T _{stg}	storage temperature		[1]	-65	175	°C

^[1] Validation performed on DPAK with mold compound.

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case		[1]	-	2	2.6	K/W

[1] Validation performed on DPAK with mold compound.

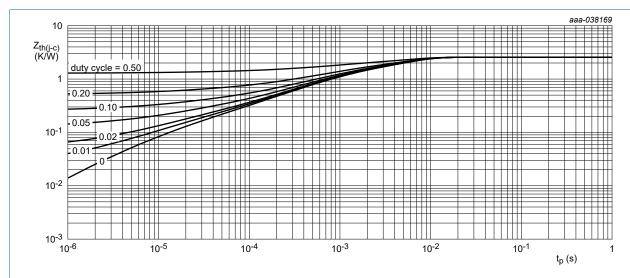


Fig. 1. Transient thermal impedance as a function of pulse duration; maximum values

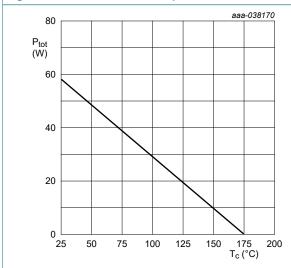


Fig. 2. Power dissipation; maximum values

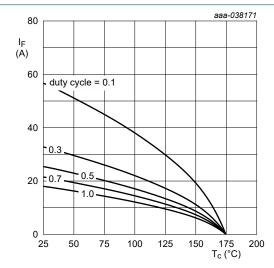


Fig. 3. Forward current as a function of case temperature; maximum values

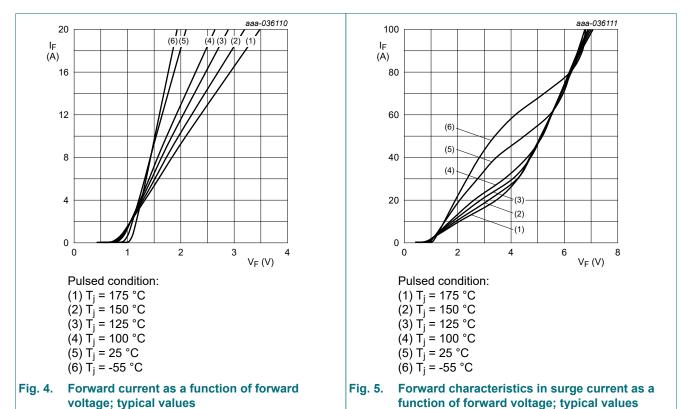
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10. Characteristics

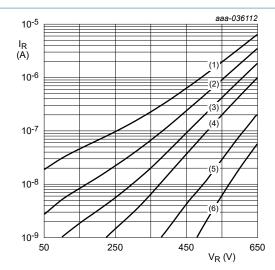
Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{DC}	DC blocking voltage		[1]	650	-	-	V
V _F	forward voltage	I _F = 10 A; T _j = 25 °C	[1]	-	1.5	1.8	V
		I _F = 10 A; T _j = 150 °C	[2]	-	1.95	2.6	V
I _R	reverse current	V _R = 650 V; T _j = 25 °C	[1]	-	1	60	μA
		V _R = 650 V; T _j = 150 °C	[2]	-	10	120	μA
C _d	diode capacitance	f = 1 MHz; V _R = 1 V; T _j = 25 °C	[2]	-	340	-	pF
		f = 1 MHz; V _R = 400 V; T _j = 25 °C	[2]	-	36	-	pF
Q _C	total capacitive charge	$V_R = 400 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 10 \text{ A}; $ $T_j = 25 ^{\circ}\text{C}$	[2]	-	22	-	nC

- [1] Parameters 100% tested.
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Pulsed condition:

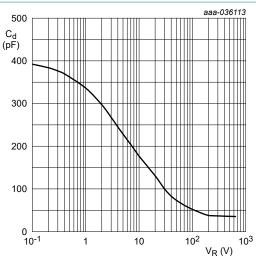
(1) $T_j = 175 \, ^{\circ}C$

(2) $T_j = 150 \,^{\circ}\text{C}$ (3) $T_j = 125 \,^{\circ}\text{C}$ (4) $T_j = 100 \,^{\circ}\text{C}$

(5) $T_i = 25 °C$

(6) $T_j = -55 \,^{\circ}\text{C}$

Fig. 6. Reverse current as a function of reverse voltage; typical values



 $f = 1 MHz; T_{amb} = 25 °C$

Fig. 7. Diode capacitance as a function of reverse voltage; typical values

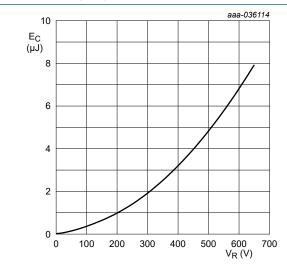
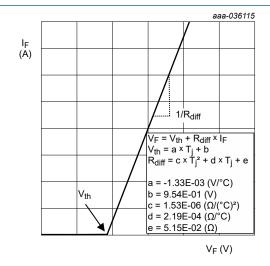


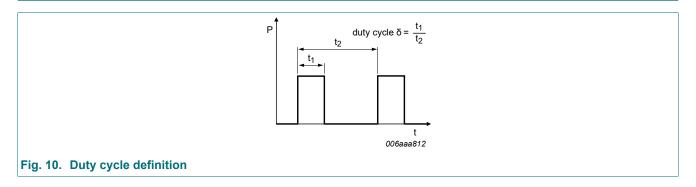
Fig. 8. Capacitance stored energy as a function of reverse voltage; typical values



Simplified forward characteristics mode

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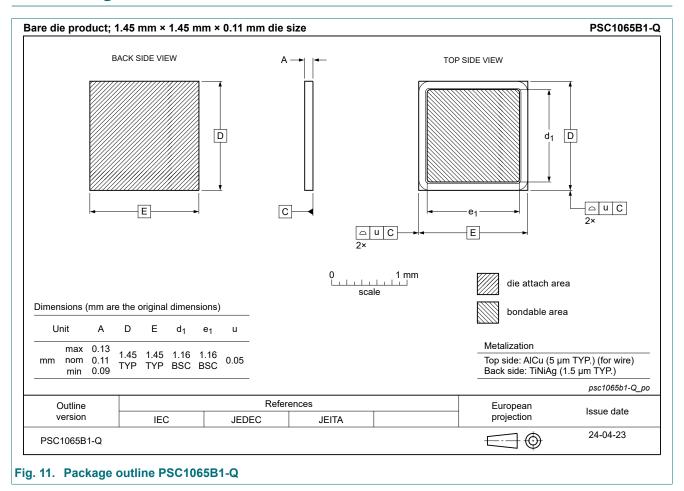
11. Test information



Quality information

Qualified according to AEC-Q101 in SMD package with epoxy mold compound.

12. Package outline



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13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PSC1065B1-Q v.2	20240614	Product data sheet	-	PSC1065B1-Q v.1	
Modifications:	Section "Applications" changedProduct status changed				
PSC1065B1-Q v.1	20240522	Preliminary data sheet	-	-	

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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