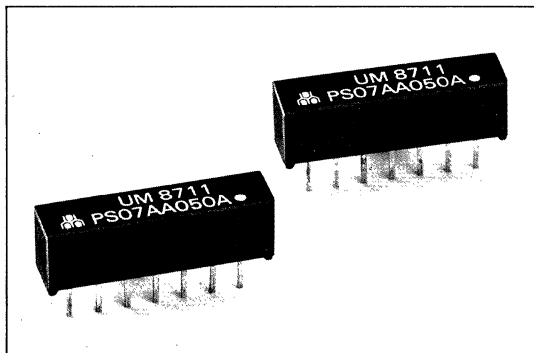
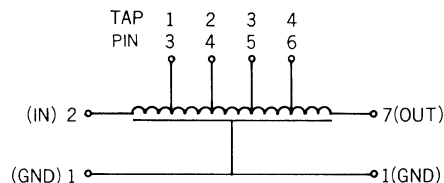
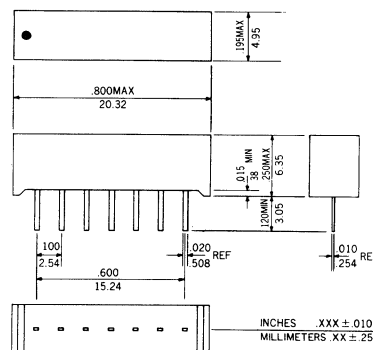


**PS07 SERIES: 7-PIN SIP 5-TAP EQUALLY-SPACED****GENERAL SPECIFICATIONS:**

Total Delay: 10 to 100ns
 Tap Delays: 20% increments
 Characteristic Impedance: 50 to 100 ohms ($\pm 10\%$)
 Distortion: $\pm 10\%$ max.
 Temperature Coefficient of Delay: 200 ppm/ $^{\circ}\text{C}$ max.
 Operating Temperature Range: 0°C to $+70^{\circ}\text{C}$
 Storage Temperature Range: -55°C to $+125^{\circ}\text{C}$
 Insulation Resistance: 1000 megohms min.
 50VDC, 25°C

FEATURES:

- 7-Pin SIP Package
- 5 Equally-Spaced Delay Taps
- Optional Internal Termination
- Other Delays Available upon Request

CIRCUIT CONFIGURATION AND PIN CONNECTIONS:**Circuit A****PACKAGE DIMENSIONS:****Package A****PART NUMBER SYSTEM:**

PS07 A A 100 B

Characteristic Impedance in ohm
 A=50 ohms B=100 ohms

Delay Time in ns
 For examples: 100=100ns
 050=50ns

Circuit Configuration - See schematics Above
 circuit A is standard, other pin connections
 are available upon request

Package Dimensions
 Package A is standard

Basic Series Number
 Passive Delay Line, SIP 7 pins

ELECTRICAL SPECIFICATIONS:

PART NO.	IMPEDANCE $Z_0 \pm 10\%(\Omega)$	TOTAL DELAY TIME $\pm 5\%(ns)$	TAP DELAY (ns)	RISE TIME T_r max. (ns)	ATTENUATION max. (%)
PS07AA010A	50	10	2.0 ± 0.5	4.0	5.0
PS07AA020A	50	20	4.0 ± 2.0	6.0	5.0
PS07AA030A	50	30	6.0 ± 3.0	9.0	5.0
PS07AA040A	50	40	8.0 ± 3.0	12.0	5.0
PS07AA050A	50	50	10.0 ± 3.0	15.0	5.0
PS07AA100A	50	100	20 ± 3.0	30.0	6.0
PS07AA010B	100	10	2.0 ± 0.5	4.0	5.0
PS07AA020B	100	20	4.0 ± 2.0	6.0	5.0
PS07AA030B	100	30	6.0 ± 3.0	9.0	5.0
PS07AA040B	100	40	8.0 ± 3.0	11.0	5.0
PS07AA050B	100	50	10.0 ± 3.0	14.0	5.0
PS07AA100B	100	100	20 ± 3.0	28.0	6.0