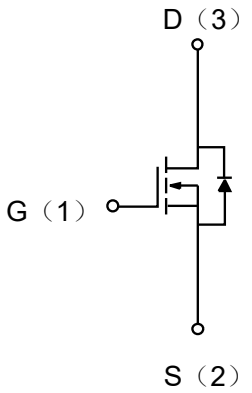
Description

The enhancement mode MOS is extremely high density cell and low on-resistance.

MOSFET Product Summary		
V_{DS}(V)	$R_{DS(on)}$(Ω)	I_D(A)
20	0.3@ V_{GS}=4.5V	0.6



Electrical characteristics per line@25℃(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D =250μA,V_{GS}=0V	20		-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =20V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{DS} =0V,V_{GS}=±10V	-	-	±10	μA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =250μA	0.5		0.85	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=4.5V, I_D =0.6A	-	0.3	0.35	Ω
		V_{GS}=2.5V, I_D =0.5A	-	0.5	0.55	Ω
		V_{GS}=1.8V, I_D =0.35A	-	0.6	0.85	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	V_{GS}=0V, V_{DS} =16V, f=1MHz	-	135		pF
Output Capacitance	C_{DSS}		-	23		pF
Reverse Transfer Capacitance	C_{RSS}		-	18		pF
SWITCHING PARAMETERS						
Turn-On Delay Time	$t_{d(on)}$	V_{DS}=10V, V_{GS} =4.5V, R_G=10Ω, I_D =0.2A	-		15	ns
Turn-Off Delay Time	$t_{d(off)}$		-		55	ns

Absolute maximum rating@25℃

Rating		Symbol	Value	Units
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±12	V
Drain Current	Continuous	I_D	0.6	A
	Pulsed	I_D	3.0	A
Total Power Dissipation	T_A=25℃	P_D	170	mW
	T_A=125℃	P_D	155	mW
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55 to 150	℃

Typical Characteristics

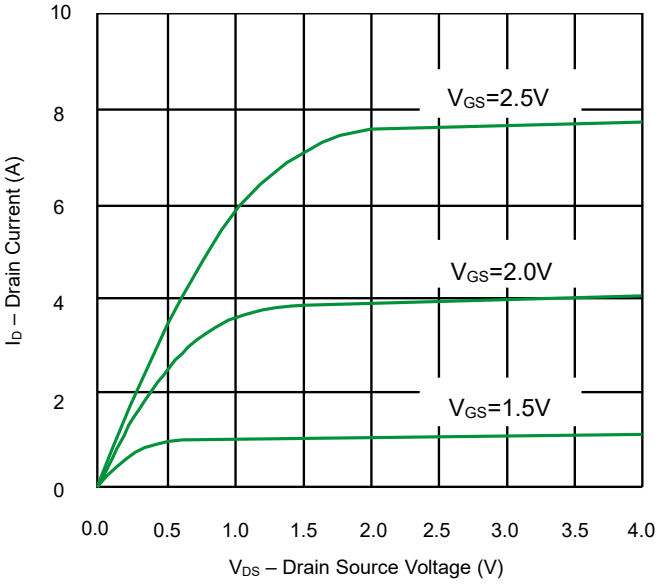


Fig 1. Output Characteristics

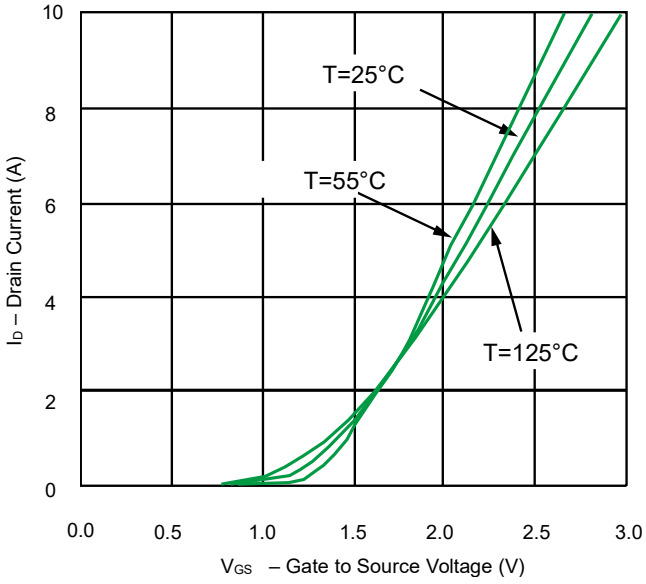


Fig 2. Transfer Characteristics

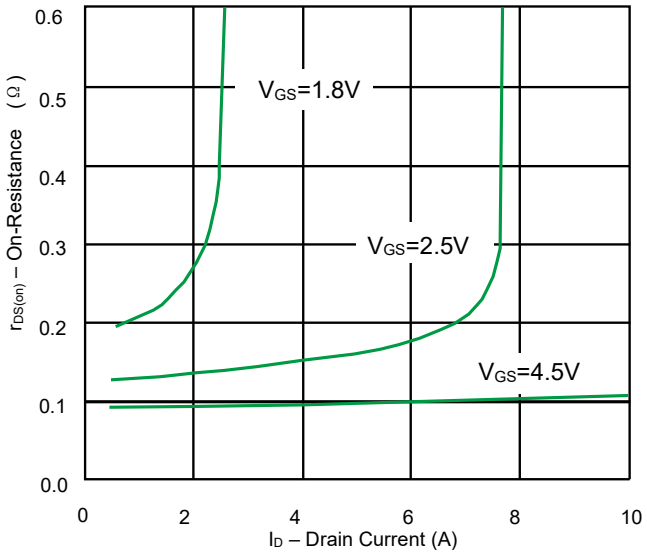


Fig 3. On-Resistance vs. Drain Current

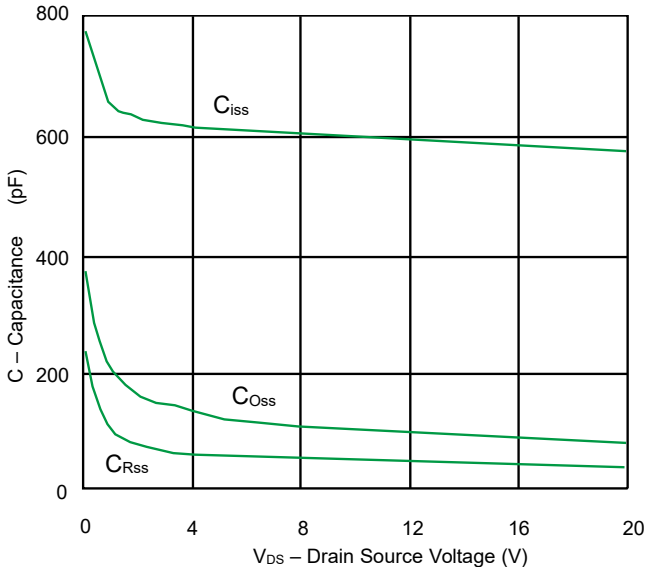
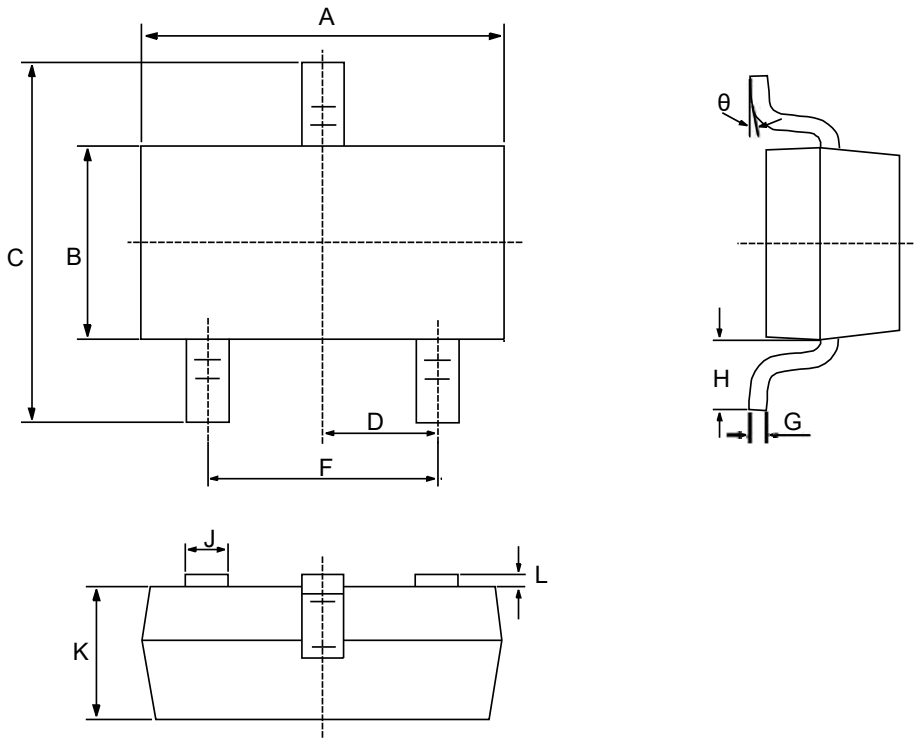
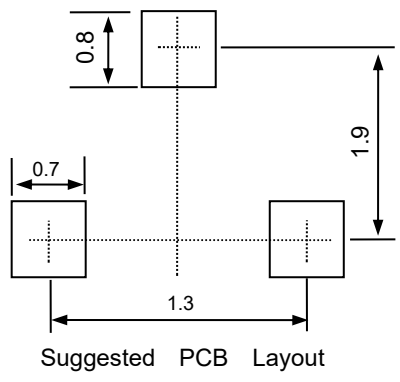


Fig 4. Capacitance

Product dimension （SOT-323）



Dim	Millimeters	
	MIN	MAX
A	1.80	2.20
B	1.15	1.35
C	2.00	2.45
D	0.65BSC	
F	1.20	1.40
G	0.05	0.25
H	0.525REF	
J	0.20	0.40
K	0.80	1.10
L	0.00	0.10
θ	0°	10°



Suggested PCB Layout

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