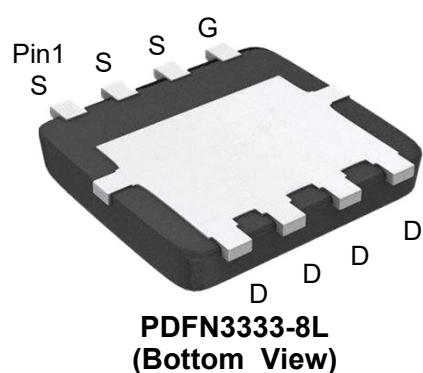


N-Channel MOSFET

Description

The PNM8PN03R13 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. This device is suitable for use as a load switch or in PWM applications.

| MOSFET Product Summary | | |
|------------------------|-----------------------|----------|
| $V_{DS}(V)$ | $R_{DS(on)}(m\Omega)$ | $I_D(A)$ |
| 30 | 10@ $V_{GS} = 10V$ | 39 |
| | 13@ $V_{GS} = 4.5V$ | |

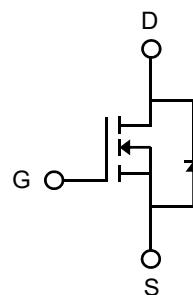


Feature

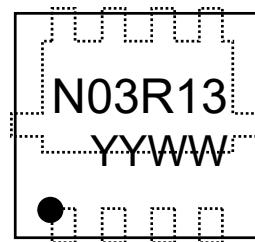
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Applications

- PWM applications
- Load switch
- Power management
- DC-DC Converters
- Wireless Chargers



Circuit Diagram



Marking (Top View)

Absolute maximum rating@25°C

| Rating | Symbol | Value | Units |
|--|-----------------|----------|-------|
| Drain-Source Voltage | V_{DS} | 30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous ¹⁾ | I_D | 39 | A |
| $T_C=100^\circ C$ | | 24.4 | |
| Pulsed Drain Current ²⁾ | I_{DM} | 150 | A |
| Total Power Dissipation ³⁾ | P_D | 29.8 | W |
| Avalanche Current ⁴⁾ | I_{AS} | 22 | A |
| Avalanche Energy ⁴⁾ | E_{AS} | 24 | mJ |
| Thermal Resistance Junction-to-Ambient ⁵⁾ | $R_{\theta JA}$ | 44.25 | °C/W |
| Junction and Storage Temperature Range | T_J, T_{STG} | -55~+150 | °C |

Electrical characteristics per line@25°C (unless otherwise specified)

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Units |
|---|--------------|---|------|------|-----------|---------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS} = 0V, I_D = 250\mu A$ | 30 | - | - | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = 30V, V_{GS} = 0V$ | - | - | 1.0 | μA |
| Gate-Body Leakage Current | I_{GSS} | $V_{GS} = \pm 20V, V_{DS} = 0V$ | - | - | ± 100 | nA |
| On Characteristics | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 250\mu A$ | 1.0 | 1.5 | 2.4 | V |
| Drain-Source On-State Resistance | $R_{DS(ON)}$ | $V_{GS} = 10V, I_D = 20A$ | - | 10 | 13 | mΩ |
| | | $V_{GS} = 4.5V, I_D = 15A$ | - | 13 | 20 | |
| Dynamic Characteristics⁶⁾ | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$ | - | 813 | - | pF |
| Output Capacitance | C_{oss} | | - | 105 | - | |
| Reverse Transfer Capacitance | C_{rss} | | - | 84 | - | |
| Switching Characteristics⁶⁾ | | | | | | |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DS} = 15V, V_{GS} = 10V, R_G = 10\Omega, I_D = 20A$ | - | 4.1 | - | ns |
| Turn-on Rise Time | t_r | | - | 9.6 | - | |
| Turn-Off Delay Time | $t_{d(off)}$ | | - | 28 | - | |
| Turn-Off Fall Time | t_f | | - | 13.3 | - | |
| Total Gate Charge | Q_g | $V_{DS} = 15V, V_{GS} = 10V, I_D = 20A$ | - | 16.4 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 2.4 | - | |
| Gate-Drain Charge | Q_{gd} | | - | 3.0 | - | |
| Gate Resistance | R_g | $V_{GS}=0V, V_{DS}=0V, f=1MHz$ | - | 2.47 | - | Ω |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage | V_{SD} | $V_{GS} = 0V, I_S = 2A$ | - | - | 1.3 | V |

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. Repetitive Rating: Pulse width limited by maximum junction temperature($T_{J_Max}=150^{\circ}C$).
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. This single-pulse measurement was taken under the following condition ($L=100\mu H, V_{GS}=10V, V_{DS}=30V$)while it's value is limited by $T_{J_Max}=150^{\circ}C$.
5. Device mounted on infinite heatsink.
6. Guaranteed by design, not subject to production.

Typical Characteristics

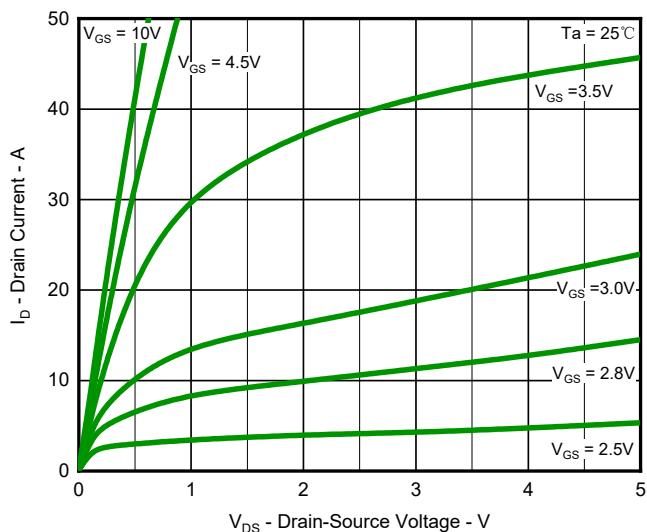


Fig.1 Output Characteristics

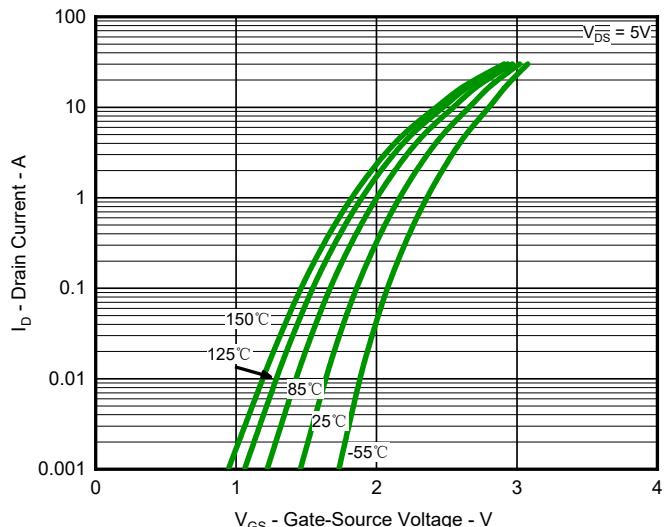


Fig.2 Typical Transfer Characteristic

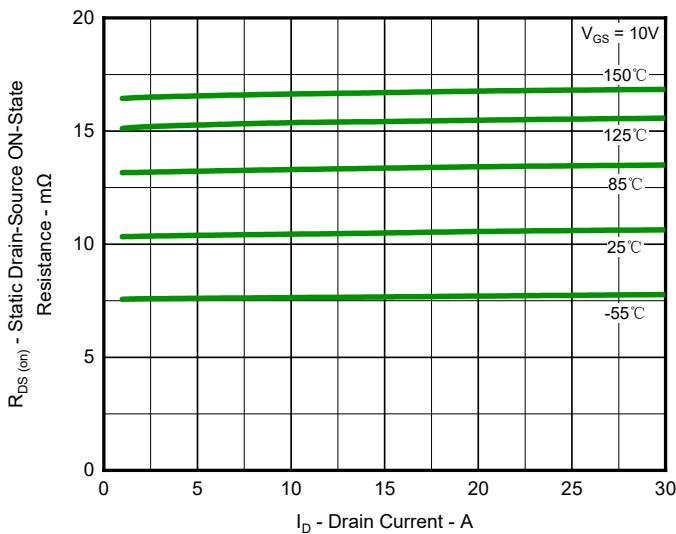


Fig.3 Typical On-Resistance vs. Drain Current and Temperature

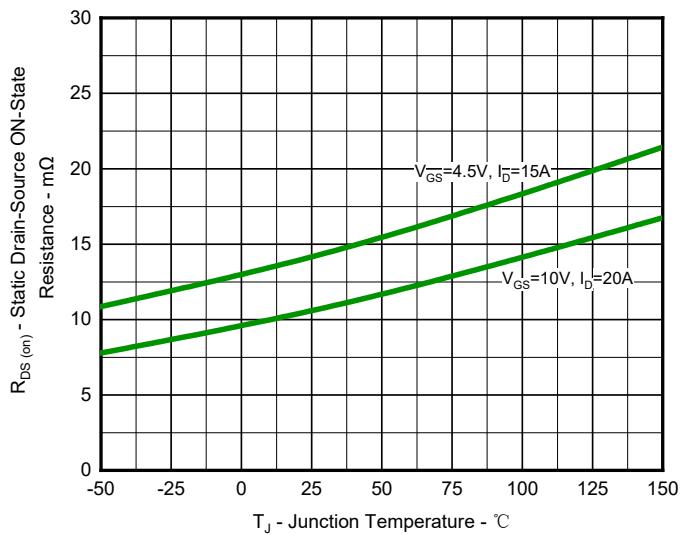


Fig.4 On-Resistance Variation with Temperature

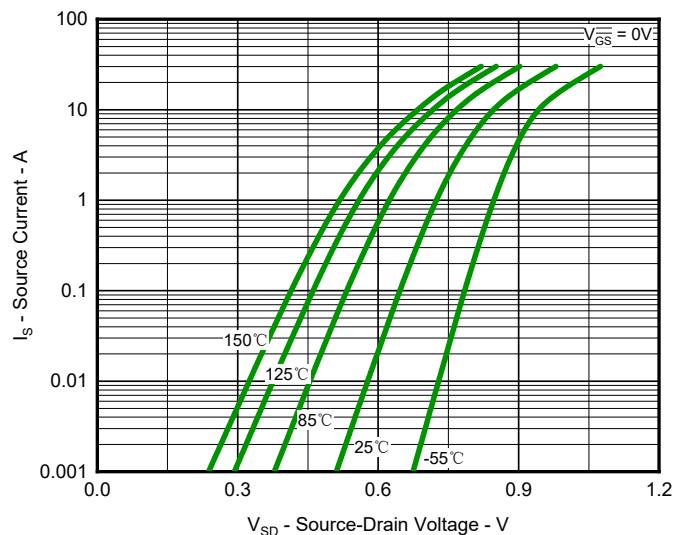


Fig.5 Diode Forward Voltage vs. Current

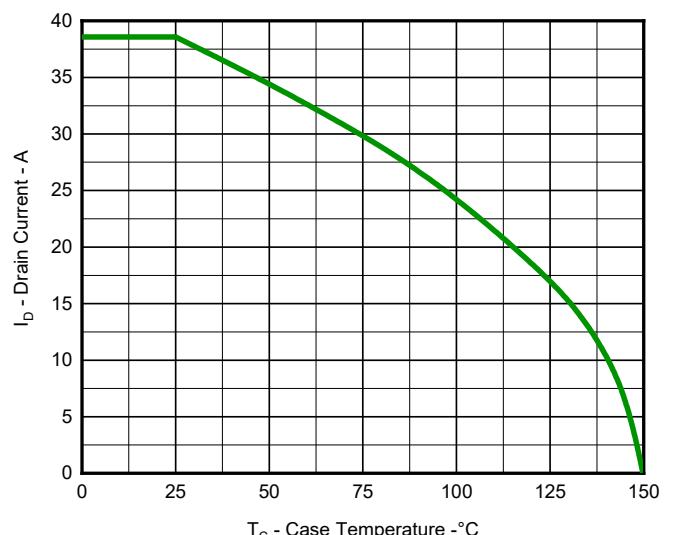


Fig.6 Maximum Drain Current vs. Case Temperature

N-Channel MOSFET

PNM8PN03R13

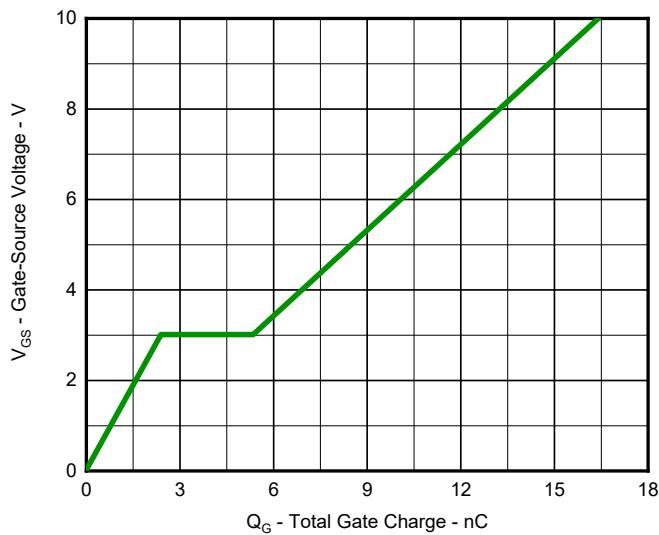


Fig.7 Gate Charge Characteristics

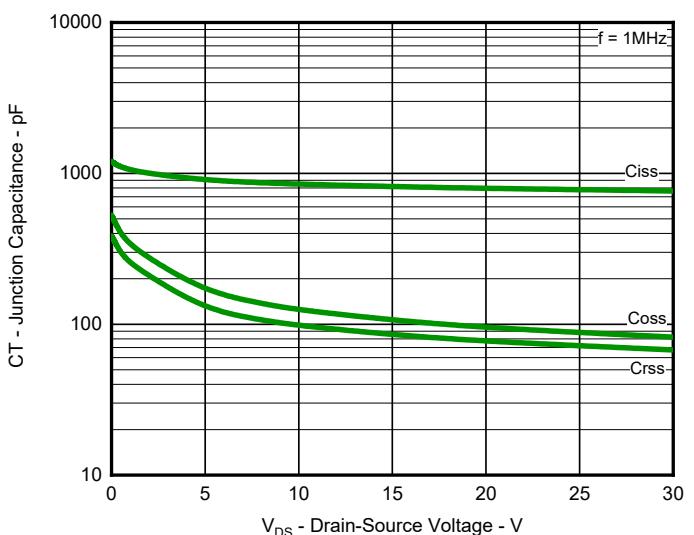


Fig.8 Typical Junction Capacitance

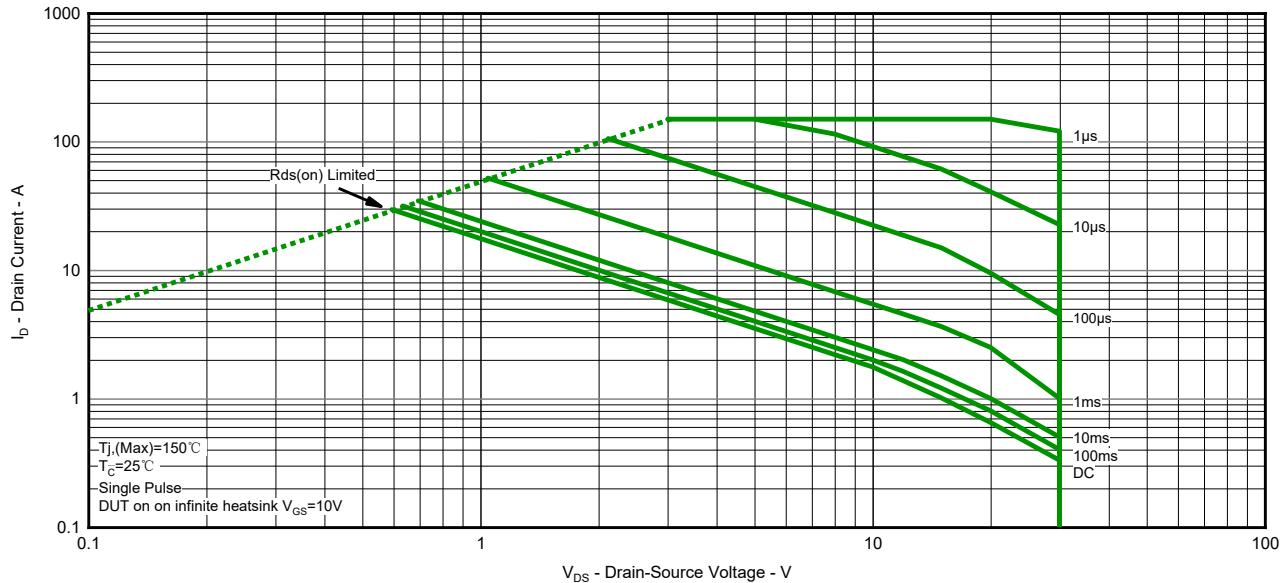


Fig.9 Safe Operation Area

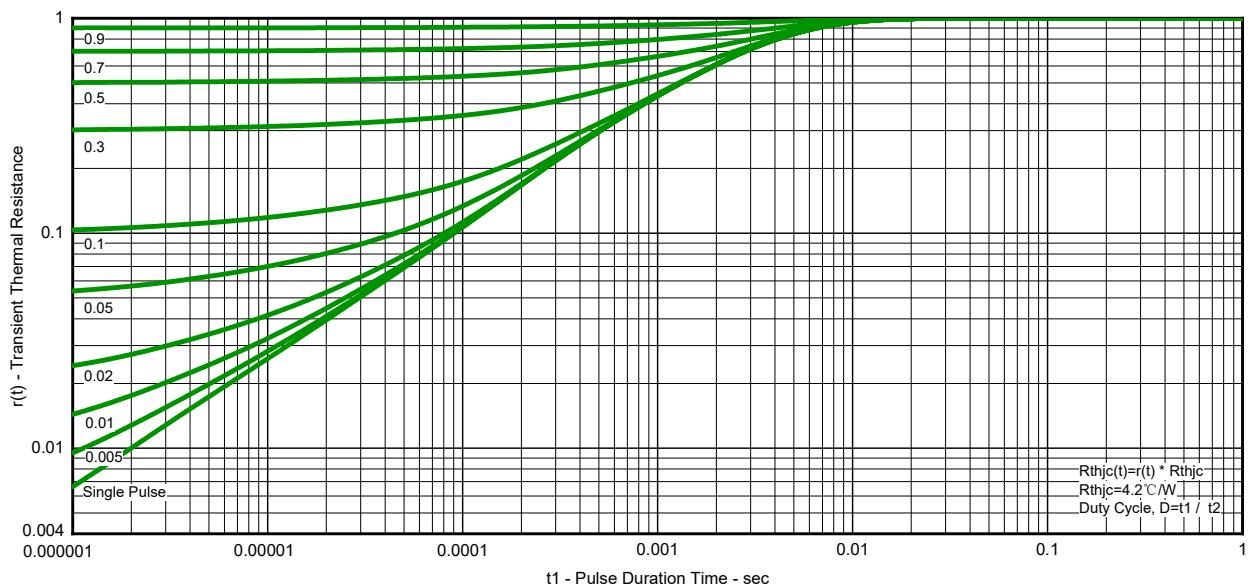
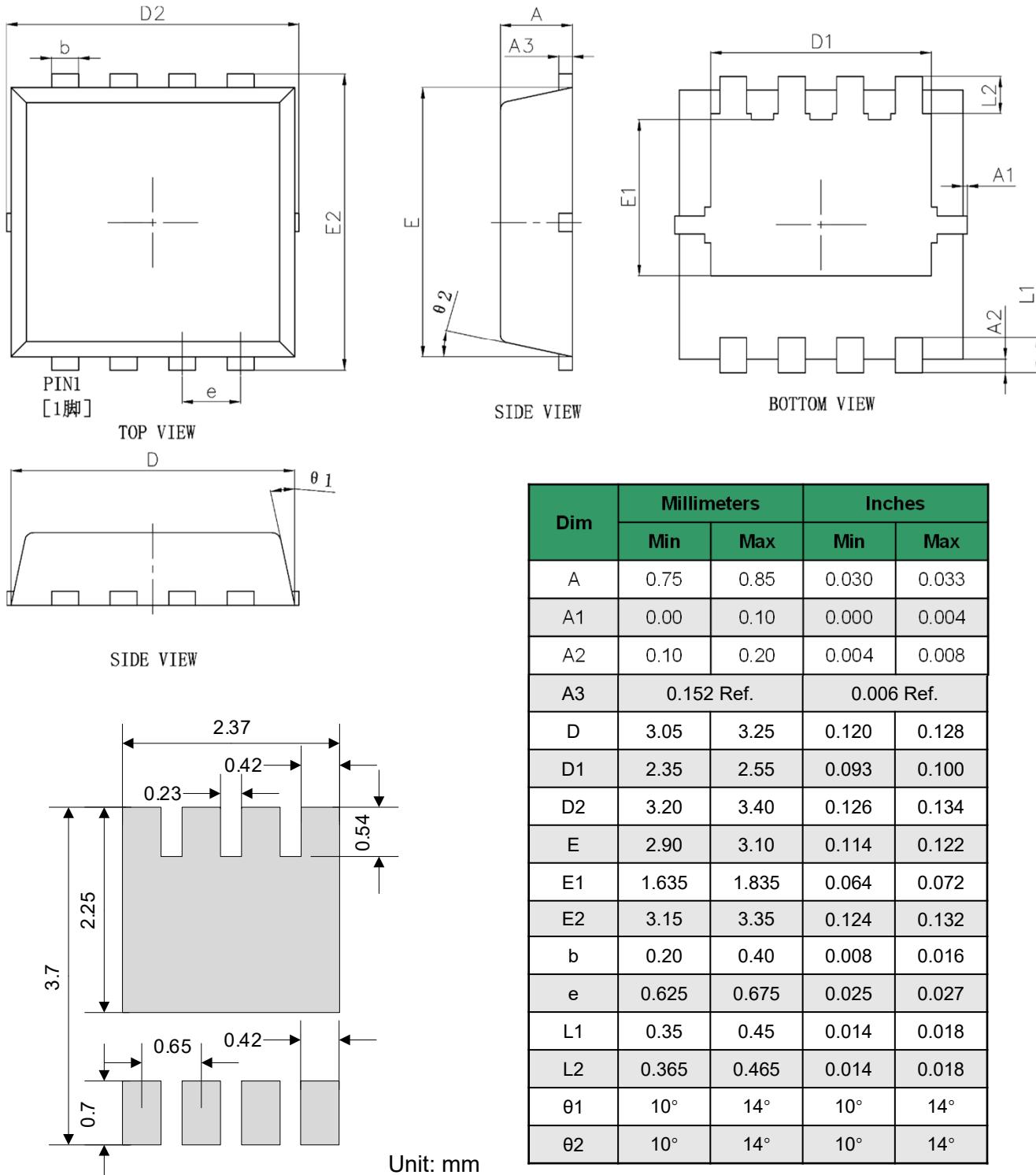


Fig.10 Transient Thermal Resistance

Product Dimension (PDFN3333-8L)

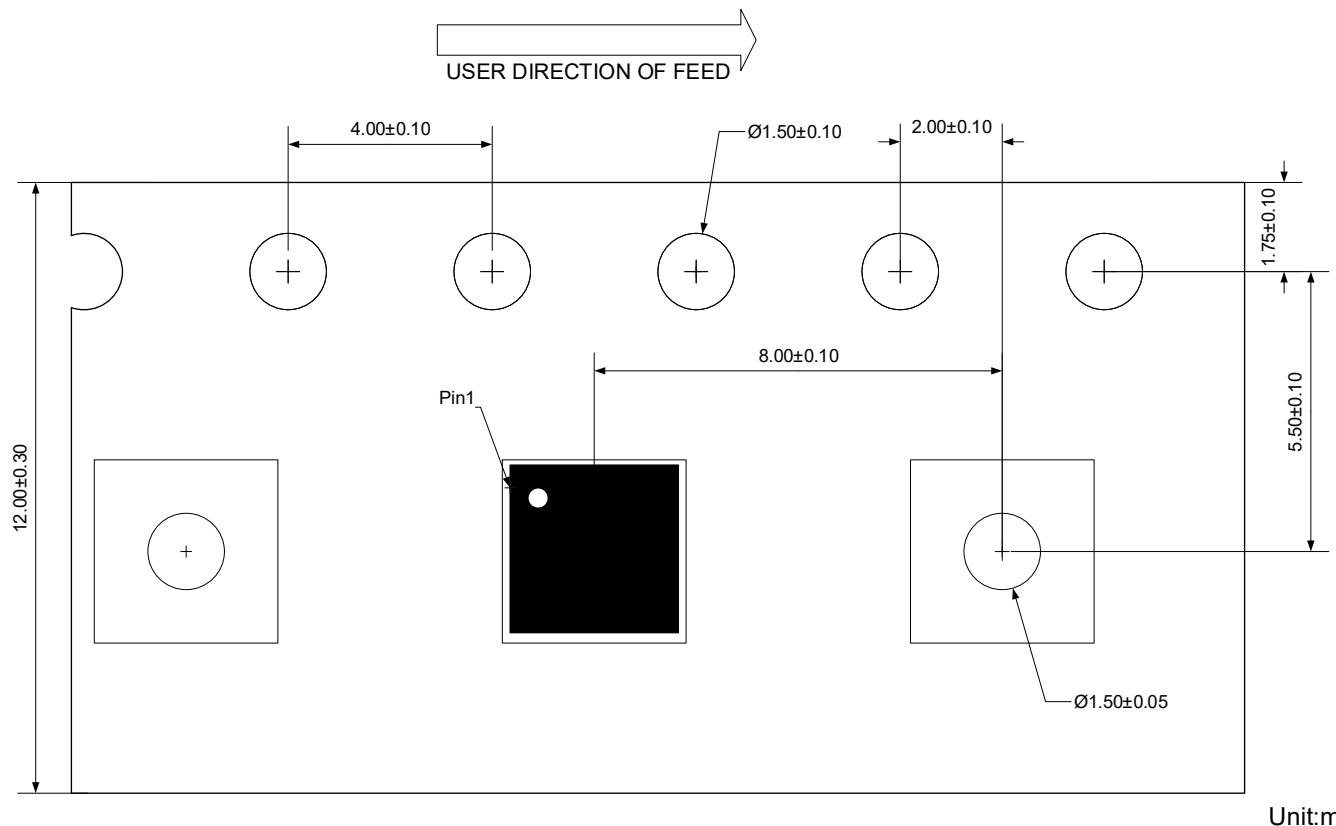


Suggested PCB Layout

Ordering Information

| Device | Package | Reel | Shipping |
|-------------|----------------------|------|--------------------|
| PNM8PN03R13 | PDFN3333-8L(Pb-Free) | 13" | 5000 / Tape & Reel |

Load With Information



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