High Precision CC/CV Primary-Side Converter

General Description

The PN8360 consists of a high precision CC/CV primary side controller and a 650V power MOSFET, specifically designed for a high performance low power AC/DC charger and LED lighting with minimal external components. PN8360 operates in primary-side sensing and regulation, so opto-coupler and TL431 could be eliminated. PN8360 offers complete protection coverage with automatic self-recovery feature including Cycle-by-Cycle current limiting protection (OCP), over voltage protection (OVP) and feedback loop open protection (OLP), over temperature protection (OTP) and short circuit protection etc. Internal HV Start-up circuit and the chip's very low consumption help to meet the strict standby power standard. In CC control, the current and output power setting can be adjusted externally by the sense resistor Rcs at CS pin. In CV control, PFM operations are utilized to achieve high performance and high efficiency. In addition, good load regulation is achieved by the built-in cable drop compensation.

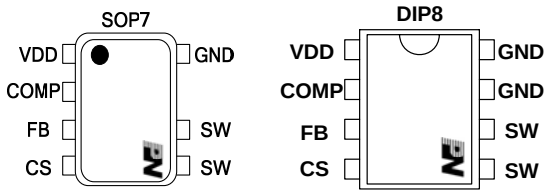
Features

- Internal 650 V avalanche-rugged power MOSFET
- ±5% Constant current Regulation at Universal AC input
- Primary-side Sensing and Regulation Without TL431 and Opt coupler
- Programmable CV and CC Regulation
- Programmable Cable Drop Compensation
- Built-in Primary winding inductance compensation
- Internal HV Start-up Circuit
- Excellent Protection Coverage:
  - ✧ Over Temperature Protection (OTP)
  - ✧ VDD Under Voltage Lockout (UVLO)
  - ✧ Cycle-by-Cycle Current Limiting (OCP)
  - ✧ Open Loop Protection (OLP)
  - ✧ VDD Over Voltage Protection (OVP)
  - ✧ Auto- recovery protection Mode

Applications

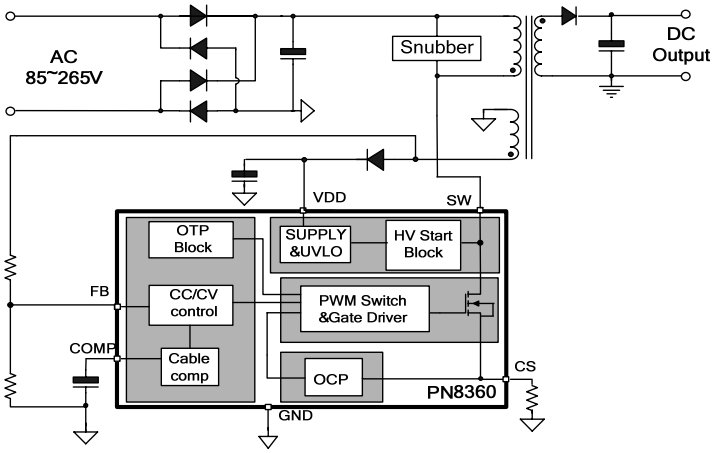
- Switch AC/DC Adaptor and Battery Charger
- LED Light

Package/Order Information



Order codes	Package	Vcable	Typical power
			85~265 V_{AC}
PN8360SSC-R1	**SOP7**	**3%**	**12W**
PN8360SSC-R1B	**SOP7**	**6%**	**12W**
PN8360SSC-R1C	**SOP7**	**0%**	**12W**
PN8360NEC-T1	**DIP8**	**3%**	**15W**
PN8360NEC-T1B	**DIP8**	**6%**	**15W**
PN8360NEC-T1C	**DIP8**	**0%**	**15W**

Typical Application



Pin Definitions

Table 1. Pin Definitions

SOP-7 Pin Number	DIP-8 Pin Number	Pin Name	Pin Function Description
1	1	VDD	Positive Supply voltage Input
2	2	COMP	Loop compensation
3	3	FB	The voltage feedback from auxiliary winding. Connected to resistor divider from auxiliary winding reflecting output voltage
4	4	CS	Current Sense Input
5	5	SW	HV MOSFET Drain pin. The Drain pin is connected to the primary lead of the transformer.
6	6		
7	7,8	GND	Ground

Typical power

Table 2. Typical power

Part number	Package	85-265V_{AC}
PN8360	**SOP-7**	**12W**
	DIP-8	**15W**

Note:

1. Maximum practical continuous power in adapter design at 45°C ambient temperature, with enough cooling conditions.

Absolute Maximum Ratings

Supply voltage Pin VDD……..…..-0.3~25V

High-Voltage Pin, SW……..…650V

Pin FB, CS, COMP……...…-0.3~5.5V

Operating Junction Temperature……..…..-40~150℃

Package Thermal Temperature(SOP-7)……...…… ……80°C/W

Package Thermal Temperature(DIP-8)……...……40°C/W

Storage Temperature Range……-55~150℃

Lead Temperature (Soldering, 10Secs)……260℃

ESD voltage Protection (HBM)……4.0kV

Pulse drain current……3.0A

Electrical Characteristics

Table 3. Power section (T_J=25°C, V_{DD} = 17 V; unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V_{BVDSS}	Break-down voltage	I_{SW} = 250uA, T_J = 25℃	650	690		V
I_{OFF}	Off-state drain current	V_{SW} = 500V			100	uA
$R_{DS(on)}$	Drain-source on state resistance	I_{SW} = 1A,T_J = 25℃		2.5		Ω

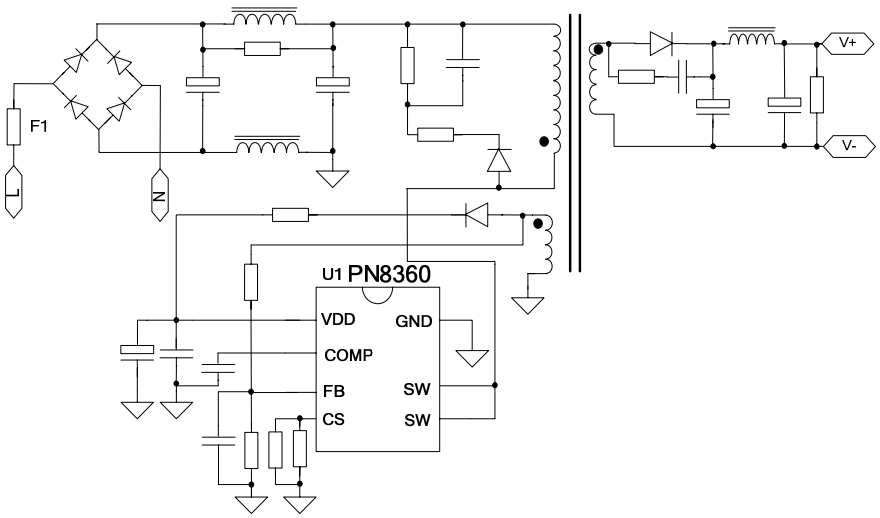
Table 4. Supply section (T_J = 25°C, V_{DD} = 17 V; unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
SUPPLY VOLTAGE SECTION						
V_{SW_START}	Drain-source start voltage		20			V
I_{DD_CH}	Start up charging current	V_{DD}<V_{DDOFF}, Isw charge V_{DD}	-0.6	-1	-1.4	mA
V_{DD}	Operating voltage range	After turn-on	8		25	V
V_{DDon}	VDD start up threshold		13.5	15	16.5	V
V_{DDoff}	VDD under voltage shutdown threshold		7	8	9	V
V_{DDovp}	VDD over voltage protect		25	27	29	V
$V_{DDclamp}$	VDD clamp voltage		27	30	33	V
SUPPLY CURRENT SECTION						
I_{DDon}	Operating supply current, switching	V_{DD}＝17V	0.2	0.4	1.0	mA
I_{DDoff}	Operating supply current, not switching	V_{DD} = 10 V	0.1	0.2	0.4	mA
I_{DD_FAULT}	Operating supply current in protecting	V_{DD}＝17V after fault	0.1	0.2	0.4	mA

Table 5. Controller section (T_J = 25°C, V_{DD} = 17 V; unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
CURRENT SECTION						
T_{LEB}	Leading edge blanking time			450		ns
V_{TH_OC1}	Current limiting threshold voltage		485	500	515	mV
V_{TH_OC2}	Current limiting threshold voltage When light load			330		mV
FB SECTION						
V_{REF1}	No-load feedback voltage reference		1.945	1.965	1.985	V
$T_{OFF-MIN}$	Minimum turn off time			3.8		us
$T_{OFF-MAX}$	Maximal turn off time			10		ms
T_{ONMAX}	Maximal turn on time		16	25	30	us
COMP SECTION						
V_{cable1}	Line resistance value compensation			0%		
V_{cable2}	Line resistance value compensation			3%		
V_{cable3}	Line resistance value compensation			6%		
THERMAL SECTION						
T_{SD}	Thermal shutdown temperature		140	160		°C
T_{HYST}	Thermal shutdown hysteresis			30		°C

Typical circuit



Operation Description

1. Startup

At start up, the internal high-voltage current source supplies the internal bias and charges the external VDD capacitor. When VDD reaches 15V, the device starts switching and the internal high-voltage current source stops charging the capacitor. The device is in normal operation provided VDD does not drop below 8V. After start up, the bias is supplied from the auxiliary transformer winding.

2. CC Operation Mode

In CC operation, The PN8360 captures the auxiliary flyback signal at FB pin through a resistor divider network. The pulse width of the auxiliary flyback signal determines the PN8360 oscillator frequency. The higher the output voltage is, the shorter the pulse width is. And the chip oscillator frequency is higher, thus the constant output current can be achieved.

3. CV Operation Mode

In CV operation, The PN8360 captures the auxiliary flyback signal at FB pin through a resistor divider network. The voltage of the auxiliary flyback signal determines the PN8360 oscillator frequency. In full load mode, the chip oscillator frequency decreases while the output current decreases. In no load standby mode, the frequency is further reduced to minimize standby power.

4. Current Sensing and Leading Edge Blanking

Cycle-by-Cycle current limiting is offered in PN8360. The switch current is detected by a sense resistor into the CS pin. The CC set-point and maximum output power can be externally adjusted by external current sense resistor at CS pin.

An internal leading edge blanking circuit chops off the sensed voltage spike at initial power MOSFET on state so that the external RC filtering on sense input is no longer needed.

5. Programmable Cable drop Compensation

The Cable drop compensation block compensates the voltage drop across the cable. As the load current decreases from full load to no load, the voltage drop across the cable decreases. In the no load mode, the block decrease the CV set-point and inversely in the full load mode the block increase the CV set-point. The compensation is determined by the chip inside setting, different version chip could meet different compensation ranges.

6. Protection Control

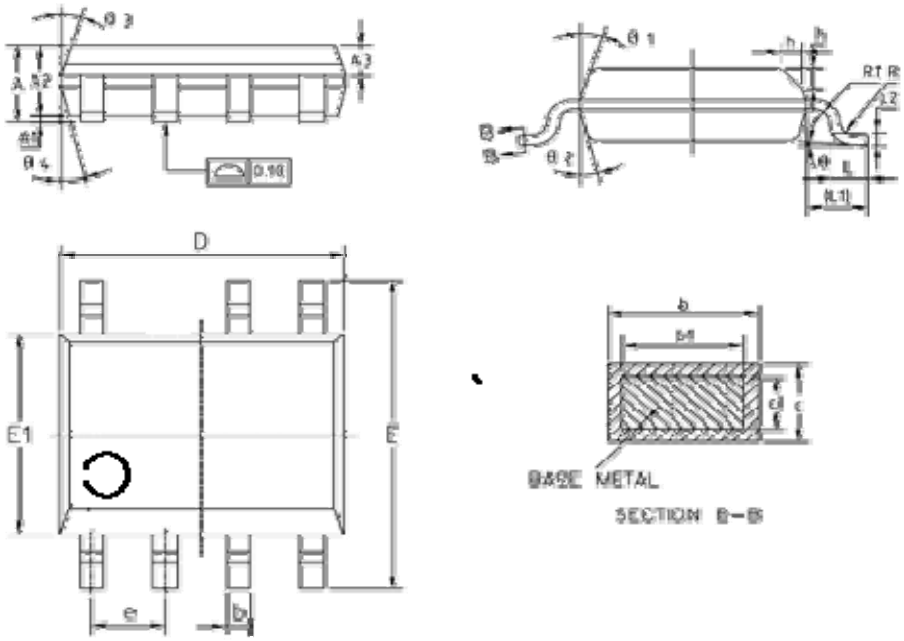
The PN8360 has several self-protection functions, such as Over-Voltage Protection, Over-Temperature Protection, Feedback Loop open Protection, Output short circuit Protection, CS resistor short circuit Protection and Under Voltage Lockout on VDD. All protections are implemented as auto-restart mode.

Package Dimensions

Table 6. SOP-7 mechanical data

Size / symbol	Min.(mm)	Nom(mm)	Max.(mm)	Size / symbol	Min.(mm)	Nom(mm)	Max.(mm)
A	—	—	1.75	D	4.70	4.90	5.10
A1	0.10	0.15	0.225	E	5.80	6.00	6.20
A2	1.30	1.40	1.50	E1	3.70	3.90	4.10
A3	0.60	0.65	0.70	e	1.728（SC）		
b	0.39	—	0.48	h	0.25	—	0.50
b1	0.38	0.41	0.43	L	0.50	—	0.80
c	0.21	—	0.26	L1	1.05BSC		
c1	0.19	0.20	0.21	θ	0°	—	8°

Figure 1. Package dimensions

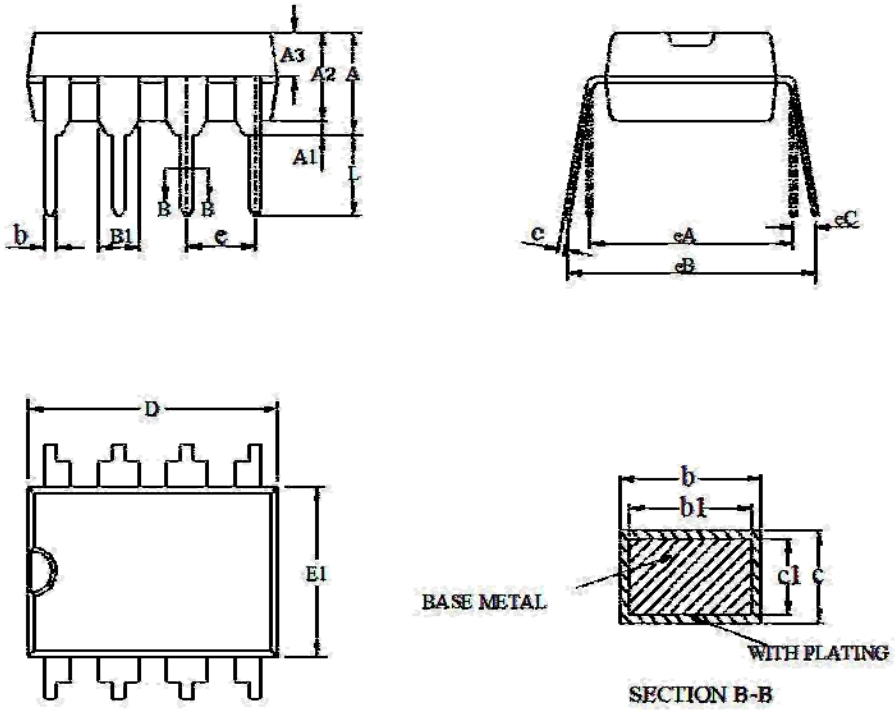


Order codes	Vcable	Mark	Note
PN8360SSC-R1	3%	PN8360 YWWAXXXX	Y：Year； WW：Week； A：PN8360SSC-T1；XXXX：Inernal code
PN8360SSC-R1B	6%	PN8360 YWWBXXXX	Y：Year； WW：Week； B：PN8360SSC-T1B；XXXX：Inernal code
PN8360SSC-R1C	0%	PN8360 YWWCXXXX	Y：Year； WW：Week； C：PN8360SSC-T1C；XXXX：Inernal code

Table 7. DIP-8 mechanical data

Size / symbol	Min(mm)	Max(mm)	Size / symbol	Min(mm)	Max(mm)
A	3.60	4.00	c1	0.23	0.27
A1	0.51	——	D	9.05	9.45
A2	3.00	3.40	E1	6.15	6.55
A3	1.55	1.65	e	2.54BSC	
b	0.44	0.53	e A	7.62BSC	
b1	0.43	0.48	e B	7.62	9.30
B1	1.52BSC		e C	0.00	0.84
c	0.24	0.32	L	3.00	——

Figure 2. Package dimensions



Order codes	Vcable	Mark	Note
PN8360NEC-T1	3%	PN8360 YWWAXXXX	Y：Year； WW：Week； A：PN8360NEC-T1；XXXX：Inernal code
PN8360NEC-T1B	6%	PN8360 YWWBXXXX	Y：Year； WW：Week； B：PN8360NEC -T1B；XXXX：Inernal code
PN8360NEC-T1C	0%	PN8360 YWWCXXXX	Y：Year； WW：Week； C：PN8360NEC -T1C；XXXX：Inernal code