

## General Description

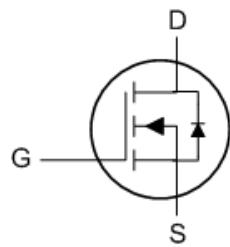
These N-Channel enhancement mode power field effect transistors are using advanced super junction technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

BVDSS	RDSON	ID
650V	0.7Ω	10A

## Features

- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

## TO220 Pin Configuration



## Applications

- High efficient switched mode power supplies
- TV Power
- Adapter/charger
- Server Power
- PV Inverter / UPS

## Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	10	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	6.3	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	40	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	500	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	10	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	125	W
	Power Dissipation – Derate above $25^\circ\text{C}$	1	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1	$^\circ\text{C/W}$



650V N-Channel MOSFETs

PMP10N65M

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)****Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	650	---	---	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=650\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=520\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=100^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

**On Characteristics**

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=5\text{A}$	---	0.57	0.7	$\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_{\text{D}}=250\mu\text{A}$	2	3	4	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=30\text{V}$ , $I_{\text{D}}=4\text{A}$	---	12	---	S

**Dynamic and switching Characteristics**

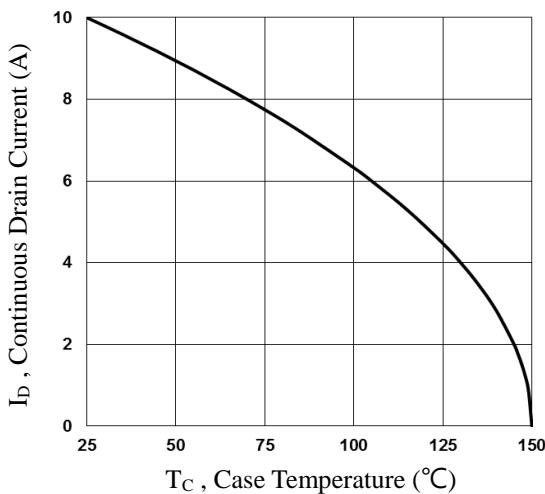
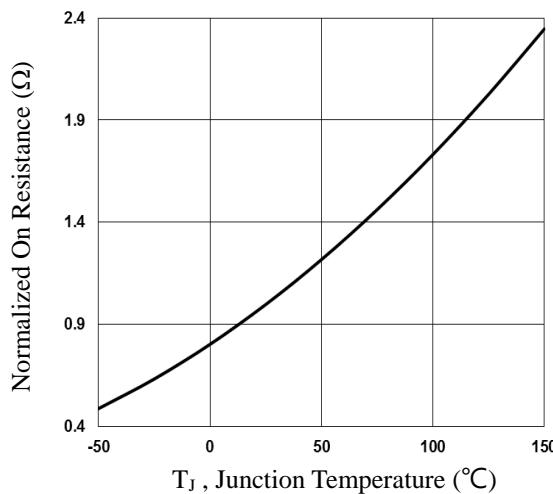
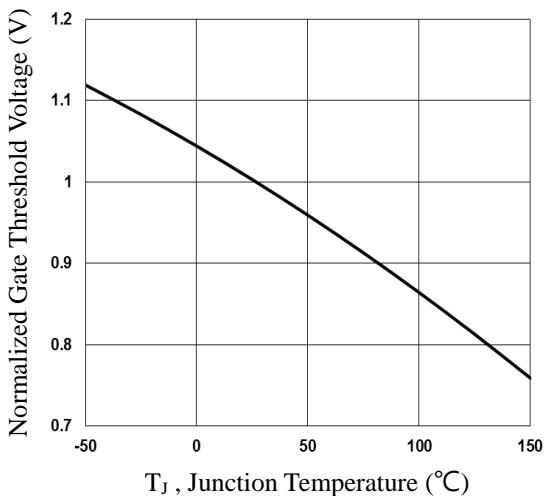
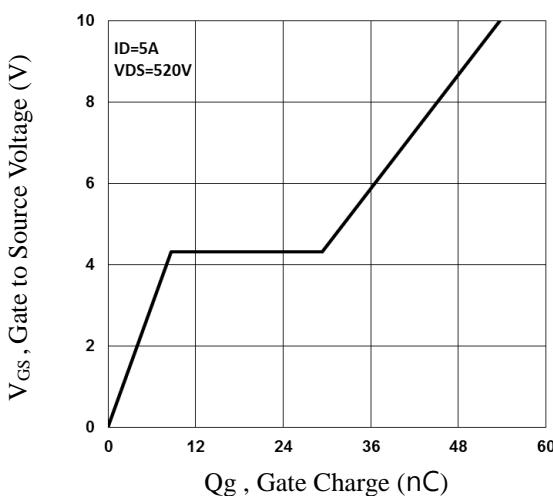
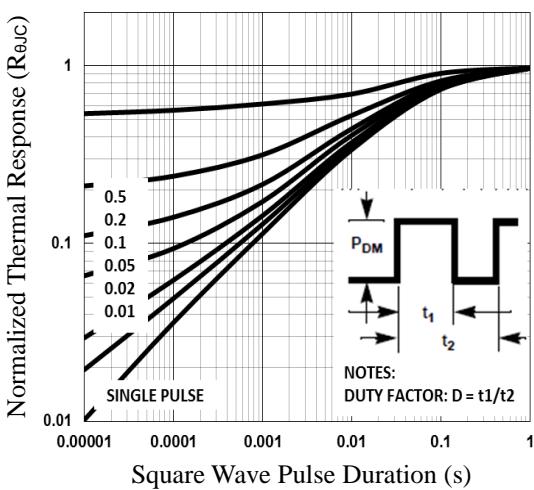
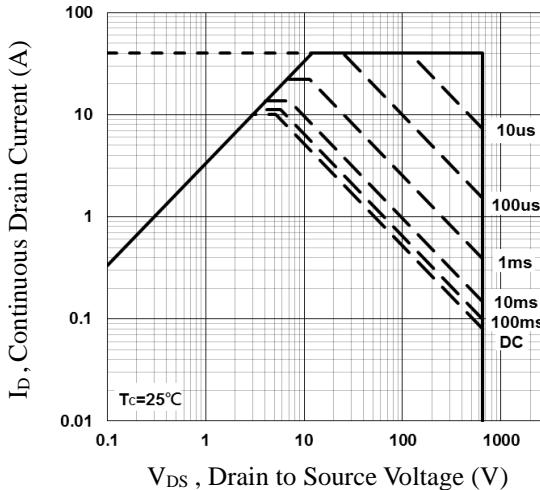
$Q_g$	Total Gate Charge <sup>3,4</sup>	$V_{\text{DS}}=520\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=5\text{A}$	---	54	80	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>3,4</sup>		---	8.6	13	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>3,4</sup>		---	20.5	30	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>3,4</sup>	$V_{\text{DD}}=300\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_{\text{G}}=25\Omega$ $I_{\text{D}}=5\text{A}$	---	45	90	ns
$T_r$	Rise Time <sup>3,4</sup>		---	50	100	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>3,4</sup>		---	105	200	
$T_f$	Fall Time <sup>3,4</sup>		---	42	80	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	1890	3800	pF
$C_{\text{oss}}$	Output Capacitance		---	175	350	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	27	54	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	1.6	3.2	$\Omega$

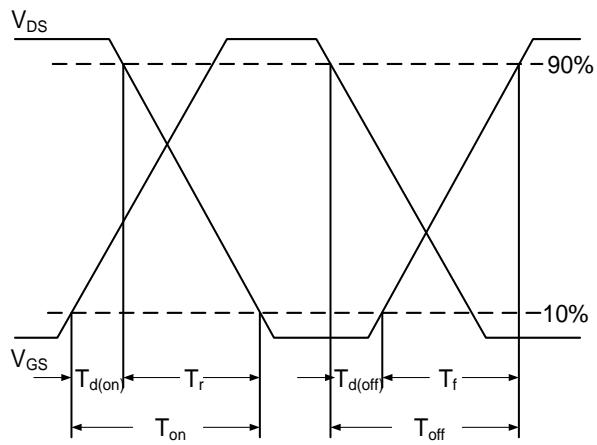
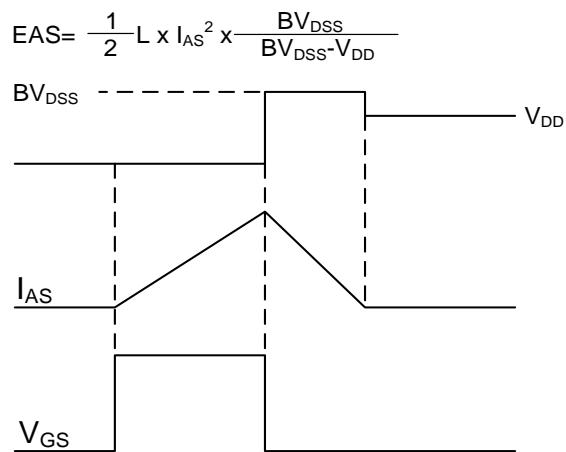
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	10	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	20	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V

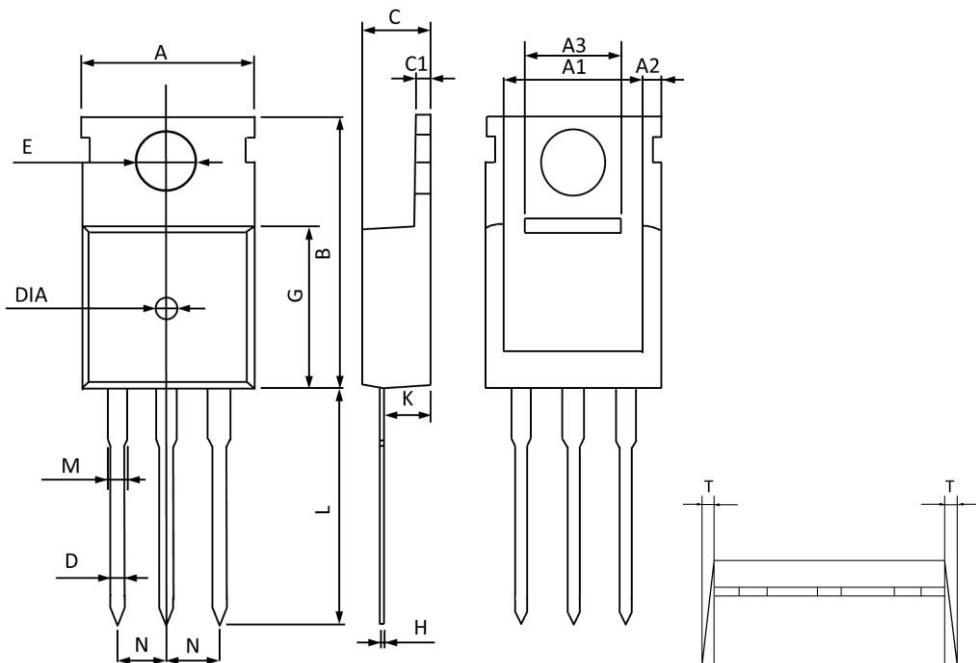
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=10\text{mH}$ ,  $I_{\text{AS}}=10\text{A}$ ,  $R_{\text{G}}=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**

**Fig.2 Normalized RDS(on) vs. T<sub>j</sub>**

**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.300	9.700	0.406	0.382
A1	8.840	8.440	0.348	0.332
A2	1.250	1.050	0.049	0.041
A3	5.300	5.100	0.209	0.201
B	16.200	15.400	0.638	0.606
C	4.680	4.280	0.184	0.169
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	3.800	3.400	0.150	0.134
G	9.300	8.700	0.366	0.343
H	0.600	0.400	0.024	0.016
K	2.700	2.100	0.106	0.083
L	13.600	12.800	0.535	0.504
M	1.500	1.100	0.059	0.043
N	2.590	2.490	0.102	0.098
T	W0.35		W0.014	
DIA	Φ1.5 TYP.	deep0.2 TYP.	Φ0.059 TYP.	deep0.008 TYP.