

# **PMF63UNE** 20 V, N-channel Trench MOSFET 20 April 2016

Product data sheet

### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

### 3. Applications

- LED driver
- Power management
- Low-side loadswitch
- Switching circuits

### 4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	20	V
V <sub>GS</sub>	gate-source voltage			-8	-	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-	2.2	А
Static characte	Static characteristics						
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 2 A; T <sub>j</sub> = 25 °C		-	57	65	mΩ

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

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# 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	souce		
3	D	drain	1 2 SC-70 (SOT323)	G S 017aaa255

# 6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PMF63UNE	SC-70	plastic surface-mounted package; 3 leads	SOT323			

# 7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PMF63UNE	Z%V

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> = 25 °C		-	20	V
V <sub>GS</sub>	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	$V_{GS}$ = 4.5 V; $T_{amb}$ = 25 °C; t ≤ 5 s	[1]	-	2.2	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	2	А
		V <sub>GS</sub> = 4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	1.3	А
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	8	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	300	mW
			[1]	-	395	mW
		T <sub>sp</sub> = 25 °C		-	1.8	W
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	0.37	Α

Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.
 Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.

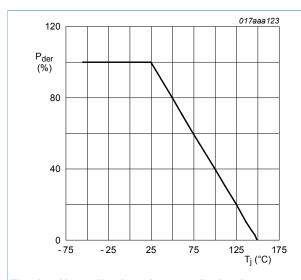
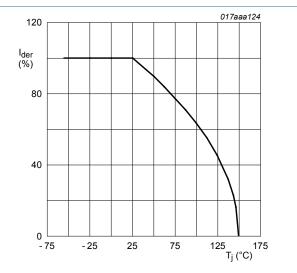


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

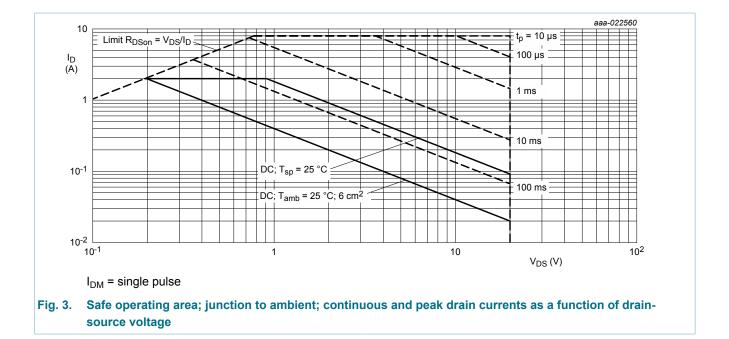
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$$I_{der} = \frac{I_D}{I_D(25^{\circ}C)} \times 100 \%$$

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### 9. Thermal characteristics

Table 6. 1	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance	in free air	[1]	-	363	418	K/W
	from junction to		[2]	-	276	317	K/W
	ampient	in free air; t ≤ 5 s	[2]	-	238	273	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	60	69	K/W

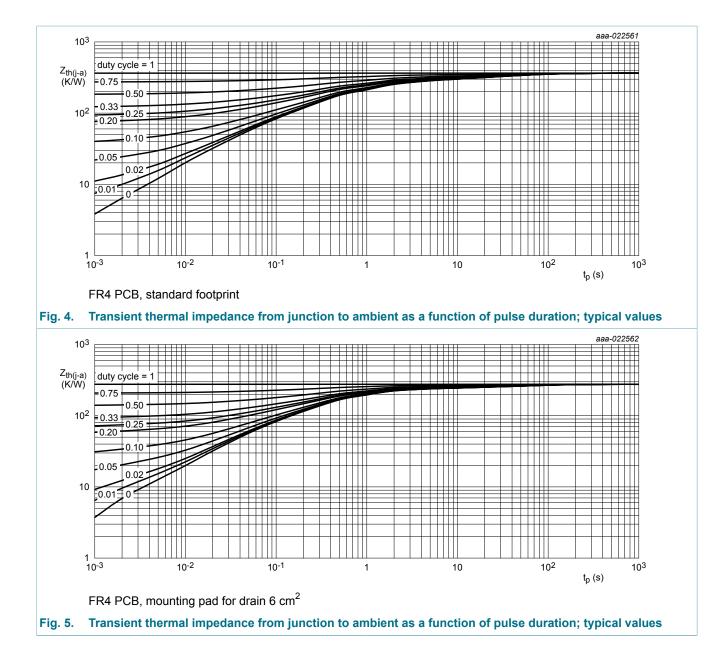
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

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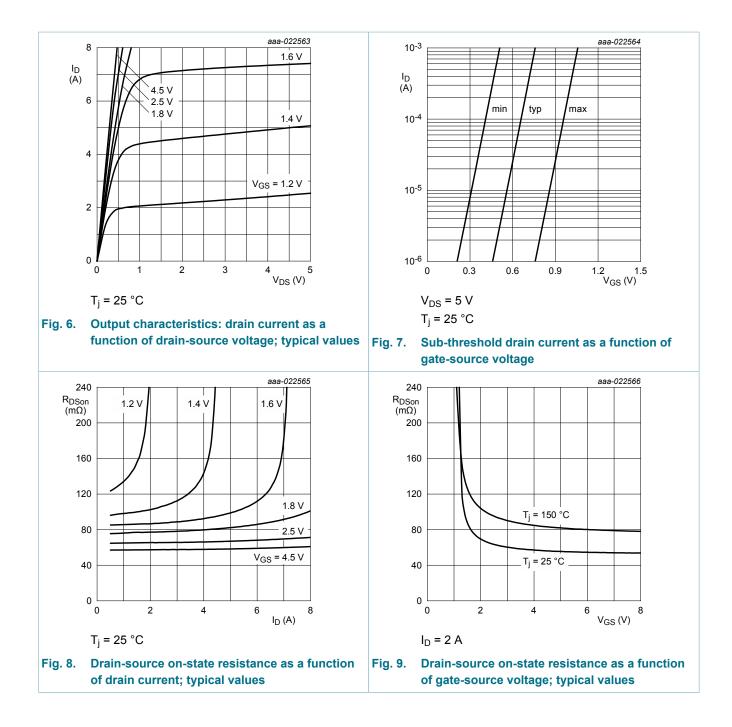
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### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	20	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	$I_D$ = 250 µA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = 25 °C	0.45	0.7	1	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 20 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	-	1	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 8 V; T <sub>j</sub> = 25 °C	-	-	10	μA
		$V_{GS}$ = -8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-10	μA
		$V_{GS}$ = 4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	5	μA
		$V_{GS}$ = -4.5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-5	μA
R <sub>DSon</sub>	drain-source on-state	$V_{GS}$ = 4.5 V; I <sub>D</sub> = 2 A; T <sub>j</sub> = 25 °C	-	57	65	mΩ
	resistance	$V_{GS}$ = 4.5 V; I <sub>D</sub> = 2 A; T <sub>j</sub> = 150 °C	-	84	96	mΩ
		$V_{GS}$ = 2.5 V; I <sub>D</sub> = 1.8 A; T <sub>j</sub> = 25 °C	-	64	74	mΩ
		$V_{GS}$ = 1.8 V; I <sub>D</sub> = 0.8 A; T <sub>j</sub> = 25 °C	-	78	88	mΩ
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 5 V; I <sub>D</sub> = 2 A; T <sub>j</sub> = 25 °C	-	9	-	S
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C	-	1.8	-	Ω
Dynamic ch	aracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 10 V; I <sub>D</sub> = 2 A; V <sub>GS</sub> = 4.5 V;	-	3.9	5.85	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.3	-	nC
Q <sub>GD</sub>	gate-drain charge		-	0.9	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS}$ = 10 V; f = 1 MHz; $V_{GS}$ = 0 V;	-	289	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	51	-	pF
C <sub>rss</sub>	reverse transfer capacitance	_	-	42	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 10 V; I <sub>D</sub> = 2 A; V <sub>GS</sub> = 4.5 V;	-	8	-	ns
t <sub>r</sub>	rise time	R <sub>G(ext)</sub> = 6 Ω; T <sub>j</sub> = 25 °C	-	27	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	35	-	ns
t <sub>f</sub>	fall time		-	19	-	ns
Source-drai	n diode		ı I			
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 0.37 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	0.7	1.2	V

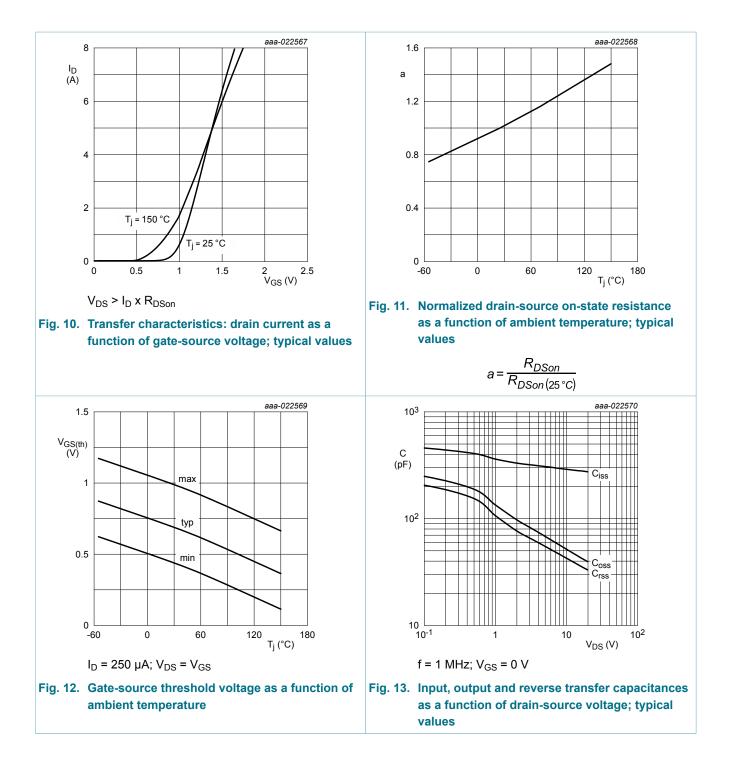
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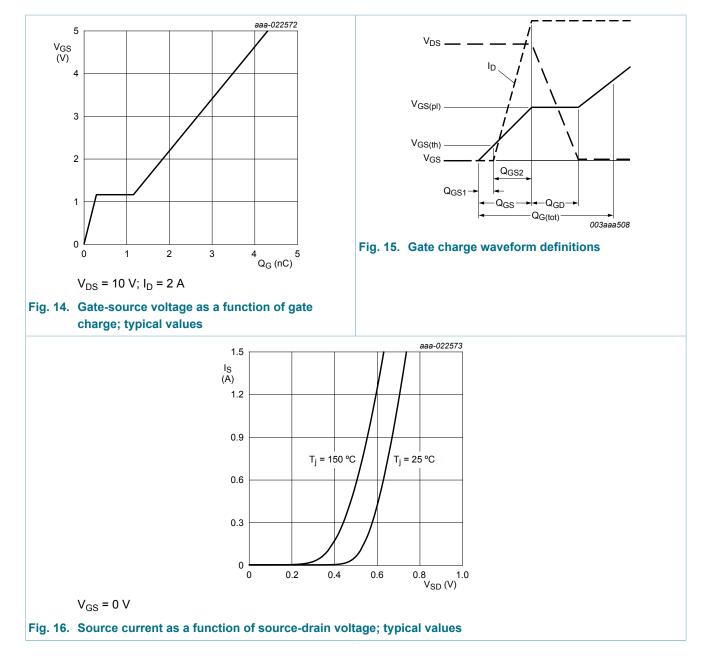
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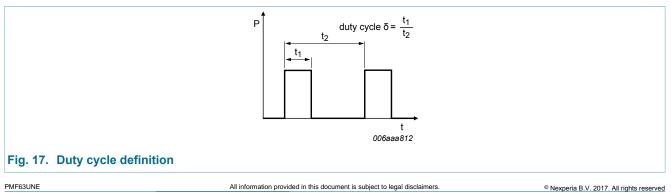
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# **11. Test information**



### 12. Package outline

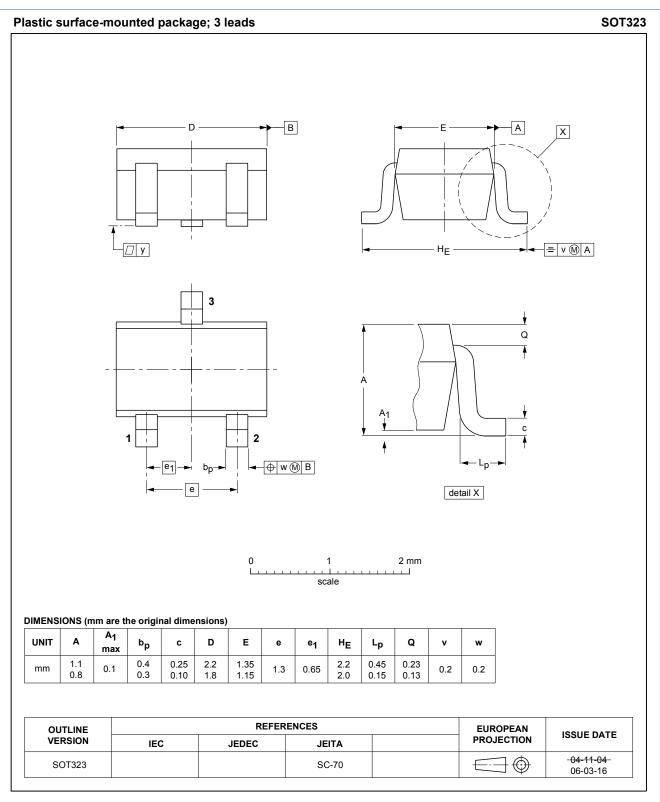
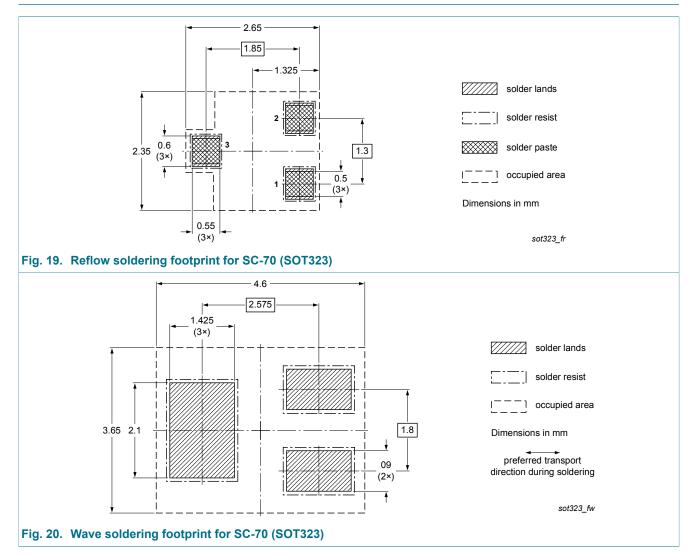


Fig. 18. Package outline SC-70 (SOT323)

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### **13. Soldering**



# 14. Revision history

Table 8. Revision his	le 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMF63UNE v.1	20160420	Product data sheet	-	-			

#### 20 V, N-channel Trench MOSFET

### 15. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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