

40 V, 1 A low VF MEGA Schottky barrier rectifier

27 November 2015

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection in a leadless ultra small DSN1006-2 (SOD993) Surface-Mounted Device (SMD) package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \le 1 A$
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage, typical: V_F = 510 mV
- Low reverse current, typical: $I_R = 13 \ \mu A$
- Package height typ. 270 µm

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Low power consumption applications
- Ultra high-speed switching
- LED backlight for mobile application

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; T _{sp} ≤ 140 °C; square wave	-	-	1	A
V _R	reverse voltage	T _j = 25 °C	-	-	40	V
V _F	forward voltage	$\label{eq:IF} \begin{array}{l} I_F = 1 \text{ A}; \ t_p \leq 300 \ \mu s; \ \delta \leq 0.02 \ ; \\ T_j = 25 \ ^\circ C \end{array}$	-	510	610	mV
I _R	reverse current	V_R = 20 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C	-	2.1	6	μA
		V_R = 40 V; $t_p \le 3$ ms; $\overline{\delta} \le 0.3$; T _j = 25 °C	-	13	40	μA

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		1 - 2
2	A	anode	1 2 Transparent top view	sym001
			DSN1006-2 (SOD993)	

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
PMEG4010ESB	DSN1006-2	DSN1006-2, leadless ultra small package; 2 terminals; body 1.0 x 0.6 x 0.27 mm	SOD993				

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG4010ESB	4E

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
l _F	forward current	T _{sp} ≤ 135 °C; δ = 1		-	1.4	А
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; T _{amb} ≤ 105 °C; square wave	[1]	-	1	A
		δ = 0.5 ; f = 20 kHz; T _{sp} ≤ 140 °C; square wave		-	1	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	4	А
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	10	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	0.525	W
			[3]	-	1	W
			[1]	-	1.78	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for anode and cathode 1 cm² each.

9. Thermal characteristics

Table 6.Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistar from junction to ambient	thermal resistance	[1][3	[1][2]	-	-	240	K/W
	-		[1][3]	-	-	125	K/W
	ambient		[1][4]	-	-	70	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	15	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

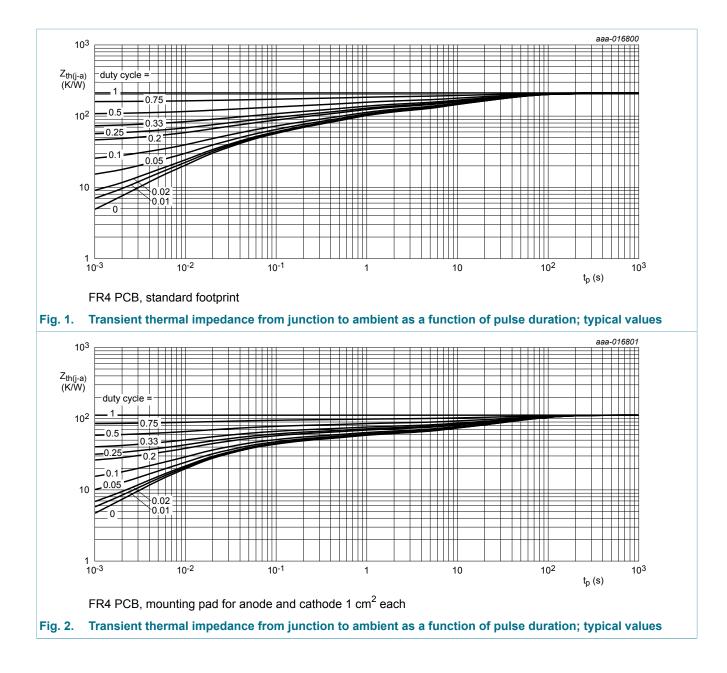
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for anode and cathode 1 cm² each.

- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
 - [5] Soldering point of anode tab.

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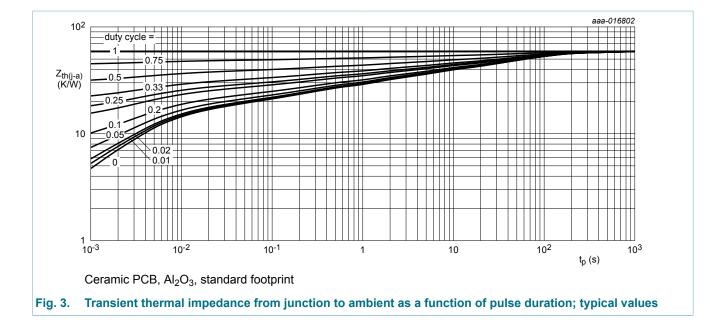
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10. Characteristics

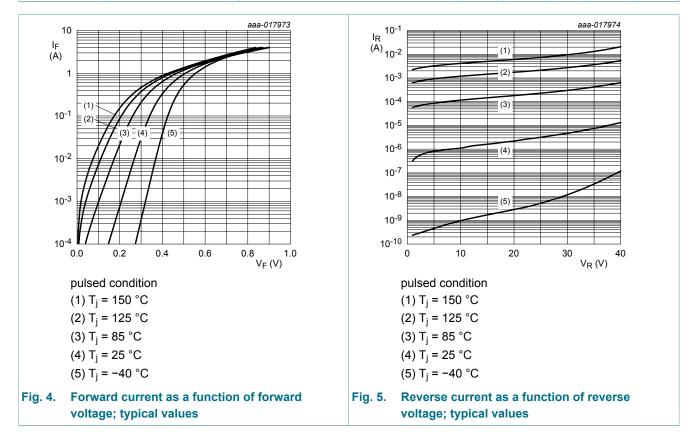
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	$I_{R} = 1 \text{ mA; } t_{p} = 300 \mu\text{s; } \delta = 0.02 ; \\ T_{j} = 25 ^{\circ}\text{C}$	40	-	-	V
V _F	forward voltage	I_F = 1 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	205	-	mV
		I_F = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	270	-	mV
		$I_F = 100 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02 \ ; \\ T_j = 25 \ ^\circ\text{C}$	-	340	385	mV
		$I_F = 200 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02 \ ; \\ T_j = 25 \ ^\circ\text{C}$	-	370	-	mV
		I_F = 500 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	430	495	mV
		I_F = 700 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	465	-	mV
		$I_F = 1 \text{ A}; t_p \le 300 \mu\text{s}; \delta \le 0.02 ;$ $T_j = 25 ^\circ\text{C}$	-	510	610	mV
I _R	reverse current	V_R = 5 V; $t_p \le 3$ ms; $\delta \le 0.3$; T_j = 25 °C	-	0.8	-	μA
		V_R = 10 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C	-	1	4	μA
		V_R = 20 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C	-	2.1	6	μA

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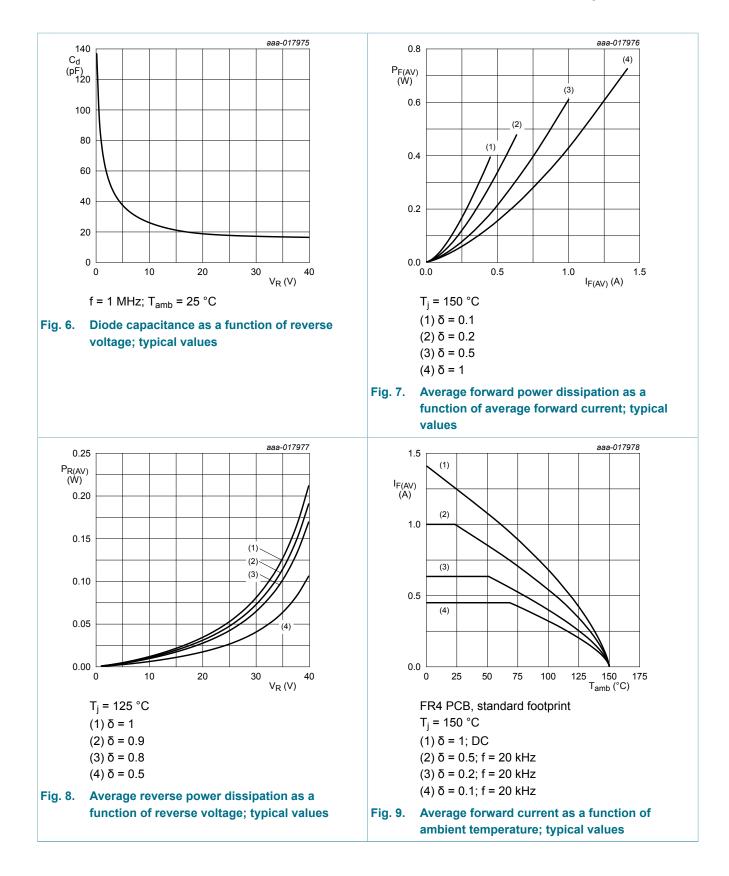
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V_R = 40 V; $t_p \le 3$ ms; $\delta \le 0.3$; T _j = 25 °C	-	13	40	μA
C _d dia	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	75	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	22	-	pF
t _{rr}	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	2.9	-	ns



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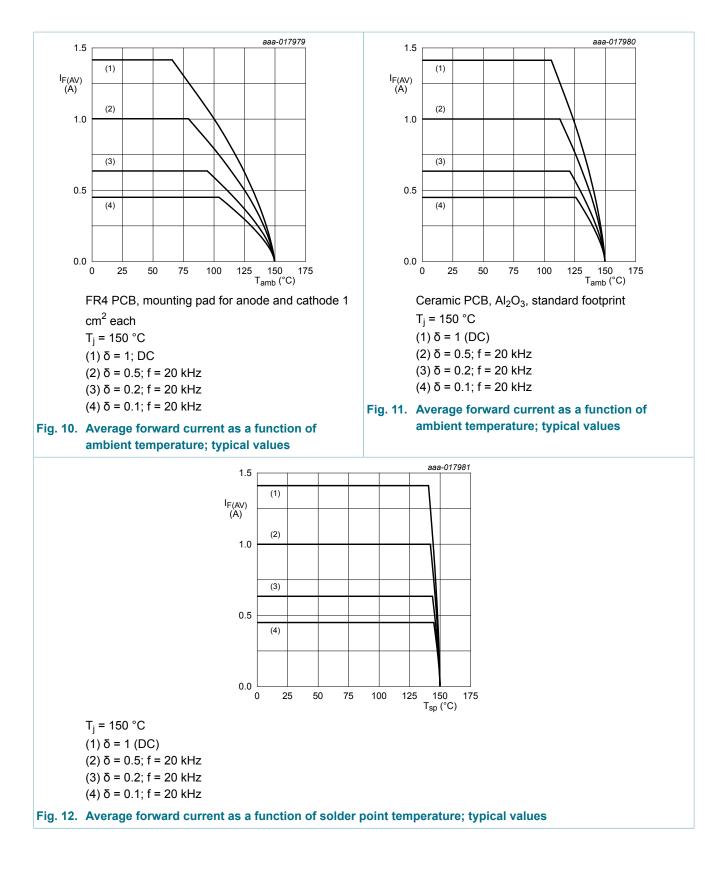
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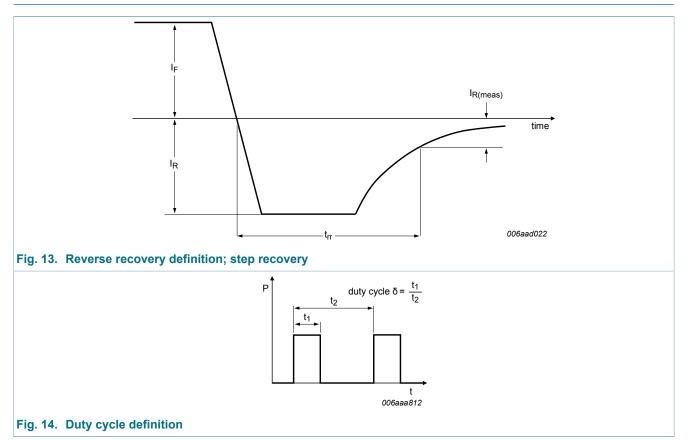
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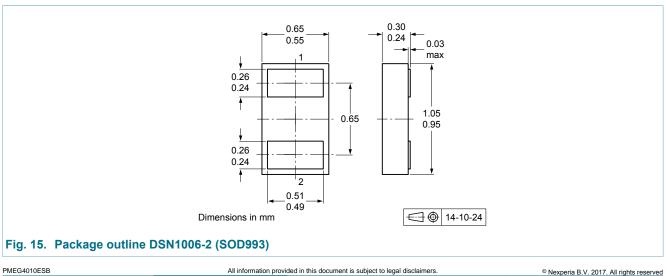
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11. Test information



The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

12. Package outline

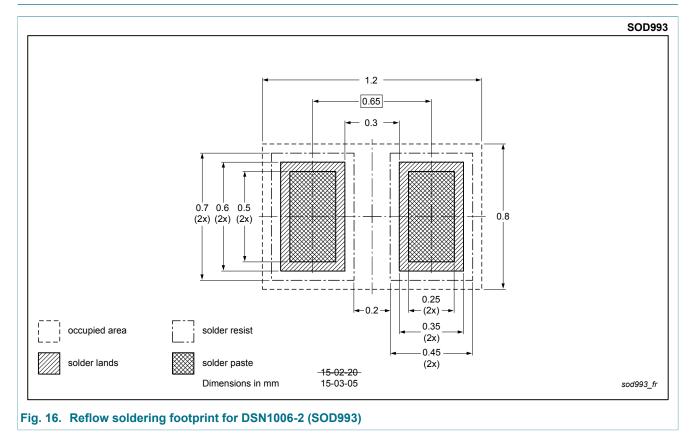


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13. Soldering



14. Mounting

SOD993 is an ultra small Discretes Silicon No-leads (DSN) package allowing maximized utilization of the package area for active silicon. Due to the special product design, Nexperia investigated the board assembly process parameters. In order to have an optimum soldering quality, Nexperia advises following the assembly recommendations explained in <u>AN11689</u>.

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15. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG4010ESB v.3	20151127	Product data sheet	-	PMEG4010ESB v.2		
Modifications:	Added section "Mou					
PMEG4010ESB v.2	20150624	Product data sheet	-	PMEG4010ESB v.1		
PMEG4010ESB v.1	20150512	Preliminary data sheet	-	-		

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16. Legal information

16.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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