



PMEG4010ER

1 A low VF MEGA Schottky barrier rectifier

13 October 2017

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 1$ A
- Reverse voltage: $V_R \leq 40$ V
- Low forward voltage
- High power capability due to clip-bond technology
- AEC-Q101 qualified
- Small and flat lead SMD plastic package
- Capable for reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data



Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5$; $f = 20$ kHz; $T_{amb} \leq 115$ °C; square wave	[1]	-	-	1	A
		$\delta = 0.5$; $f = 20$ kHz; $T_{sp} \leq 140$ °C; square wave		-	-	1	A
V_R	reverse voltage	$T_j = 25$ °C		-	-	40	V
V_F	forward voltage	$I_F = 1$ A; $T_j = 25$ °C		-	430	490	mV
I_R	reverse current	$V_R = 40$ V; $T_j = 25$ °C		-	10	50	μA

[1] Device mounted on a ceramic PCB, Al_2O_3 , standard footprint.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 CFP3 (SOD123W)	 <i>sym001</i>
2	A	anode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG4010ER	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4010ER	BD

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	$T_j = 25\text{ °C}$		-	40	V
$I_{F(AV)}$	average forward current	$\delta = 0.5$; $f = 20\text{ kHz}$; $T_{amb} \leq 115\text{ °C}$; square wave	[1]	-	1	A
		$\delta = 0.5$; $f = 20\text{ kHz}$; $T_{sp} \leq 140\text{ °C}$; square wave		-	1	A
I_{FSM}	non-repetitive peak forward current	$t_p = 8\text{ ms}$; $T_{j(init)} = 25\text{ °C}$; square wave		-	50	A
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[2]	-	0.57	W
			[3]	-	0.95	W
			[1]	-	1.8	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on a ceramic PCB, Al_2O_3 , standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm^2 .

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	220	K/W
			[3] [2]	-	-	130	K/W
			[4] [2]	-	-	70	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[5]	-	-	18	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm^2 .

[4] Device mounted on a ceramic PCB, Al_2O_3 , standard footprint.

[5] Soldering point of cathode tab.

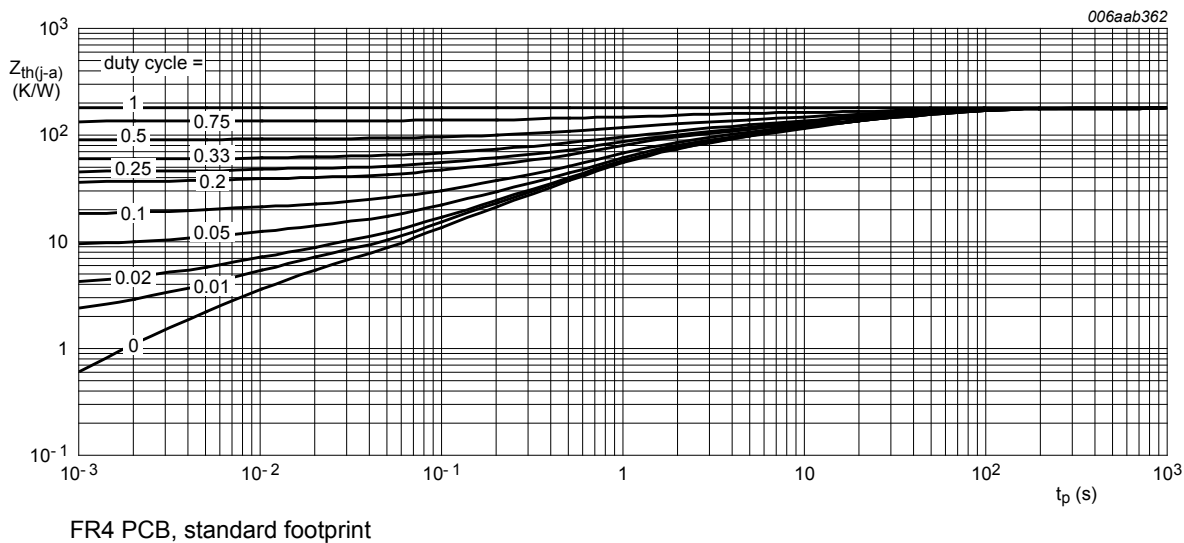


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

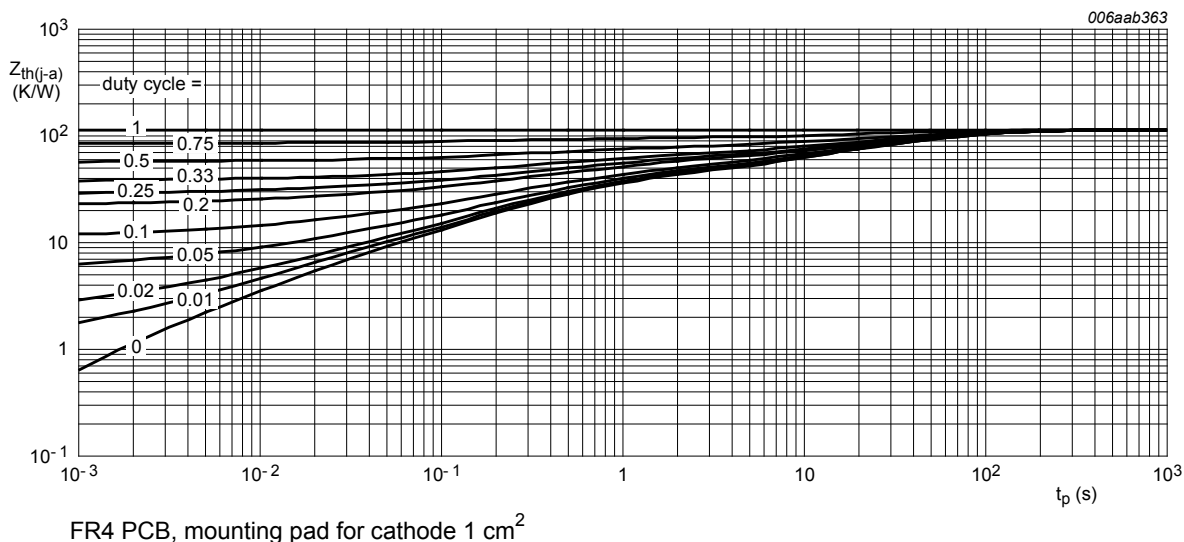
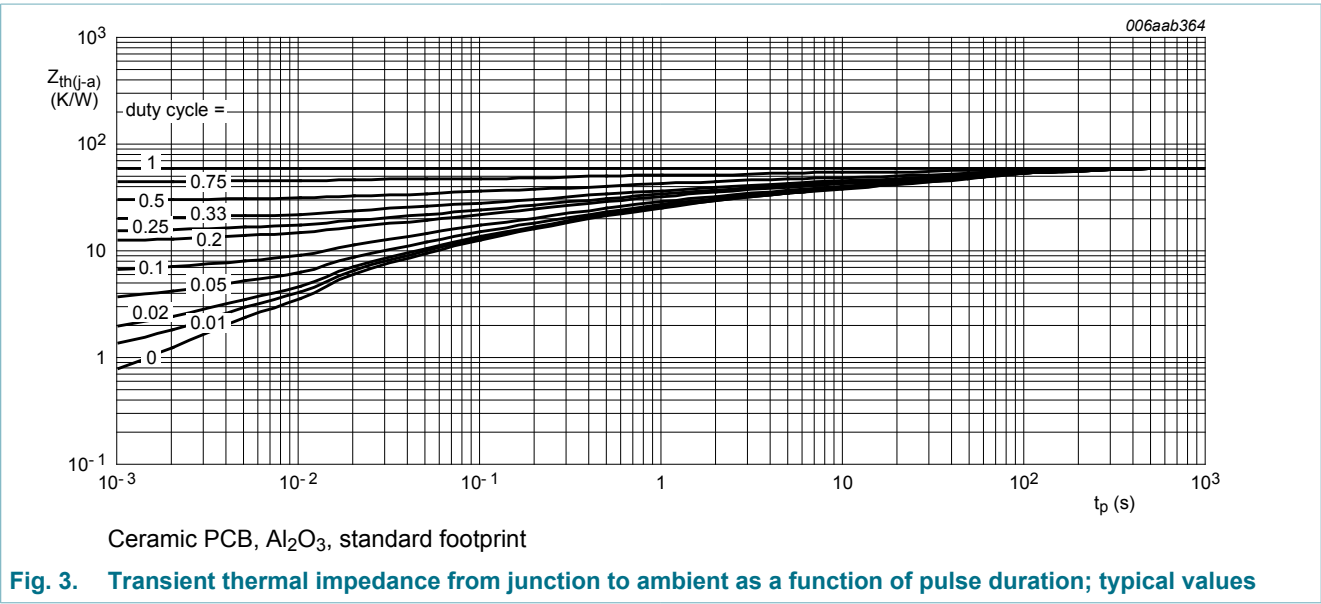


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _F	forward voltage	I _F = 0.1 A; T _j = 25 °C	-	310	360	mV
		I _F = 1 A; T _j = 25 °C	-	430	490	mV
I _R	reverse current	V _R = 10 V; T _j = 25 °C	-	3	13	µA
		V _R = 40 V; T _j = 25 °C	-	10	50	µA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	130	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	50	-	pF

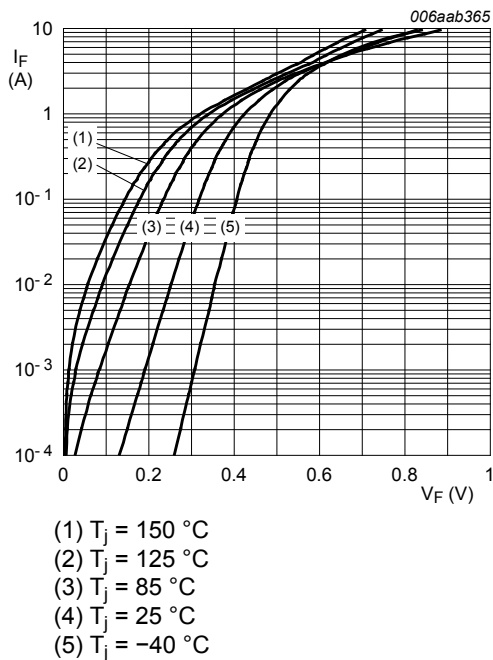


Fig. 4. Forward current as a function of forward voltage; typical values

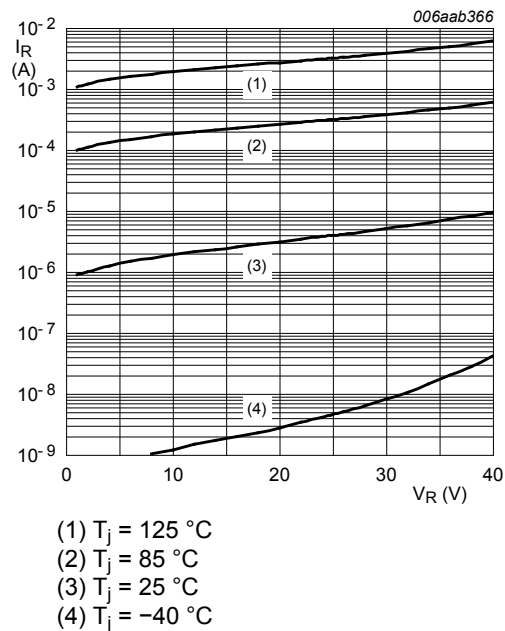


Fig. 5. Reverse current as a function of reverse voltage; typical values

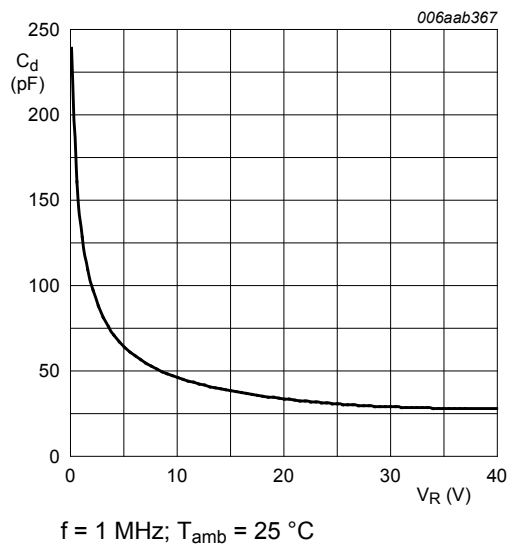


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

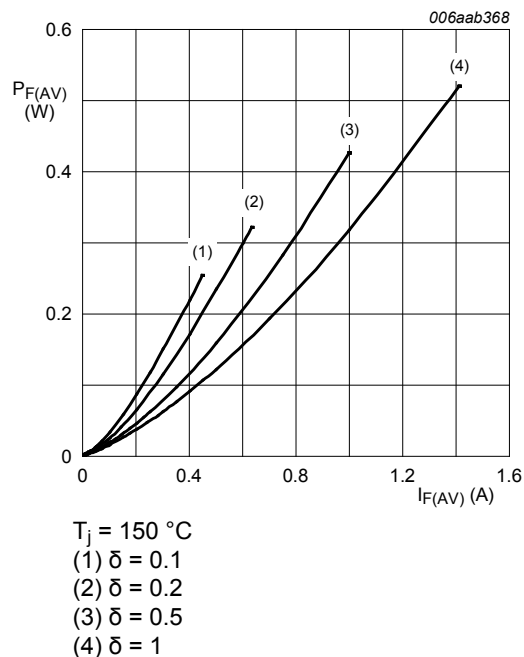


Fig. 7. Average forward power dissipation as a function of average forward current; typical values

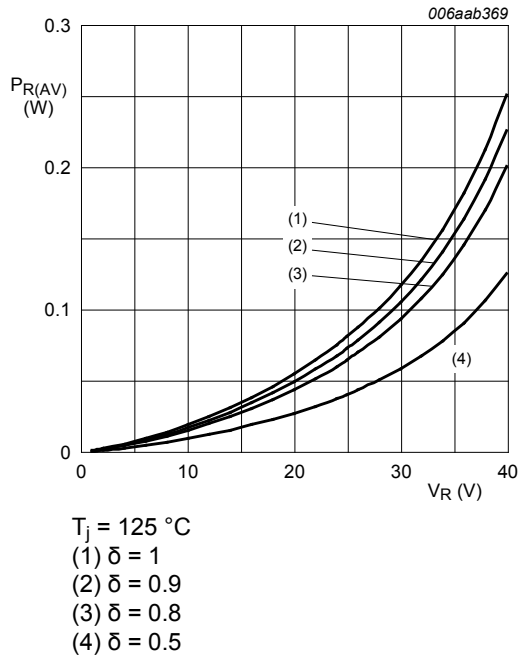


Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values

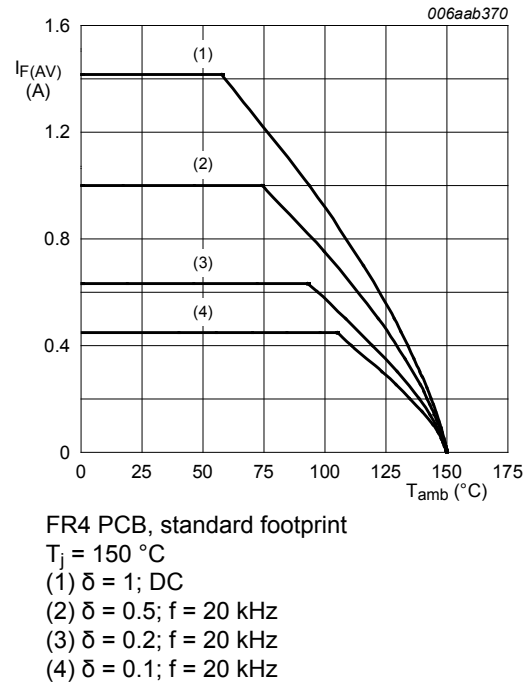


Fig. 9. Average forward current as a function of ambient temperature; typical values

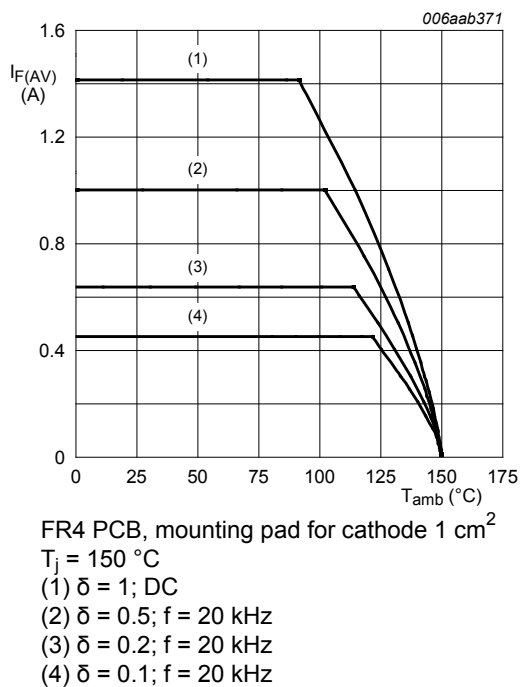


Fig. 10. Average forward current as a function of ambient temperature; typical values

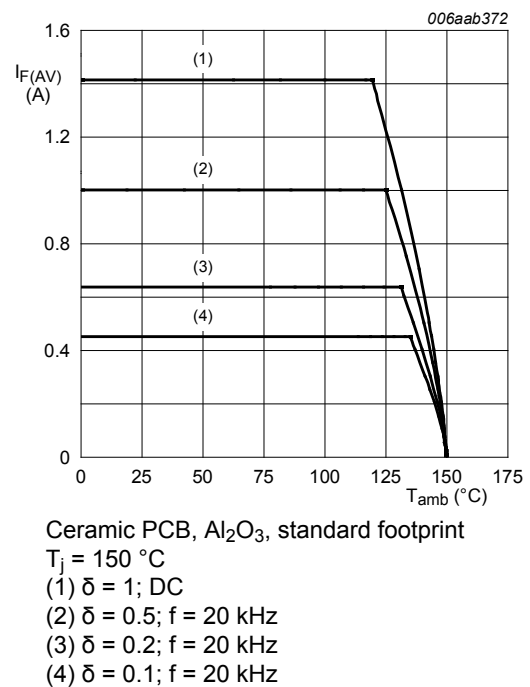
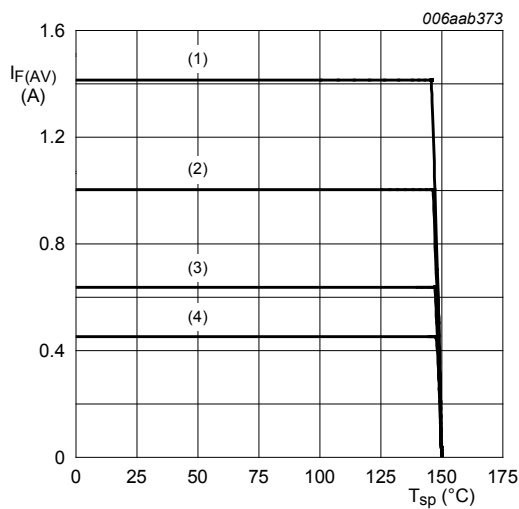


Fig. 11. Average forward current as a function of ambient temperature; typical values



$T_j = 150$ °C

(1) $\delta = 1$; DC

(2) $\delta = 0.5$; $f = 20$ kHz

(3) $\delta = 0.2$; $f = 20$ kHz

(4) $\delta = 0.1$; $f = 20$ kHz

Fig. 12. Average forward current as a function of solder point temperature; typical values

11. Test information

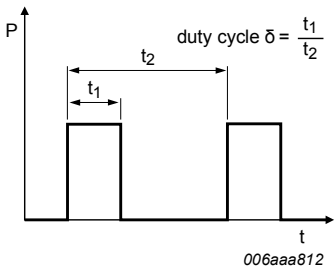


Fig. 13. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:
 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

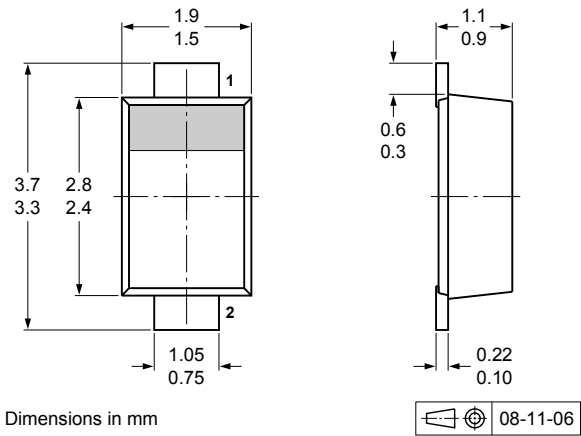


Fig. 14. Package outline CFP3 (SOD123W)

13. Soldering

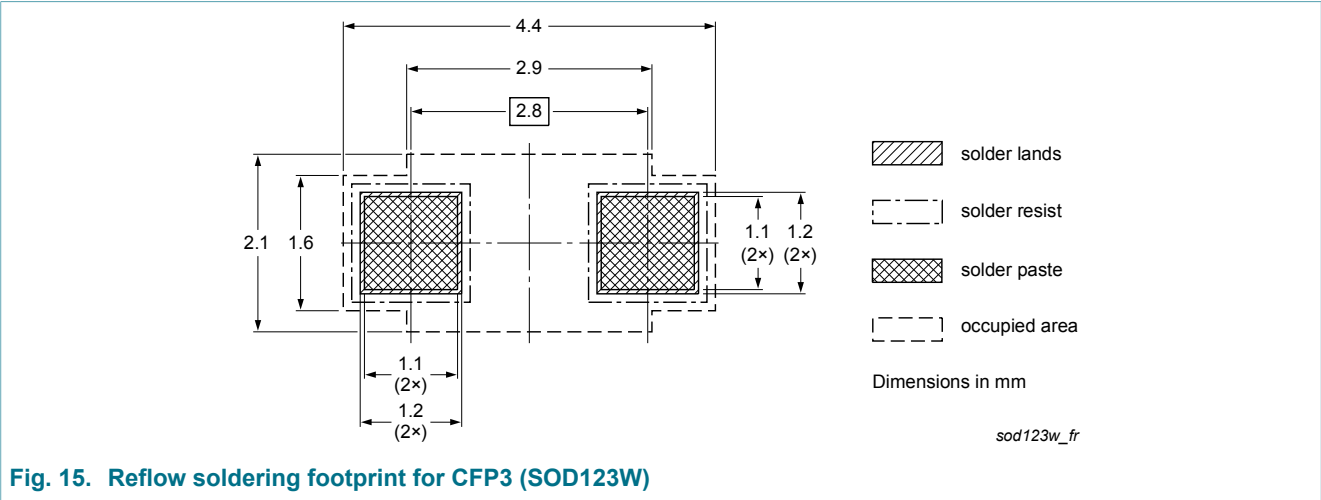


Fig. 15. Reflow soldering footprint for CFP3 (SOD123W)

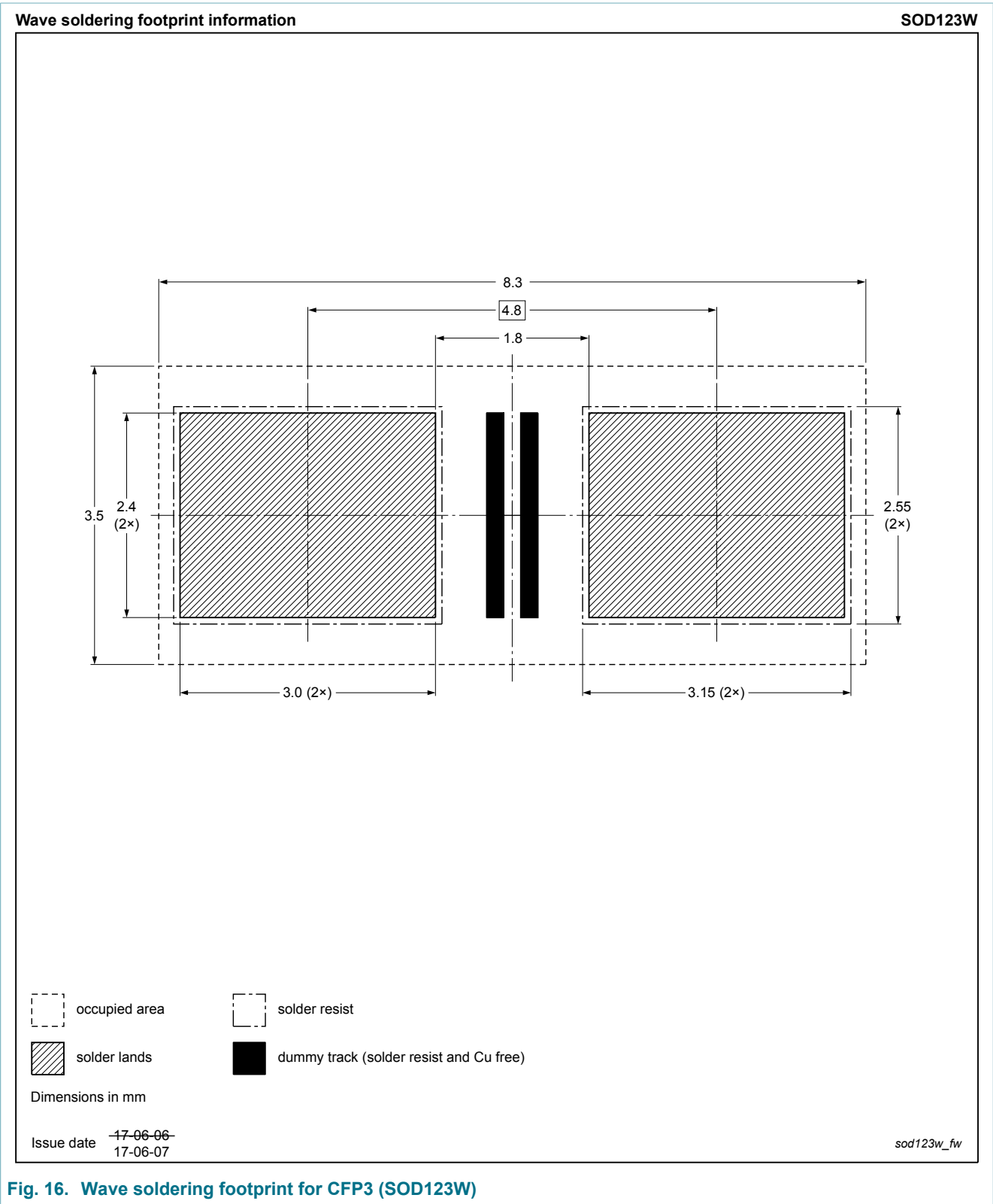


Fig. 16. Wave soldering footprint for CFP3 (SOD123W)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4010ER v.3	20171013	Product data sheet	-	PMEG4010ER_2
Modifications:	<ul style="list-style-type: none">Features and benefits: Capable for reflow and wave soldering addedSoldering: Wave soldering footprint added			
PMEG4010ER_2	20100415	Product data sheet	-	PMEG4010ER_1
PMEG4010ER_1	20081209	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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