

PMEG3015EV

30 V, 1.5 A ultra low V_{F} MEGA Schottky barrier rectifier in SOT666 package

Rev. 02 — 4 February 2010

Product data sheet

1. Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in an ultra small SMD SOT666 plastic package.

1.2 Features

Forward current: 1.5 AReverse voltage: 30 VUltra low forward voltage

Ultra small SMD packages

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Voltage clamping
- Inverse polarity protection
- Low power consumption applications

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _F	forward current	$T_{sp} \le 55 ^{\circ}C$	-	-	1.5	Α
V_R	reverse voltage		-	-	30	V
V _F	forward voltage	I _F = 1.5 A	<u>[1]</u> _	480	550	mV

[1] Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$



2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Symbol
1	cathode		
2	cathode	6 5 4	1, 2 5, 6 3, 4
3	anode		sym038
4	anode		
5	cathode		
6	cathode	1 2 3	

3. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG3015EV	-	plastic surface mounted package; 6 leads	SOT666		

4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG3015EV	1A

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{R}	reverse voltage		-	30	V
I _F	forward current	$T_{sp} \le 55 ^{\circ}C$	-	1.5	А
I _{FRM}	repetitive peak forward current	$t_p \leq 1 \text{ ms; } \delta \leq 0.25$	[1] -	4.5	А
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; square wave	[1] -	9.5	Α
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	[2] _	0.31	W
			[3]	0.58	W
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T_{stg}	storage temperature		-65	+150	°C

^[1] For SOT666 only valid, if pins 3 and 4 are connected in parallel.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1][2]			
			[3]	-	405	K/W
			<u>[4]</u> _	-	215	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	-	80	K/W

^[1] For Schottky barrier diodes thermal run-away has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and $I_{F(AV)}$ rating will be available on request.

^[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for cathode 1cm².

^[2] Reflow soldering is the only recommended soldering method.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for cathode 1cm².

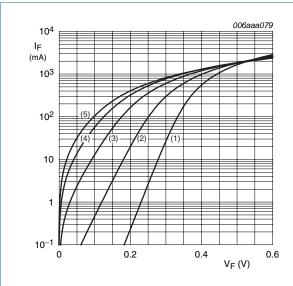
7. Characteristics

Table 7. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified.

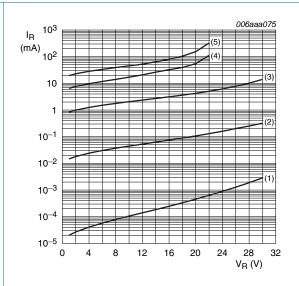
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{F}	forward voltage	$I_F = 1 \text{ mA}$	<u>[1]</u> -	125	160	mV
		$I_F = 10 \text{ mA}$	<u>[1]</u> -	185	220	mV
		$I_F = 100 \text{ mA}$	<u>[1]</u> _	255	290	mV
		I _F = 500 mA	<u>[1]</u> -	340	380	mV
		I _F = 1 A	<u>[1]</u> _	410	480	mV
		$I_F = 1.5 A$	<u>[1]</u> _	480	550	mV
I_R	reverse current	V _R = 10 V	-	60	150	μΑ
		$V_R = 30 \text{ V}$	-	400	1000	μΑ
C _d	diode capacitance	$V_R = 1 V$; $f = 1 MHz$	-	60	72	pF

^[1] Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$



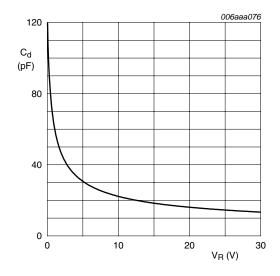
- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 85 \, ^{\circ}C$
- (4) $T_{amb} = 125 \, ^{\circ}C$
- (5) $T_{amb} = 150 \, ^{\circ}C$

Fig 1. Forward current as a function of forward voltage; typical values



- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = 25 \, ^{\circ}C$
- (3) $T_{amb} = 85 \, ^{\circ}C$
- (4) $T_{amb} = 125 \, ^{\circ}C$
- (5) T_{amb} = 150 °C

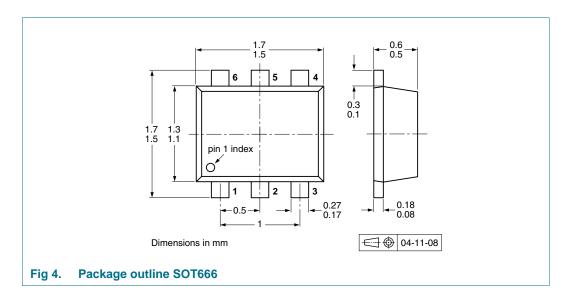
Fig 2. Reverse current as a function of reverse voltage; typical values



 $T_{amb} = 25 \, ^{\circ}C; f = 1 \, MHz$

Fig 3. Diode capacitance as a function of reverse voltage; typical values

8. Package outline



9. Packing information

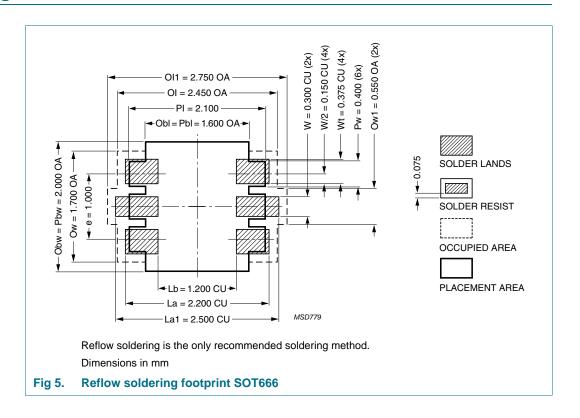
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			4000
PMEG3015EV	SOT666	4 mm pitch, 8 mm tape and reel	-115

[1] For further information and the availability of packing methods, see Section 13.

10. Soldering





11. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3015EV_2	20100204	Product data sheet	-	PMEG3015EV_1
Modifications:	 This data sheet was changed to reflect the new company name NXP Semiconductor including new legal definitions and disclaimers. No changes were made to the techn content. 			
PMEG3015EV_1	20050404	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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