**Product data sheet** 

## 1. General description

PNP switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

NPN complement: PMBT4401

### 2. Features and benefits

- High current (max. 600 mA)
- Collector-emitter voltage V<sub>CEO</sub> = 40 V
- AEC-Q101 qualified

### 3. Applications

· Switching and linear amplification

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-40	V
I <sub>C</sub>	collector current		-	-	-600	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -2 V; $I_{C}$ = -150 mA; $T_{amb}$ = 25 °C	100	-	300	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	Е	emitter		C
3	С	collector		В—
				E sym132
			SOT23	3911132



**PNP** switching transistor

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PMBT4403		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PMBT4403	%2T

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

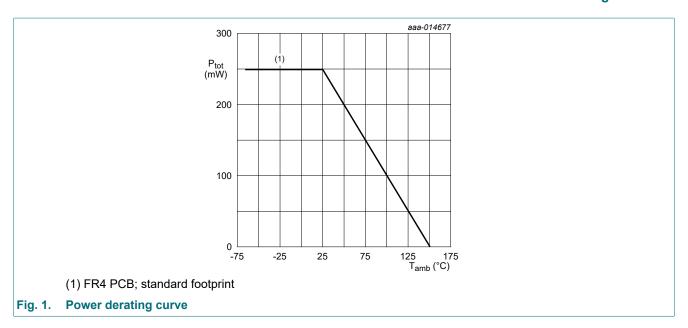
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-40	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-40	V
$V_{EBO}$	emitter-base voltage	open collector		-	-5	V
I <sub>C</sub>	collector current			-	-600	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-800	mA
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.

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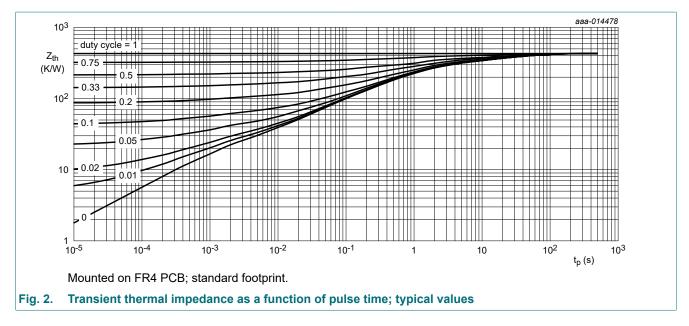


### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



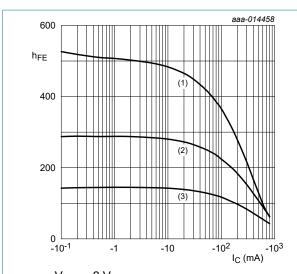
### **PNP** switching transistor

# 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = -40 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-50	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -0.1 mA; T <sub>amb</sub> = 25 °C	30	-	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -1 mA; T <sub>amb</sub> = 25 °C	60	-	-	
		V <sub>CE</sub> = -1 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C	100	-	-	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -150 mA; T <sub>amb</sub> = 25 °C	100	-	300	
		V <sub>CE</sub> = -2 V; I <sub>C</sub> = -500 mA; T <sub>amb</sub> = 25 °C	20	-	-	
V <sub>CEsat</sub> collector-emitter saturation voltage		I <sub>C</sub> = -150 mA; I <sub>B</sub> = -15 mA; T <sub>amb</sub> = 25 °C	-	-	-400	mV
	saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA; T <sub>amb</sub> = 25 °C	-	-	-750	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = -150 mA; I <sub>B</sub> = -15 mA; T <sub>amb</sub> = 25 °C	-	-	-950	mV
		I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA; T <sub>amb</sub> = 25 °C	-	-	-1.3	V
t <sub>d</sub>	delay time	I <sub>C</sub> = -150 mA; I <sub>Bon</sub> = -15 mA;	-	-	15	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = 15 mA; T <sub>amb</sub> = 25 °C	-	-	30	ns
t <sub>on</sub>	turn-on time		-	-	40	ns
t <sub>s</sub>	storage time		-	-	300	ns
t <sub>f</sub>	fall time		-	-	50	ns
t <sub>off</sub>	turn-off time		-	-	350	ns
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	8.5	pF
C <sub>e</sub>	emitter capacitance	$V_{EB}$ = -500 mV; $I_{C}$ = 0 A; $I_{c}$ = 0 A; $I_{c}$ = 0 A; $I_{c}$ = 0 A;	-	-	35	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -10 V; $I_{C}$ = -20 mA; f = 100 MHz; $T_{amb}$ = 25 °C	200	-	-	MHz

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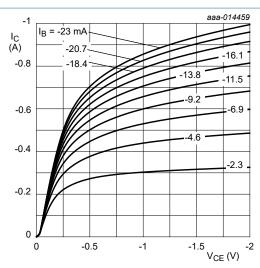


$$V_{CE} = -2 V$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

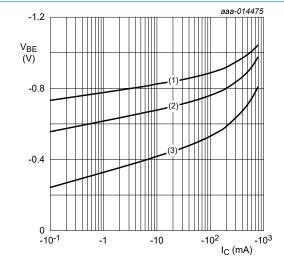
(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 3. DC current gain as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 4. Collector current as a function of collectoremitter voltage; typical values



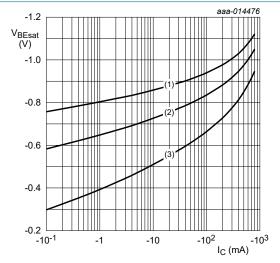
$$V_{CE}$$
 = -2  $V$ 

(1) 
$$T_{amb} = -55 \,^{\circ}C$$
  
(2)  $T_{amb} = 25 \,^{\circ}C$ 

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

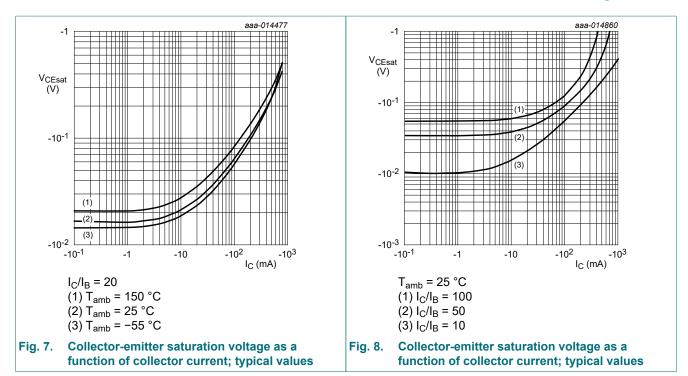
(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

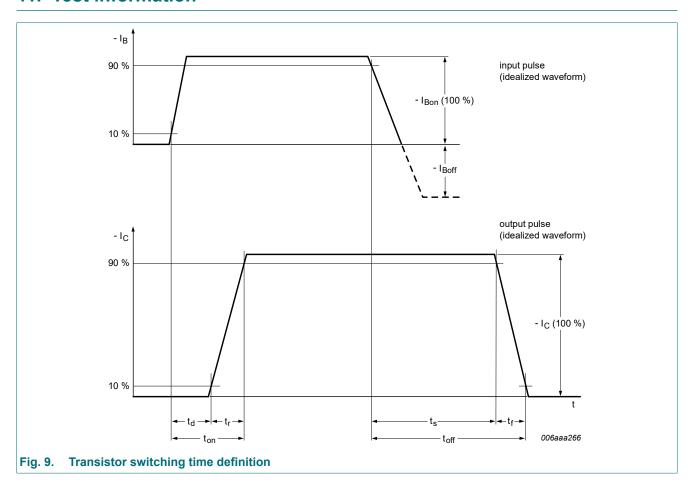
$$(3) T_{amb} = 150 °C$$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

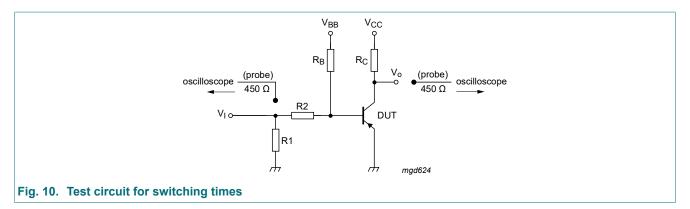
### PNP switching transistor



### 11. Test information



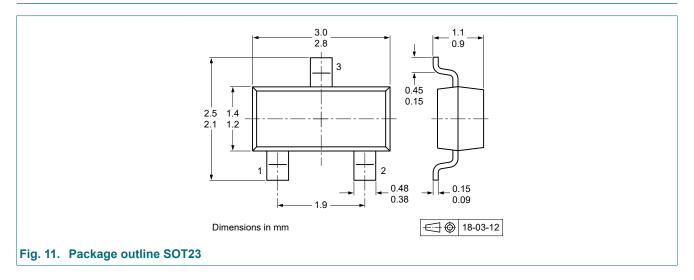
### **PNP** switching transistor



#### **Quality information**

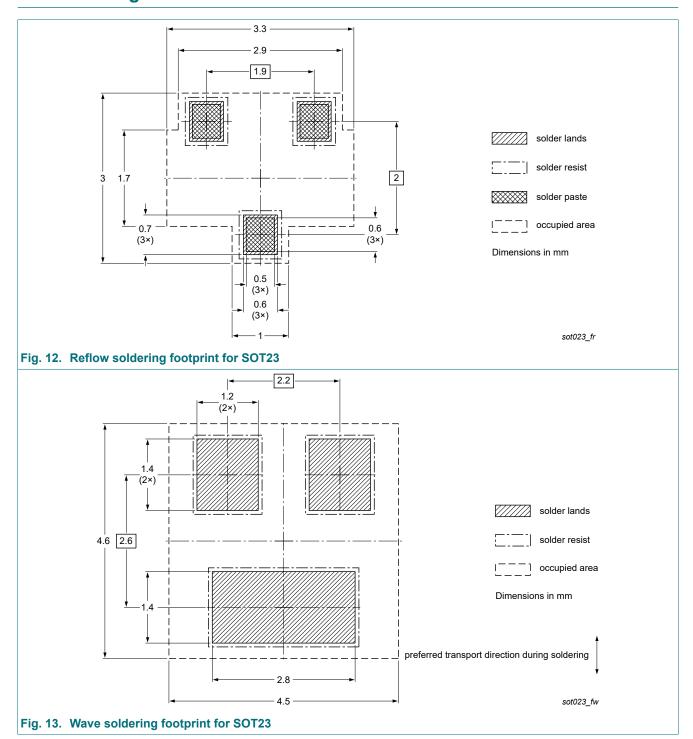
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline



### **PNP** switching transistor

# 13. Soldering



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# 14. Revision history

### **Table 8. Revision history**

Table 6. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMBT4403 v.7	20241112	Product data sheet	-	PMBT4403 v.6			
Modifications:	Data sheet turne	ed back to automotive qualifi	cation				
PMBT4403 v.6	20241008	Product data sheet	-	PMBT4403 v.5			
PMBT4403 v.5	20150305	Product data sheet	-	PMBT4403 v.4			
PMBT4403 v.4	20040121	Product data sheet	-	PMBT4403 v.3			
PMBT4403 v.3	19990415	Product specification	-	PMBT4403 v.2			
PMBT4403 v.2	19970505	Product specification	-	PMBT4403 v.1			
PMBT4403 v.1	19940901	Product specification	-	-			

### PNP switching transistor

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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