

isc Silicon PNP Transistor

PMBT3906

DESCRIPTION

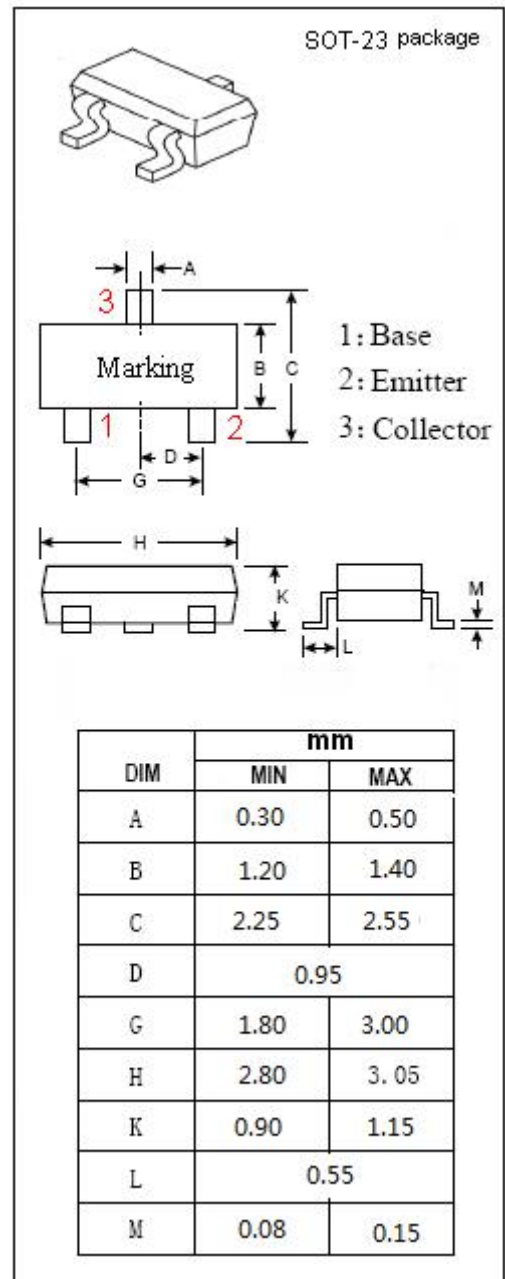
- PNP switching transistor in a SOT23 plastic package
- NPN complement:PMBT3904
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for telephony and professional communication equipment

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-40	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-100	mA
I_{CM}	Peak Collector Current	-200	mA
P_C	Collector Power Dissipation @ $T_c=75^{\circ}\text{C}$	0.25	W
T_J	Junction Temperature	-65~150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-65~150	$^{\circ}\text{C}$



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ELECTRICAL CHARACTERISTICS

 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}; I_C = 0$			-100	nA
I_{CBO}	Collector Cutoff Current	$V_{CB} = -40\text{V}; I_E = 0$			-100	nA
h_{FE-1}	DC Current Gain	$I_C = -10\text{mA}; V_{CE} = -1\text{V}$	100		300	
h_{FE-2}	DC Current Gain	$I_C = -50\text{mA}; V_{CE} = -1\text{V}$	60			
h_{FE-3}	DC Current Gain	$I_C = -100\text{mA}; V_{CE} = -1\text{V}$	30			
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -50\text{mA}; I_B = -5\text{mA}$			-0.3	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = -50\text{mA}; I_B = -5\text{mA}$			-0.95	V
f_T	Current-Gain—Bandwidth Product	$I_C = -10\text{mA}; V_{CE} = -20\text{V}; f = 100\text{MHz}$	250			MHz

Switching times

t_d	Delay Time	$I_C = -10\text{mA}; I_{B1} = -1\text{mA};$ $V_{BE(off)} = 0.5\text{V}; V_{CC} = -3\text{V}$			35	ns
t_r	Rise Time				35	ns
t_{stg}	Storage Time	$I_C = -10\text{mA}; I_{B1} = -1\text{mA}; I_{B2} = 1\text{mA}$ $V_{CC} = -3\text{V}$			225	ns
t_f	Fall Time				75	ns

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