

1. General description

NPN single switching transistor in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

PNP complement: PMBT3906MB.

2. Features and benefits

- Single general-purpose switching transistor
- Ultra small SMD plastic package
- Board-space reduction
- Low package height of 0.37 mm
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- Mobile applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	200	mA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 10 mA	100	180	300	



5. Pinning information

Table 2	. Pinning in	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		с
2	E	emitter		
3	С	collector	Transparent	B fa E
			DFN1006B-3 (SOT883B)	sym021

6. Ordering information

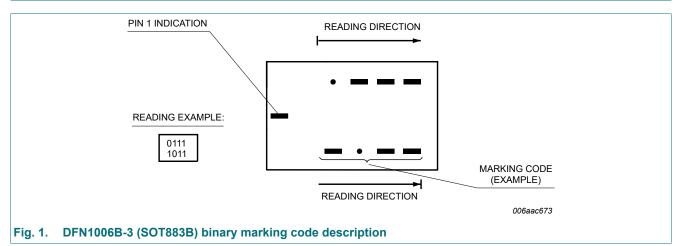
Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMBT3904MB		plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMBT3904MB	0100 0111



8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
Ic	collector current			-	200	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	200	mA
I _{BM}	peak base current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	250	mW
			[1] [3]	-	590	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Reflow soldering is the only recommended soldering method.

[2] Device mounted on an FR4 Printed-Circuit board (PCB), single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

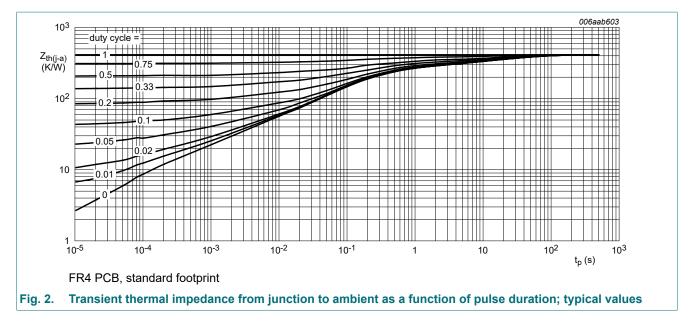
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	500	K/W
			[1] [3]	-	-	212	K/W

[1] Reflow soldering is the only recommended soldering method.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



10. Characteristics

Table 7. Characteristics

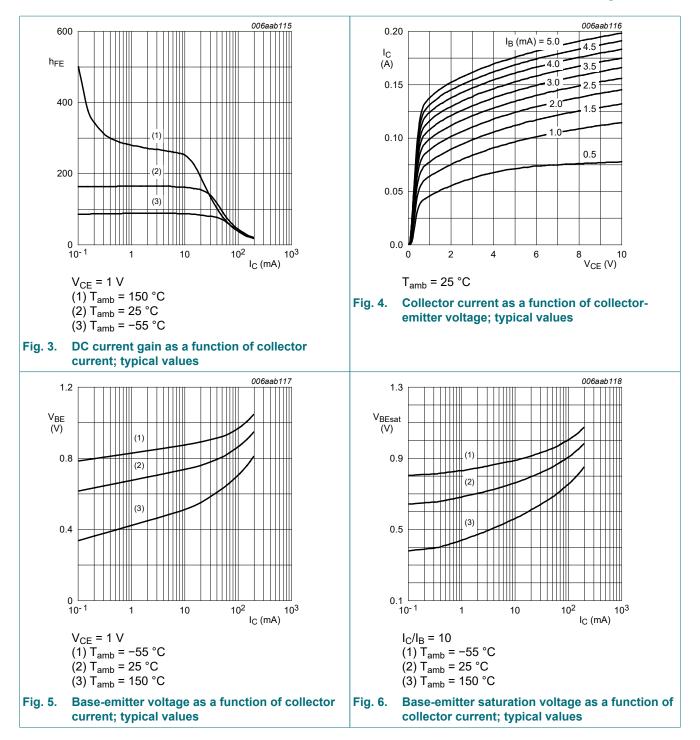
 T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A		-	-	50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 6 V; I _C = 0 A		-	-	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 0.1 mA		60	180	-	
		V _{CE} = 1 V; I _C = 1 mA		80	180	-	
		V _{CE} = 1 V; I _C = 10 mA		100	180	300	
		V _{CE} = 1 V; I _C = 50 mA		60	105	-	
		V _{CE} = 1 V; I _C = 100 mA	[1]	30	50	-	
V _{CEsat}	collector-emitter	I _C = 10 mA; I _B = 1 mA		-	75	200	mV
	saturation voltage	I _C = 50 mA; I _B = 5 mA		-	120	300	mV
V _{BEsat}	base-emitter saturation	I _C = 10 mA; I _B = 1 mA		650	750	850	mV
	voltage	I _C = 50 mA; I _B = 5 mA		-	850	950	mV
t _d	delay time	$I_{C} = 10 \text{ mA}; I_{Bon} = 1 \text{ mA}; I_{Boff} = -1 \text{ mA};$		-	-	35	ns
t _r	rise time	$V_{CC} = 3 V$		-	-	35	ns
t _{on}	turn-on time			-	-	70	ns
t _s	storage time			-	-	200	ns
t _f	fall time	-		-	-	50	ns
t _{off}	turn-off time			-	-	250	ns
C _c	collector capacitance	V _{CB} = 5 V; I _E = 0 A; i _e = 0 A; f = 1 MHz		-	-	4	pF
C _e	emitter capacitance	V _{EB} = 500 mV; I _C = 0 A; i _c = 0 A; f = 1 MHz		-	-	8	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 10 mA; f = 100 MHz		300	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 100 μA; R _S = 1 kΩ; f = 10 Hz to 15.7 kHz		-	-	5	dB

[1] Pulsed test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$

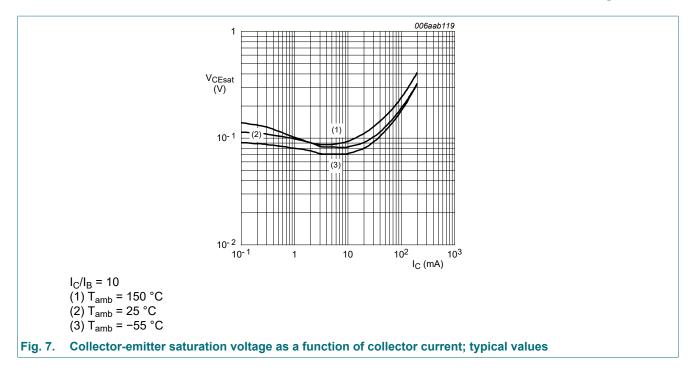
PMBT3904MB

40 V, 200 mA NPN switching transistor

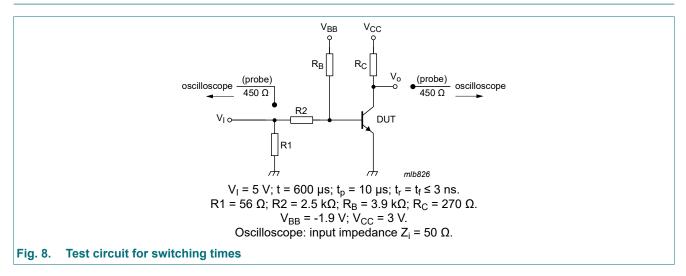


PMBT3904MB

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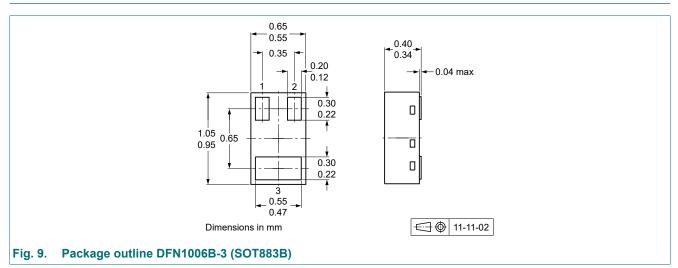
11. Test information



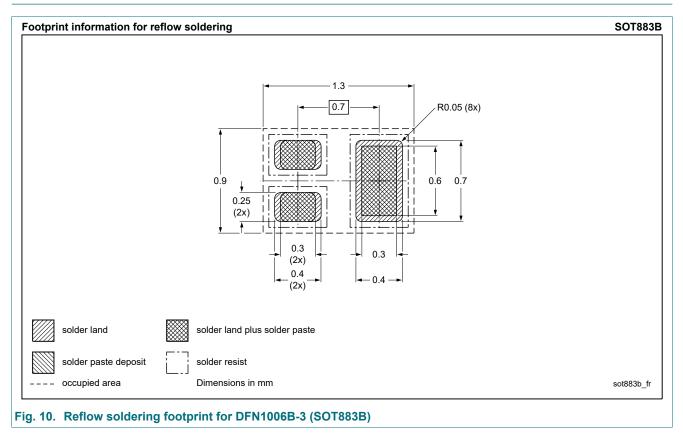
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMBT3904MB v.2	20181116	Product data sheet	-	PMBT3904MB v.1				
Modifications:	of Nexperia.	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
PMBT3904MB v.1	20120307	Product data sheet	-	-				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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40 V, 200 mA NPN switching transistor

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Contents

1.	General description	.1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	.1
5.	Pinning information	.2
6.	Ordering information	.2
7.	Marking	.2
8.	Limiting values	3
9.	Thermal characteristics	4
10.	Characteristics	. 5
11.	Test information	.7
12	Package outline	8
	Soldering	
14	Revision history	.9
	Legal information1	

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