

PJSD03LCFN2

BI-DIRECTIONAL ESD PROTECTION DIODE

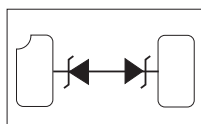
This bi-directional TVS has been designed to protect sensitive equipment against ESD and to prevent Latch-Up events in CMOS circuitry operating at 3.3Vdc and below. This offers an integrated solution to protect a single data line where the board space is a premium.

SPECIFICATION FEATURES

- 40W Power Dissipation (8/20μs Waveform)
- Low Leakage Current, Maximum of 2.5μA @3.3Vdc
- Very low Clamping voltage
- IEC 61000-4-2 ESD 30kV air, 30kV Contact Compliance
- In compliance with EU RoHS 2002/95/EC directives
- Terminals : Solderable per MIL-STD-750, Method 2026
- Case : DFN 2L, Plastic
- Marking : BS

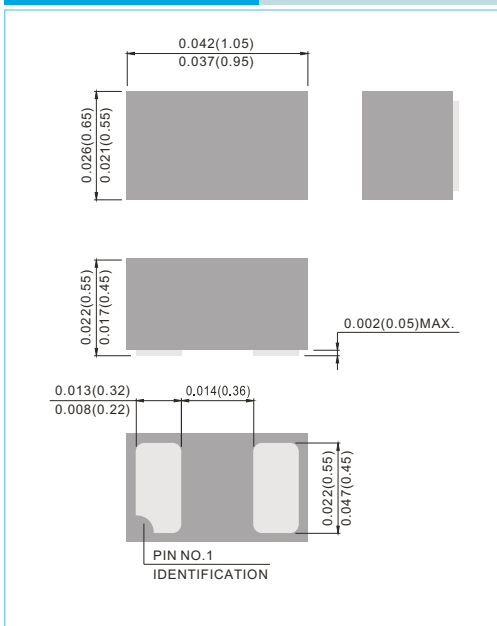
APPLICATIONS

- Video I/O ports protection
- Set Top Boxes
- Portable Instrumentation



DFN 2L

Unit : inch(mm)



MAXIMUM RATINGS

Rating	Symbol	Value	Units
Peak Pulse Power (8/20 μs Waveform)	P _{PP}	40	W
Peak Pulse Current (8/20 μs Waveform)	I _{PPM}	6	A
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS (T_J=25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V _{WRM}		-	-	3.3	V
Reverse Breakdown Voltage	V _{BR}	I _{BR} =1mA	5.4	-	7.0	V
Reverse Leakage Current	I _R	V _R =3.3V	-	-	2.5	μA
Clamping Voltage (8/20μs)	V _C	I _{PP} =6A	-	-	10	V
Off State Junction Capacitance	C _J	0 Vdc Bias f=1MHz	-	-	25	pF

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TYPICAL CHARACTERISTICS

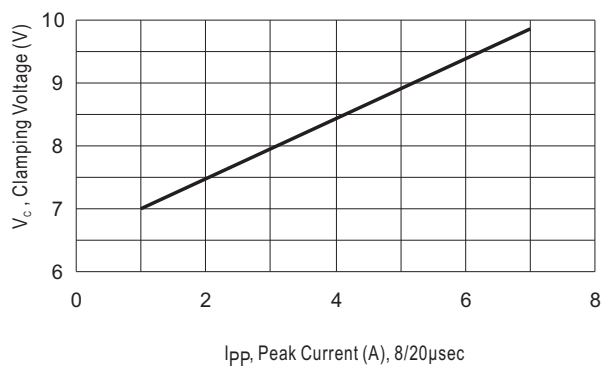


FIG. 1- TYPICAL PEAK CLAMPING VOLTAGE

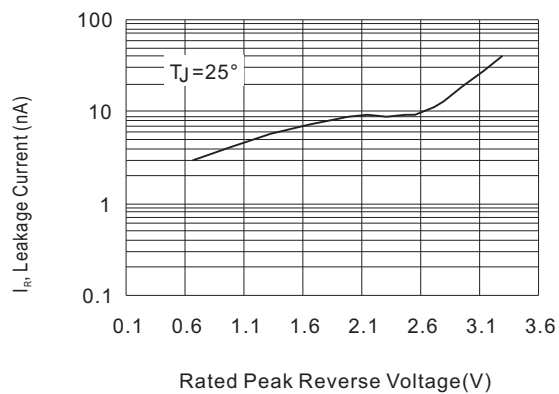


FIG. 2- TYPICAL REVERSE CHARACTERISTICS

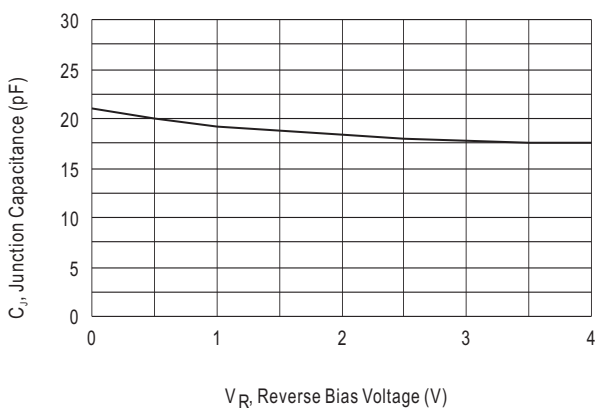
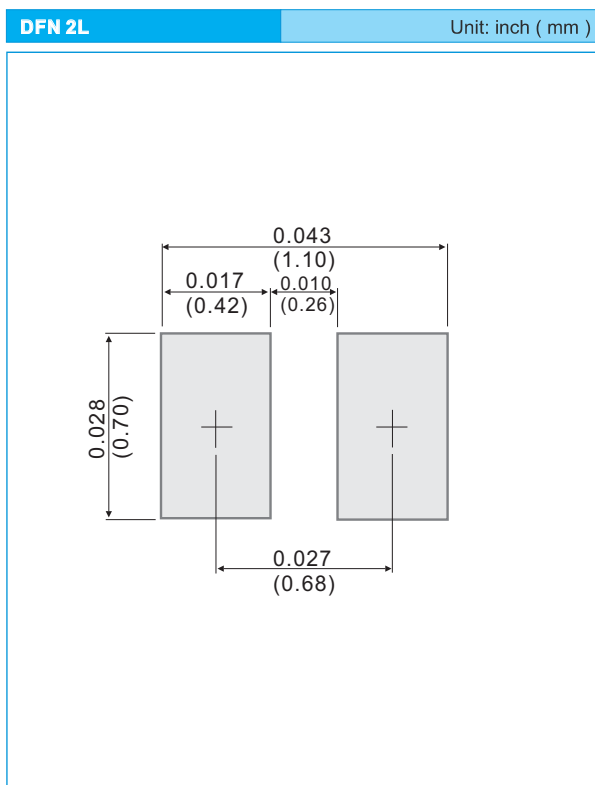


FIG. 3- Typical Junction Capacitance

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MOUNTING PAD LAYOUT



ORDER INFORMATION

- Packing information
T/R - 8K per 7" plastic Reel

LEGAL STATEMENT

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