PAN	JIT
	SEMI CONDUCTOR

### 30V N-Channel Enhancement Mode MOSFET

Current

3.9A

#### Features

Voltage

• RDS(ON), VGS@10V, ID@3.9A<48mΩ

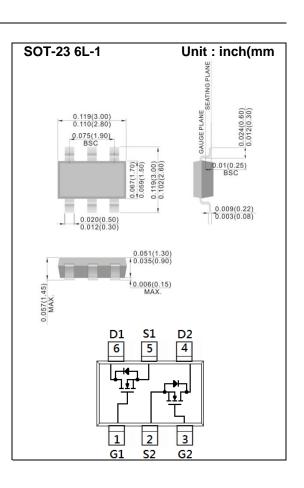
30 V

- RDS(ON) , VGS@4.5V, ID@3.2A<53mΩ
- RDS(ON) , VGS@2.5V, ID@2.5A<66mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.

(Halogen Free)

#### **Mechanical Data**

- Case: SOT-23 6L-1 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: ST0



### **Maximum Ratings and Thermal Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 12	V
Continuous Drain Current		I <sub>D</sub>	3.9	А
Pulsed Drain Current		I <sub>DM</sub>	15.6	А
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C
Typical Thermal resistance - Junction to Ambient <sup>(Note 3)</sup>		$R_{ extsf{ heta}JA}$	100	°C/W



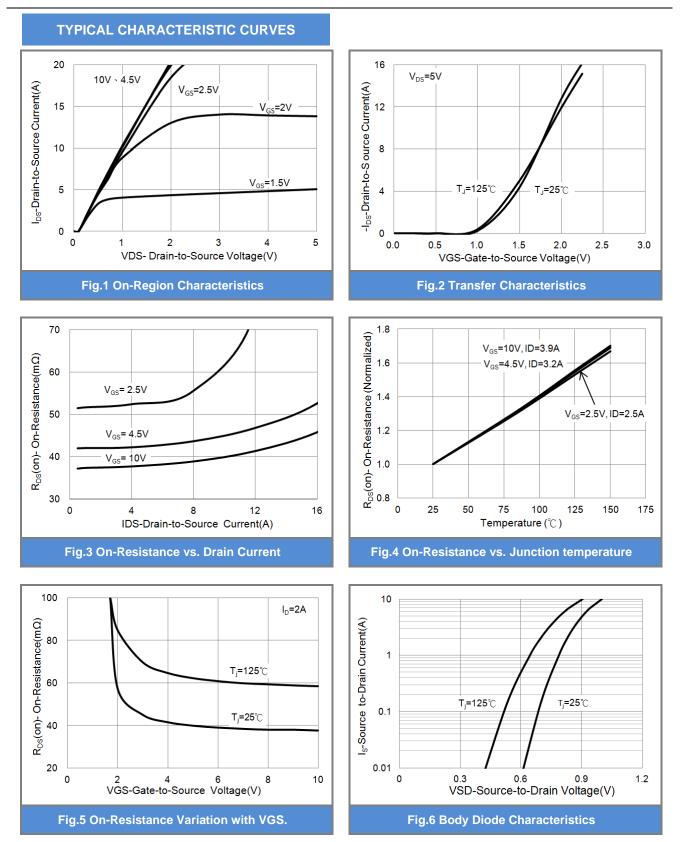
### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		1	1	T		
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}$ =0V, I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	0.4	0.72	1.2	V
Drain-Source On-State Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =3.9A	-	41	48	mΩ
	$R_{DS(on)}$	$V_{GS}$ =4.5V, I <sub>D</sub> =3.2A	-	44	53	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.5A	-	51	66	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V	-	0.01	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V <sub>DS</sub> =15V, I <sub>D</sub> =3.9A, V <sub>GS</sub> =10V <sup>(Note 1,2)</sup>	-	11.3	-	nC
Gate-Source Charge	$Q_gs$		-	1.2	-	
Gate-Drain Charge	$Q_{gd}$		-	1.6	-	
Input Capacitance	Ciss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V,	-	490	-	pF
Output Capacitance	Coss		-	44	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	32	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>		-	2	-	
Turn-On Rise Time	tr	$V_{DD}$ =15V, I <sub>D</sub> =3.9A, $V_{GS}$ =10V, $R_{G}$ =6 $\Omega$ <sup>(Note 1,2)</sup>	-	57	-	ns
Turn-Off Delay Time	td <sub>(off)</sub>		-	78	-	
Turn-Off Fall Time	tf	R <sub>G</sub> =012	-	79	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I <sub>S</sub>		-	-	1.5	А
Diode Forward Current						
Diode Forward Voltage	$V_{\text{SD}}$	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V	-	0.77	1.2	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R<sub>®JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





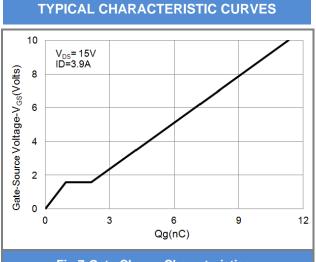


Fig.7 Gate-Charge Characteristics

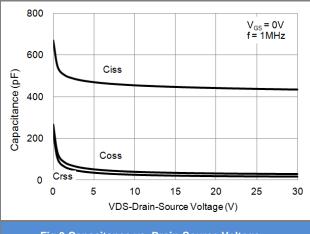
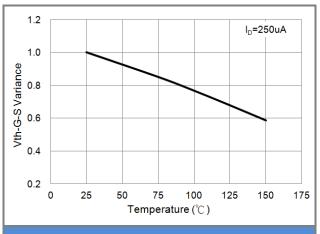


Fig.9 Capacitance vs. Drain-Source Voltage.





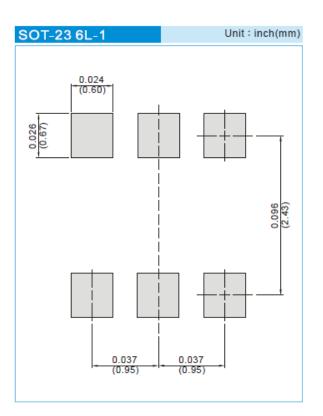




#### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJS6800_S1_00001	SOT-23 6L-1	3K pcs / 7" reel	ST0	Halogen free

#### MOUNTING PAD LAYOUT







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