

PJQ2800

20V N-Channel Enhancement Mode MOSFET

Voltage	20 V	Current	5.2A
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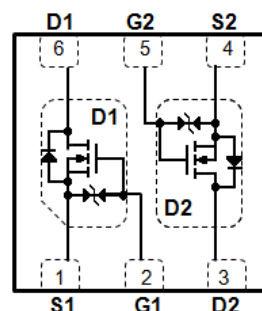
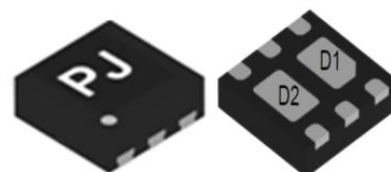
Features

- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@5.2A<32m\Omega$
- $R_{DS(ON)}$, $V_{GS}@2.5V$, $I_D@3.2A<45m\Omega$
- $R_{DS(ON)}$, $V_{GS}@1.8V$, $I_D@2.0A<65m\Omega$
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- ESD Protected
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case: DFN2020-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00032 ounces, 0.0093 grams
- Marking: 800

DFN2020-6L



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	±8	V
Continuous Drain Current		I _D	5.2	A
Pulsed Drain Current		I _{DM}	20.8	A
Power Dissipation	T _a =25°C	P _D	1.45	W
	Derate above 25°C		11.6	mW/°C
Operating Junction and Storage Temperature Range		T _J , T _{STG}	-55~150	°C
Typical Thermal resistance		R _{θJA}	86	°C/W
- Junction to Ambient ^(Note 3)				



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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.4	0.68	0.9	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =5.2A	-	24	32	mΩ
		V _{GS} =2.5V, I _D =3.2A	-	30	45	
		V _{GS} =1.8V, I _D =2.0A	-	40	65	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	-0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V	-	±3	±10	uA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =10V, I _D =5.2A, V _{GS} =4.5V (Note 1,2)	-	6.3	-	nC
Gate-Source Charge	Q _{gs}		-	1.2	-	
Gate-Drain Charge	Q _{gd}		-	1.0	-	
Input Capacitance	C _{iss}	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	515	-	pF
Output Capacitance	C _{oss}		-	60	-	
Reverse Transfer Capacitance	C _{rss}		-	47	-	
Switching						
Turn-On Delay Time	td _(on)	V _{DD} =10V, I _D =5.2A, V _{GS} =4.5V, R _G =6Ω (Note 1,2)	-	7	-	ns
Turn-On Rise Time	tr		-	43	-	
Turn-Off Delay Time	td _(off)		-	170	-	
Turn-Off Fall Time	tf		-	13	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	1.5	A
Diode Forward Voltage	V _{SD}	I _S =1.0A, V _{GS} =0V	-	0.77	1.2	V

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

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TYPICAL CHARACTERISTIC CURVES

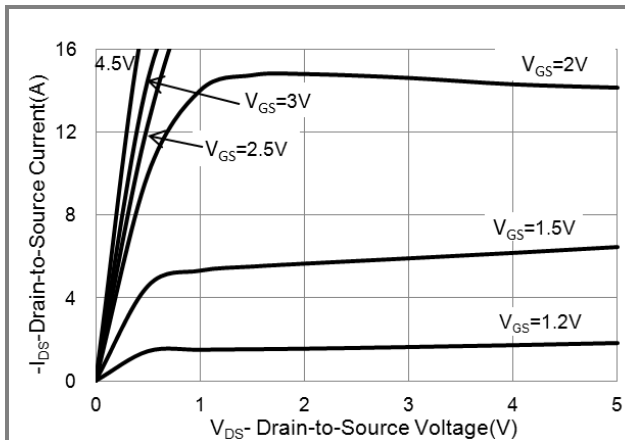


Fig.1 On-Region Characteristics

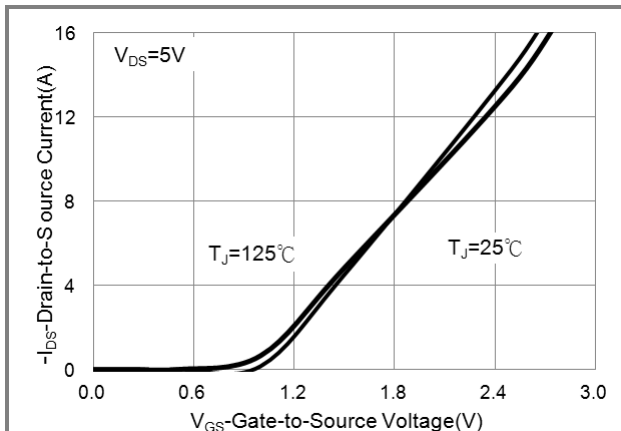


Fig.2 Transfer Characteristics

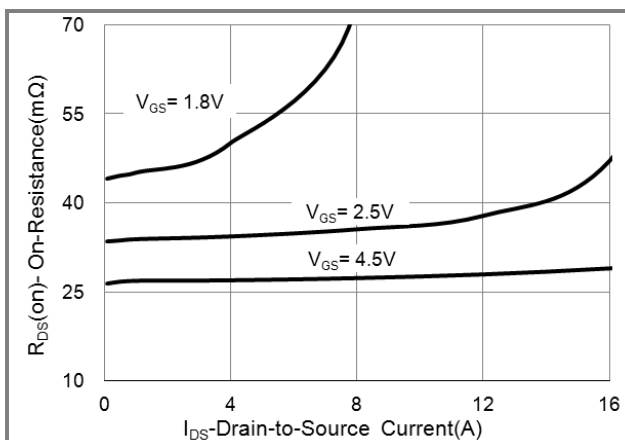


Fig.3 On-Resistance vs. Drain Current

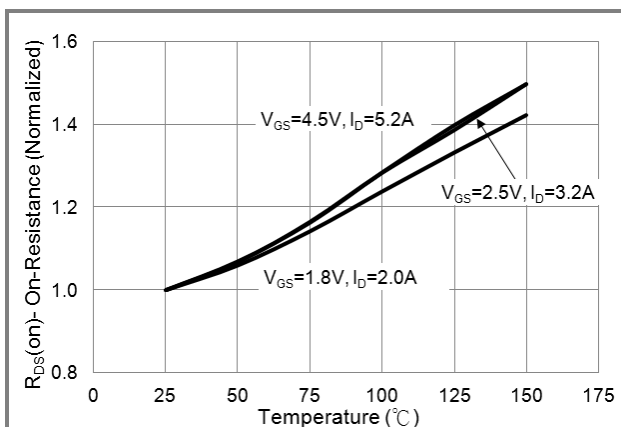


Fig.4 On-Resistance vs. Junction temperature

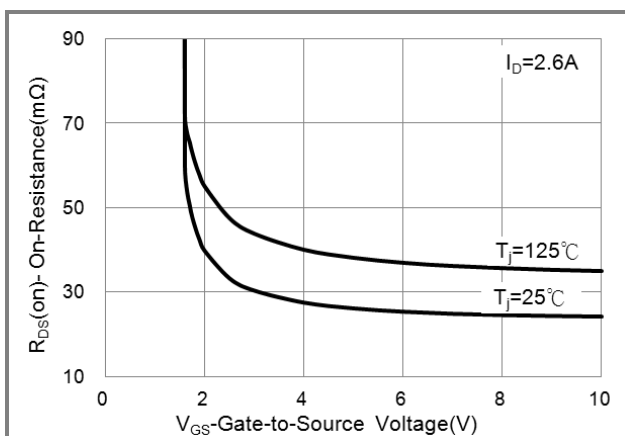


Fig.5 On-Resistance Variation with V_{GS} .

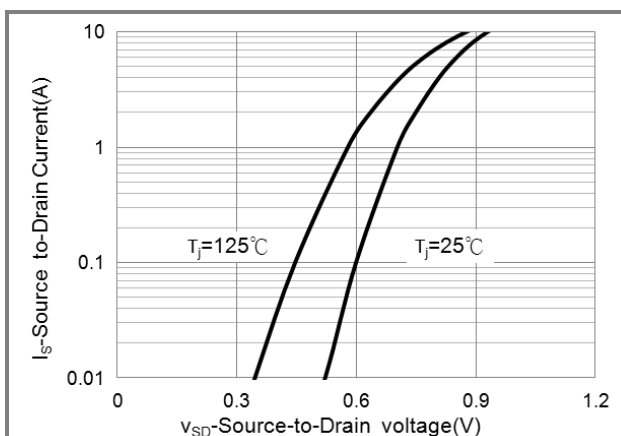


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

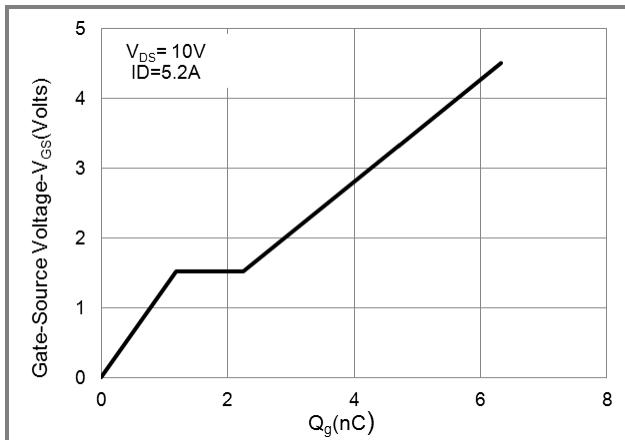


Fig.7 Gate-Charge Characteristics

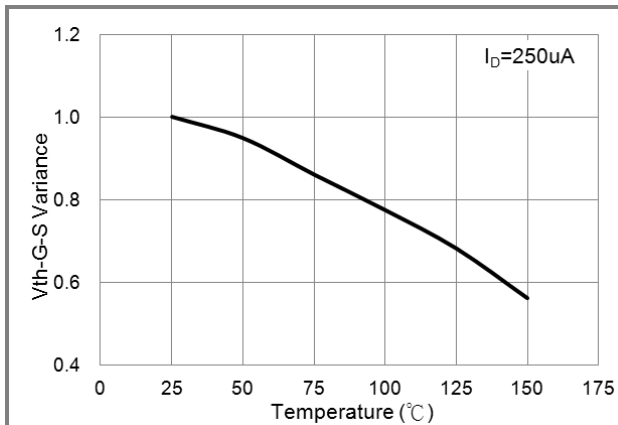


Fig.8 Threshold Voltage Variation with Temperature

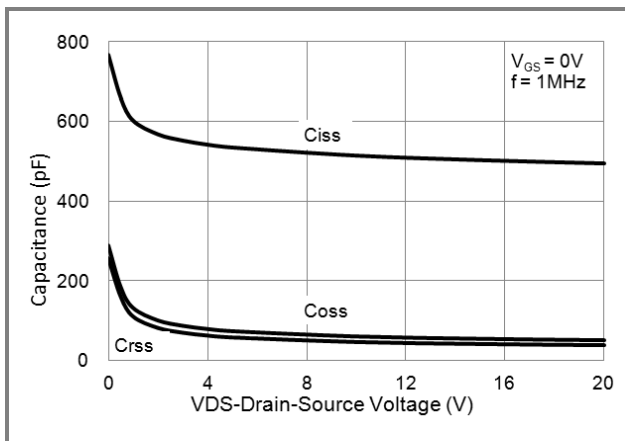


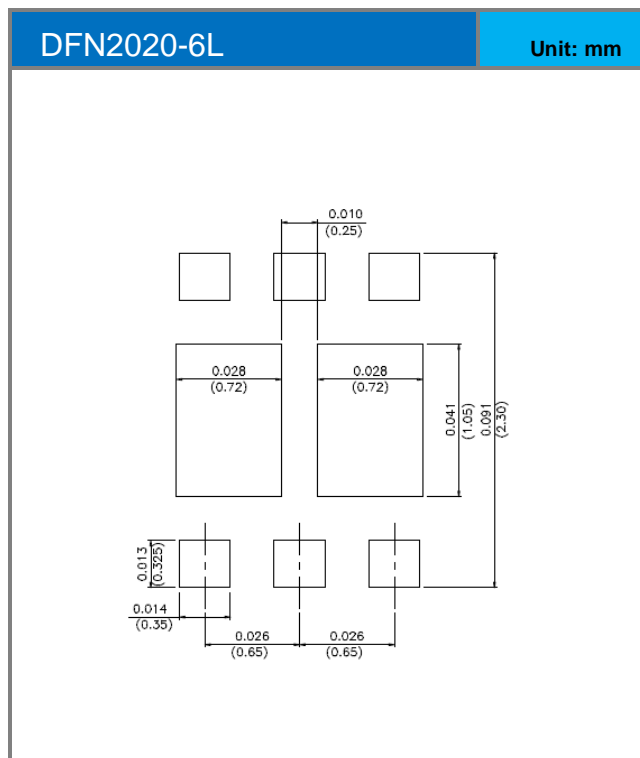
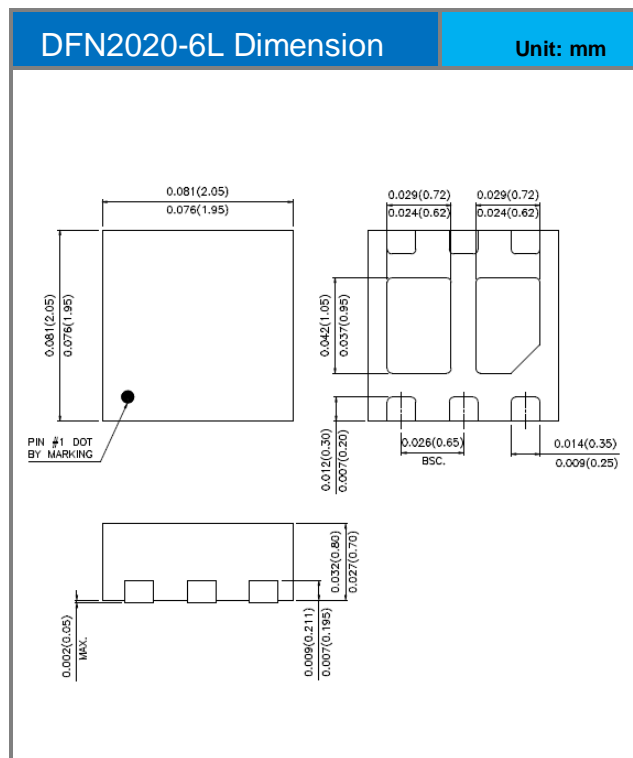
Fig.9 Capacitance vs. Drain-Source Voltage.

PJQ2800

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJQ2800_R1_00001	DFN2020-6L	3K pcs / 7" reel	800	Halogen free

MOUNTING PAD LAYOUT





PJQ2800

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