PJCF05LC SERIES

5-TVS/ZENER ARRAY FOR ESD AND LATCH-UP PROTECTION

This 5-TVS/Zener Array has been designed to Protect Sensitive Equipment against ESD and to prevent Latch-Up events in CMOS circuitry, operating at 5V,as well available for 12V,15V, and 24V Systems.This TVS array offers an integrated solution to protect up to 5 data lines where the board space is a premium.

FEATURES

- 100W Power Dissipation (8/20μs Waveform)
- Low Leakage Current
- Very Low Clamping Voltage
- IEC61000-4-2 ESD 20kV air,15kV Contact Compliance
- Operating voltage options for 5V,12V,15V, and 24V
- In compliance with EU RoHS 2002/95/EC directives

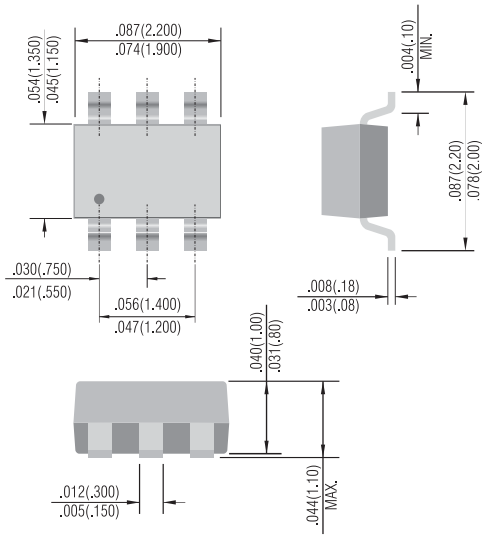
MECHANICAL DATA

- Case: SOT-363, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Appox Weight : 0.006 gram

APPLICATIONS

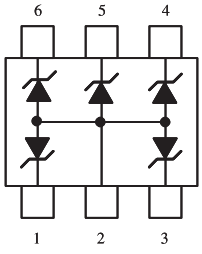
- Personal Digital Assistant (PDA)
- SIM Card Port Protection (Mobile Phone)
- Portable Instrumentation
- Mobile Phones and Accessories
- Memory Card Port Protection

SOT-363 Unit: inch (mm)



MAXIMUM RATINGS (Per Device)

RATING	SYMBOL	VALUE	UNITS
Peak Pulse Power (8/20μs Waveform)	P_{PP}	100	W
ESD Voltage (HBM)	V_{ESD}	25	kV
Operating Temperature Range	T_J	-55 to 125	°C
Storage Temperature Range	T_{STG}	-55 to 150	°C



PANJIT SEMI CONDUCTOR

PJCF05LC SERIES

ELECTRICAL CHARACTERISTICS@T_J=25°C (Per Device)

PJCF05LC Marking C05						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V_{WRM}	-	-	-	5	V
Reverse Breakdown Voltage	V_{BR}	I_{BR}=1mA	6.2	-	-	V
Reverse Leakage Current	I_R	V_R=5V	-	-	0.5	µA
Clamping Voltage (8/20µs)	V_C	I_{PP}=5A	-	-	10	V
Clamping Voltage (8/20µs)	V_C	I_{PP}=9A	-	-	11	V
Off State Junction Capacitance	C_J	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	100	pF
PJCF12LC Marking C12						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V_{WRM}	-	-	-	12	V
Reverse Breakdown Voltage	V_{BR}	I_{BR}=1mA	13.3	-	-	V
Reverse Leakage Current	I_R	V_R=12V	-	-	0.5	µA
Clamping Voltage (8/20µs)	V_C	I_{PP}=3A	-	-	18	V
Clamping Voltage (8/20µs)	V_C	I_{PP}=5A	-	-	20	V
Off State Junction Capacitance	C_J	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	50	pF
PJCF15LC Marking C15						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V_{WRM}	-	-	-	15	V
Reverse Breakdown Voltage	V_{BR}	I_{BR}=1mA	16.6	-	-	V
Reverse Leakage Current	I_R	V_R=15V	-	-	0.5	µA
Clamping Voltage (8/20µs)	V_C	I_{PP}=3A	-	-	23	V
Clamping Voltage (8/20µs)	V_C	I_{PP}=4A	-	-	25	V
Off State Junction Capacitance	C_J	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	40	pF
PJCF24LC Marking C24						
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V_{WRM}	-	-	-	24	V
Reverse Breakdown Voltage	V_{BR}	I_{BR}=1mA	26.7	-	-	V
Reverse Leakage Current	I_R	V_R=24V	-	-	0.5	µA
Clamping Voltage (8/20µs)	V_C	I_{PP}=1A	-	-	35	V
Clamping Voltage (8/20µs)	V_C	I_{PP}=2A	-	-	45	V
Off State Junction Capacitance	C_J	0Vdc Bias f=1MHz Between I/O pins and pin 2	-	-	30	pF

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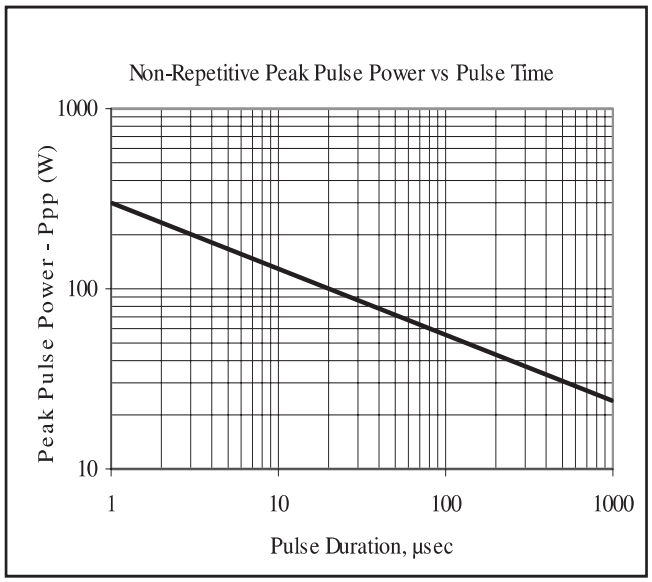
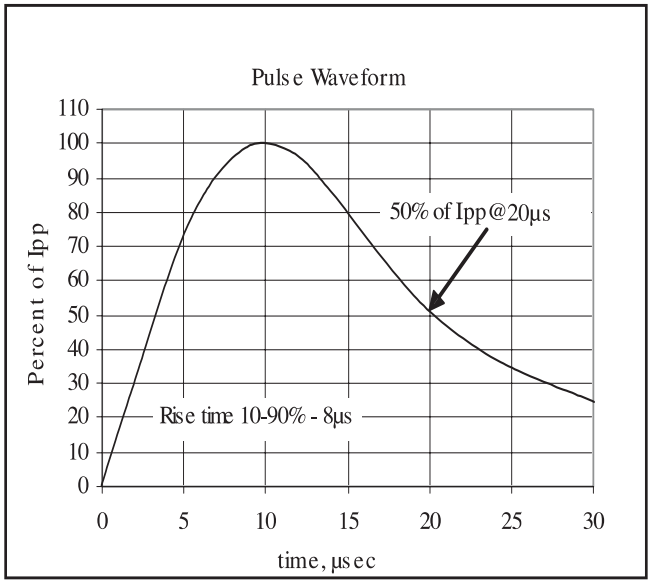
TYPICAL CHARACTERISTICS 25°C unless otherwise noted





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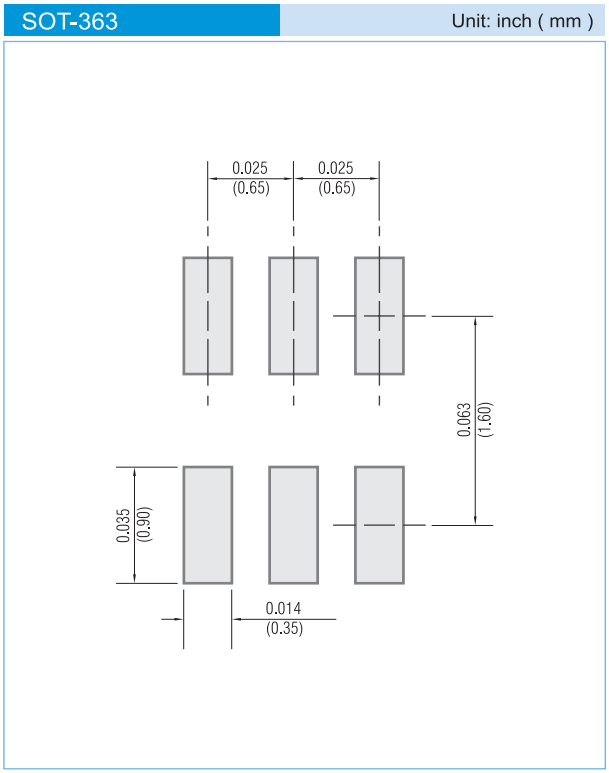
MOUNTING PAD LAYOUT

SOT-363 Unit: inch (mm)



ORDER INFORMATION

- Packing information

  T/R - 10K per 13" plastic Reel

  T/R - 3K per 7" plastic Reel

LEGAL STATEMENT