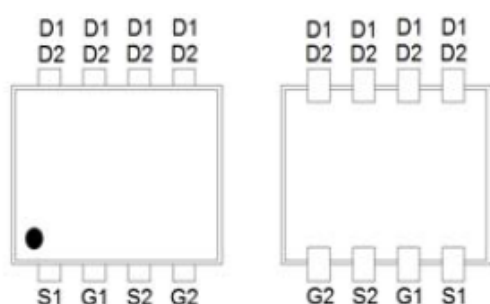


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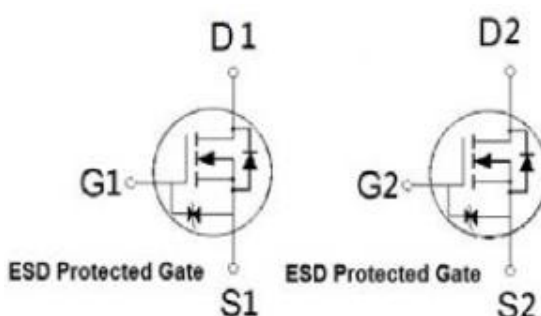
Dual N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
20V	12.5mΩ @ $V_{GS} = 4.5V$	9A



J-Lead



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±10	
Continuous Drain Current ²	$T_A = 25\text{ }^{\circ}\text{C}$	I_D	9	A
	$T_A = 70\text{ }^{\circ}\text{C}$		7	
Pulsed Drain Current ¹		I_{DM}	28	
Avalanche Current		I_{AS}	22	
Avalanche Energy	$L=0.1\text{mH}$	E_{AS}	24	mJ
Power Dissipation	$T_A = 25\text{ }^{\circ}\text{C}$	P_D	1.7	W
	$T_A = 70\text{ }^{\circ}\text{C}$		1	
Operating Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		73	$^{\circ}\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

²Package limitation current is 7A.

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Dual N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.35	0.7	1	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±8V			±30	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V, V _{GS} = 0V			1	μA
		V _{DS} =10V, V _{GS} = 0V, T _J = 70°C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 1.8V, I _D = 3A	13.6	15.1	20.2	mΩ
		V _{GS} = 2.5V, I _D = 3A	10.4	11.9	15	
		V _{GS} = 3.9V, I _D = 3A	9	10.5	13.7	
		V _{GS} = 4.5V, I _D = 3A	8.7	10.2	12.5	
Forward Transconductance ¹	g _{fs}	V _{DS} =5V, I _D =3A		40		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 10V, f = 1MHz		1133		pF
Output Capacitance	C _{oss}			214		
Reverse Transfer Capacitance	C _{rss}			168		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.5		Ω
Total Gate Charge ²	Q _g	V _{DS} = 10V, V _{GS} = 4.5V, I _D = 3A		17.4		nC
Gate-Source Charge ²	Q _{gs}			1.1		
Gate-Drain Charge ²	Q _{gd}			5.1		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 10V , I _D ≅3A, V _{GS} = 4.5V, R _G =6Ω		24		nS
Rise Time ²	t _r			32		
Turn-Off Delay Time ²	t _{d(off)}			66		
Fall Time ²	t _f			35		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Current	I _S				1.4	A
Forward Voltage ¹	V _{SD}	I _F = 3A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 3A, dI _F /dt = 100A /μS		14		nS
Reverse Recovery Charge	Q _{rr}			5.4		nC

¹Pulse test : Pulse Width $\leq 300 \mu\text{sec}$, Duty Cycle $\leq 2\%$.

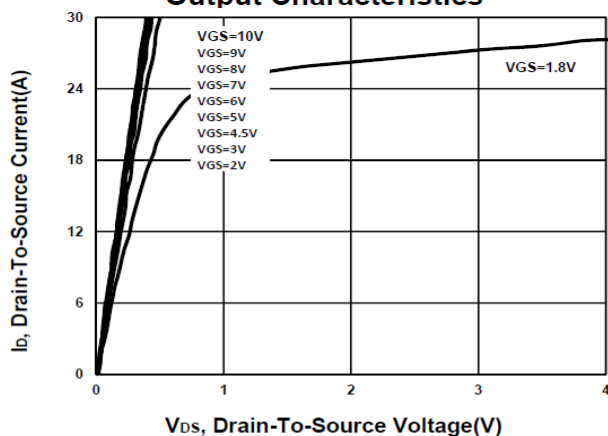
²Independent of operating temperature.

³Pulse width limited by maximum junction temperature.

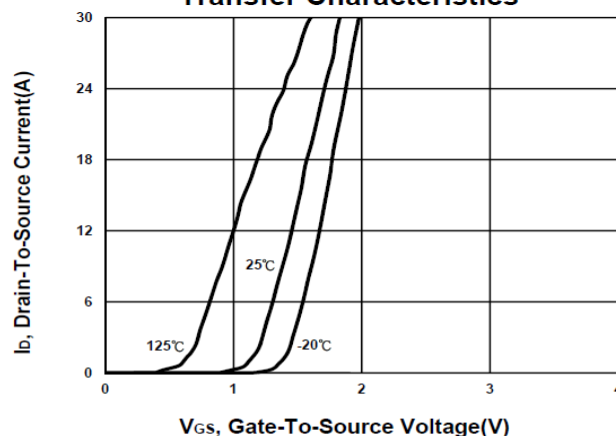
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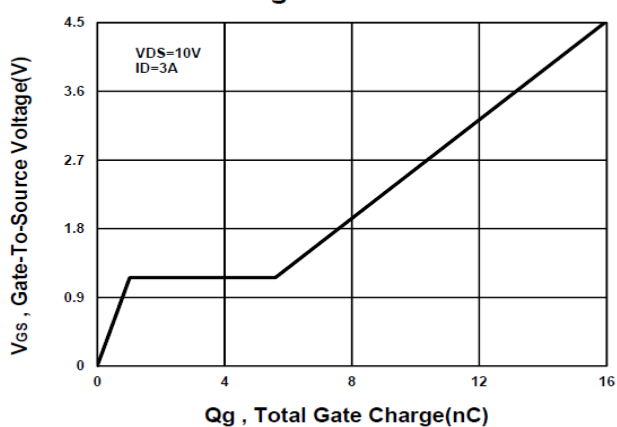
Output Characteristics



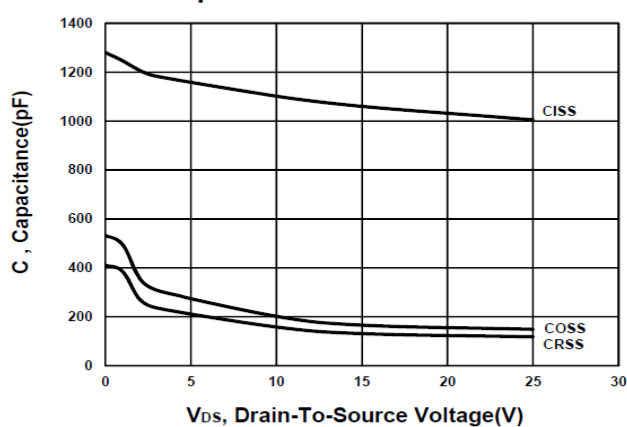
Transfer Characteristics



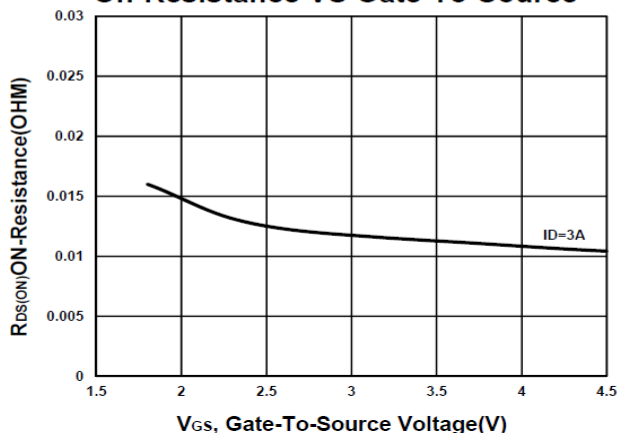
Gate charge Characteristics



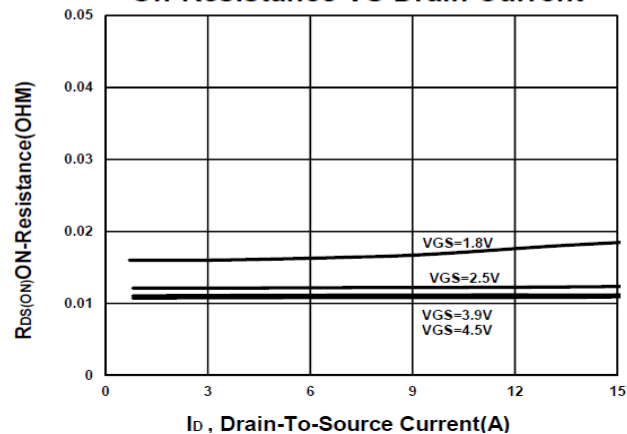
Capacitance Characteristic



On-Resistance VS Gate-To-Source



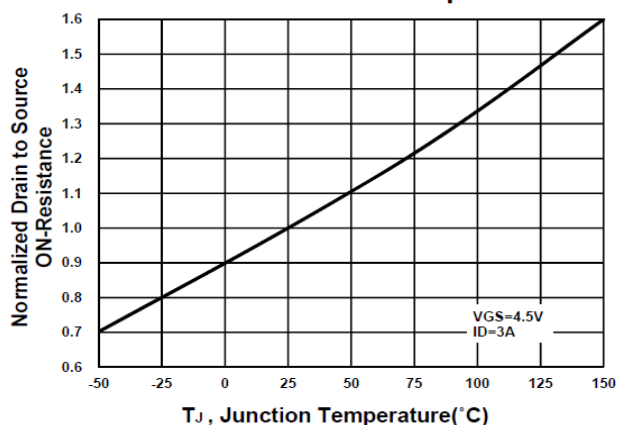
On-Resistance VS Drain Current



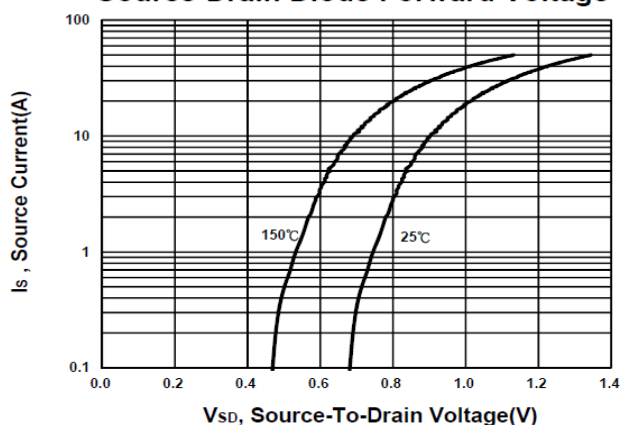
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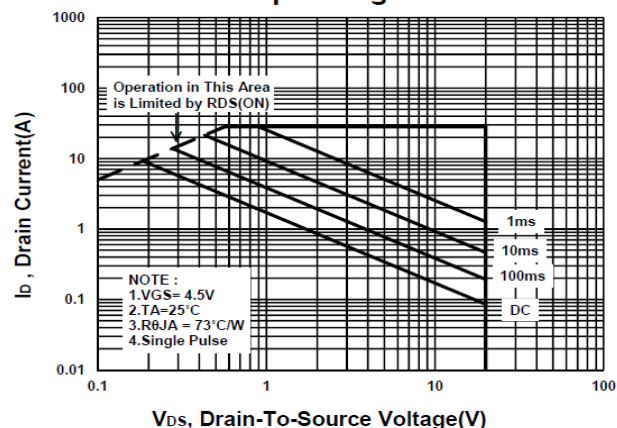
On-Resistance VS Temperature



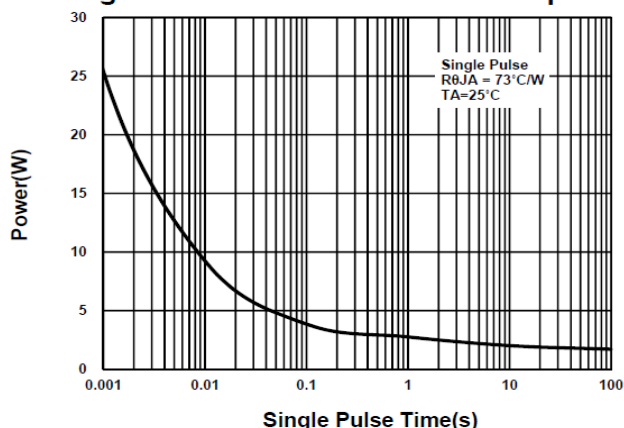
Source-Drain Diode Forward Voltage



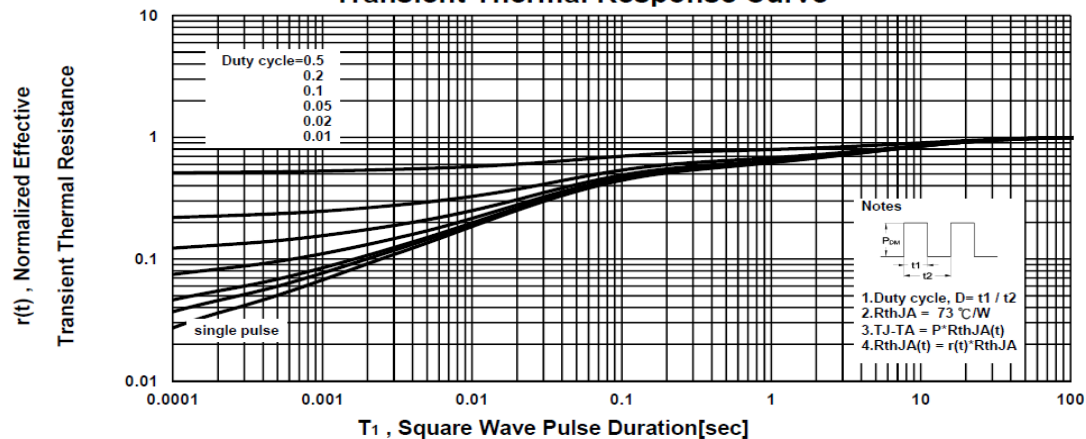
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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Dual N-Channel Enhancement Mode MOSFET

Package Dimension

J-Lead MECHANICAL DATA

Dimension	mm			Dimension	Mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	2.95	3.05	3.3	H	0.3	0.45	0.6
B	2.3	2.4	2.5	J		7°	
C	2.65	2.85	3.05	K		0.04	
D	0.25	0.32	0.4	L	0.1	0.15	0.2
E		0.65		M	0°	4°	8°
F	0.92		1.0				
G	0.01		0.1				

