

Doc. Number:

- ☒ Tentative Specification
☐ Preliminary Specification
☐ Approval Specification

MODEL NO.: PJ052IC-01M

Customer:

APPROVED BY

SIGNATURE

Name / Title

Note

Please return 1 copy for your confirmation with your signature and comments.

Approved By	Checked By	Prepared By

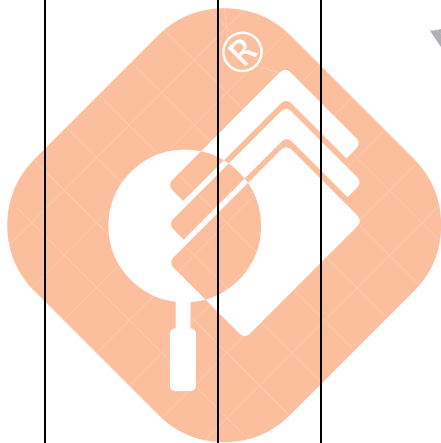
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REVISION HISTORY

Version	Date	Page (New)	Section	Description
Ver. 0.0	2017/02/13	All	All	Tentative product spec was first issued for LCD panel.



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1. PURPOSE

The specification PJ052IC-01M is a 5.2" LTPS TFT Liquid Crystal Display cell. The cell has been designed by Innolux, and manufactured by Innolux under the agreement of customer. The LTPS TFT-LCD cell will be applied to a high transmittance operating in the normally black mode LTPS TFT-LCD product.

2. GENERAL RULES OF SINGLE PANEL

2.1 GENERAL SPECIFICATION

No.	Item		Specification	unit
1	Glass thickness	TFT	0.15	mm
		CF	0.15	
2	Shipping mode		Chip	-
3	Shipping size		66.4 (H) x 120.89 (V) x 0.3 (D)	mm
4	Panel outline dimension		66.4 (H) x 120.89 (V) x 0.3 (D)	mm
5	Active screen size		64.80 x 115.20 (5.2" diagonal)	-
6	Resolution		1080RGB x 1920	pixel
7	Pixel driving element		LTPS TFT	-
8	Sub pixel size		20 x 60	um
9	Pixel arrangement		RGB-stripe	-
10	View direction (Gray inversion)		Free	-
11	Cell gap		3.2 ± 0.3	um
12	Driver IC		NT35695 *<Note>	-
13	Weight without POL		13.12 ± 10%	g

<Note> 1. Compatible IC : NT35596 / OTM1901A / R63315 / HX8399-A / OTM1906A / HX8399-B / R63350 / HX8399C / ILI7807B

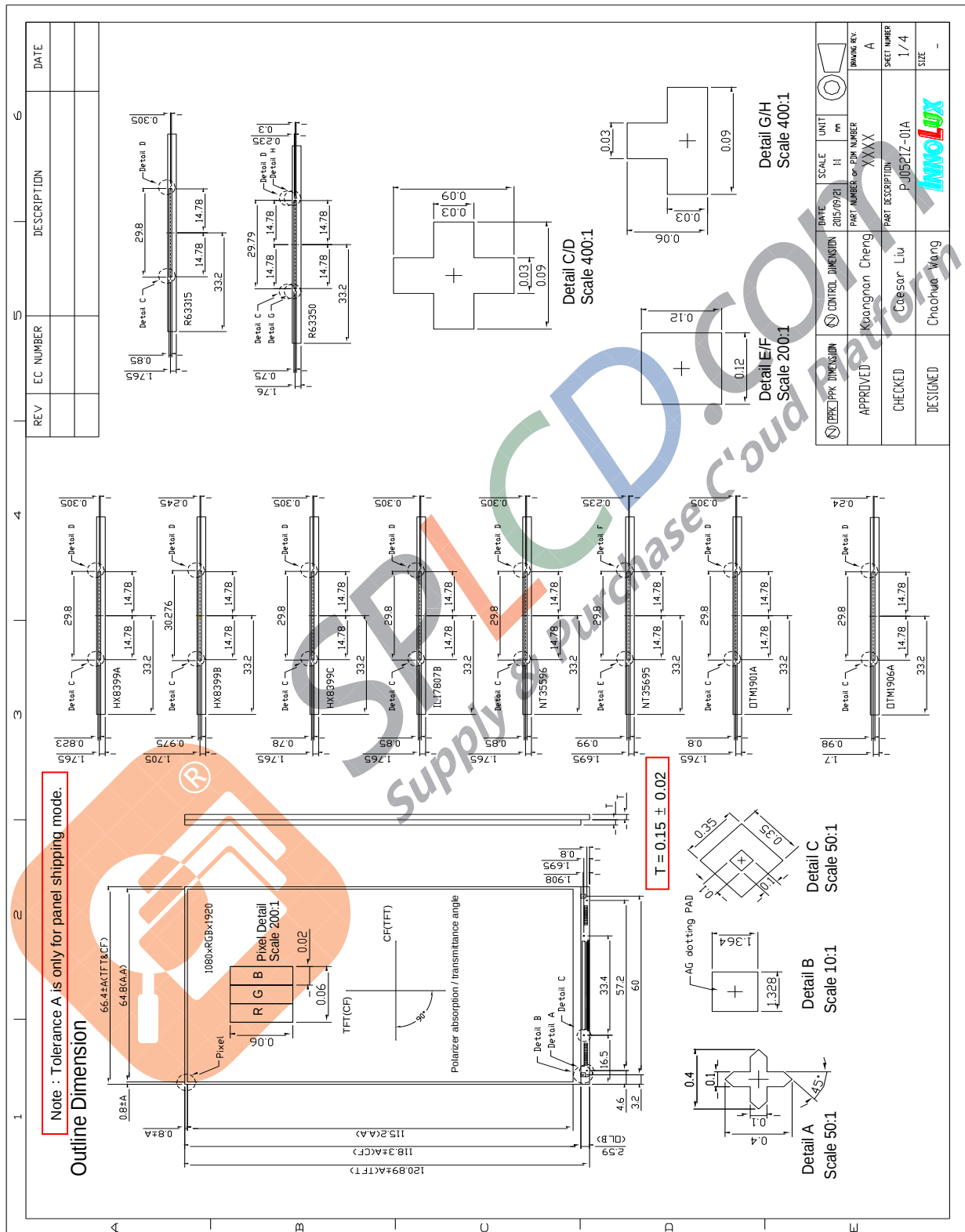
The other compatible ICs are also considered for the design of pad locations.

2. Those compatible IC should be verified for panel performance. Please refer to the IC datasheet (AP note) respectively.

3. This model is designed by the driver IC without bumping compensation.

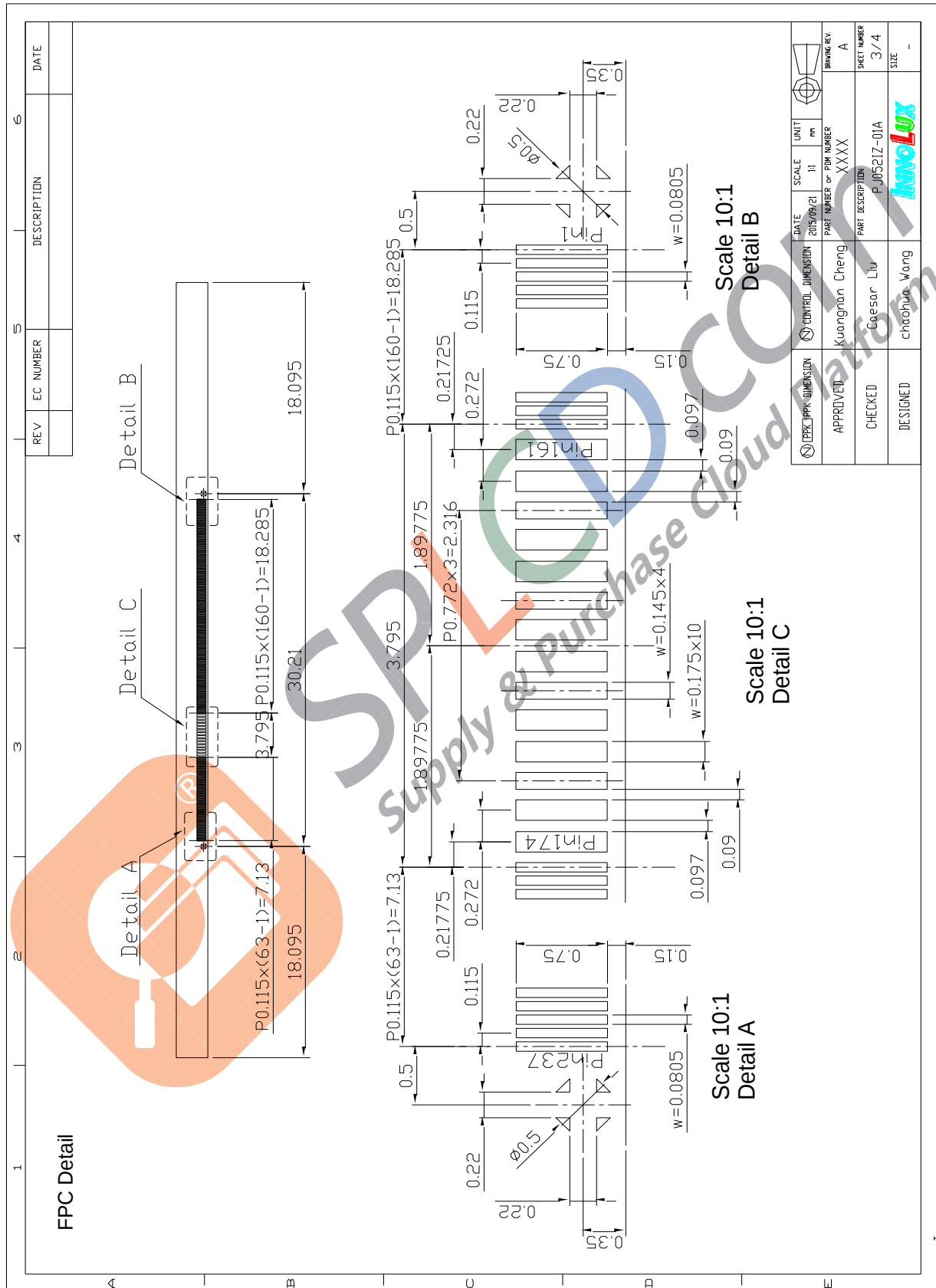
2.2 DIMENSION

2.2.1 OUTLINE DIMENSION

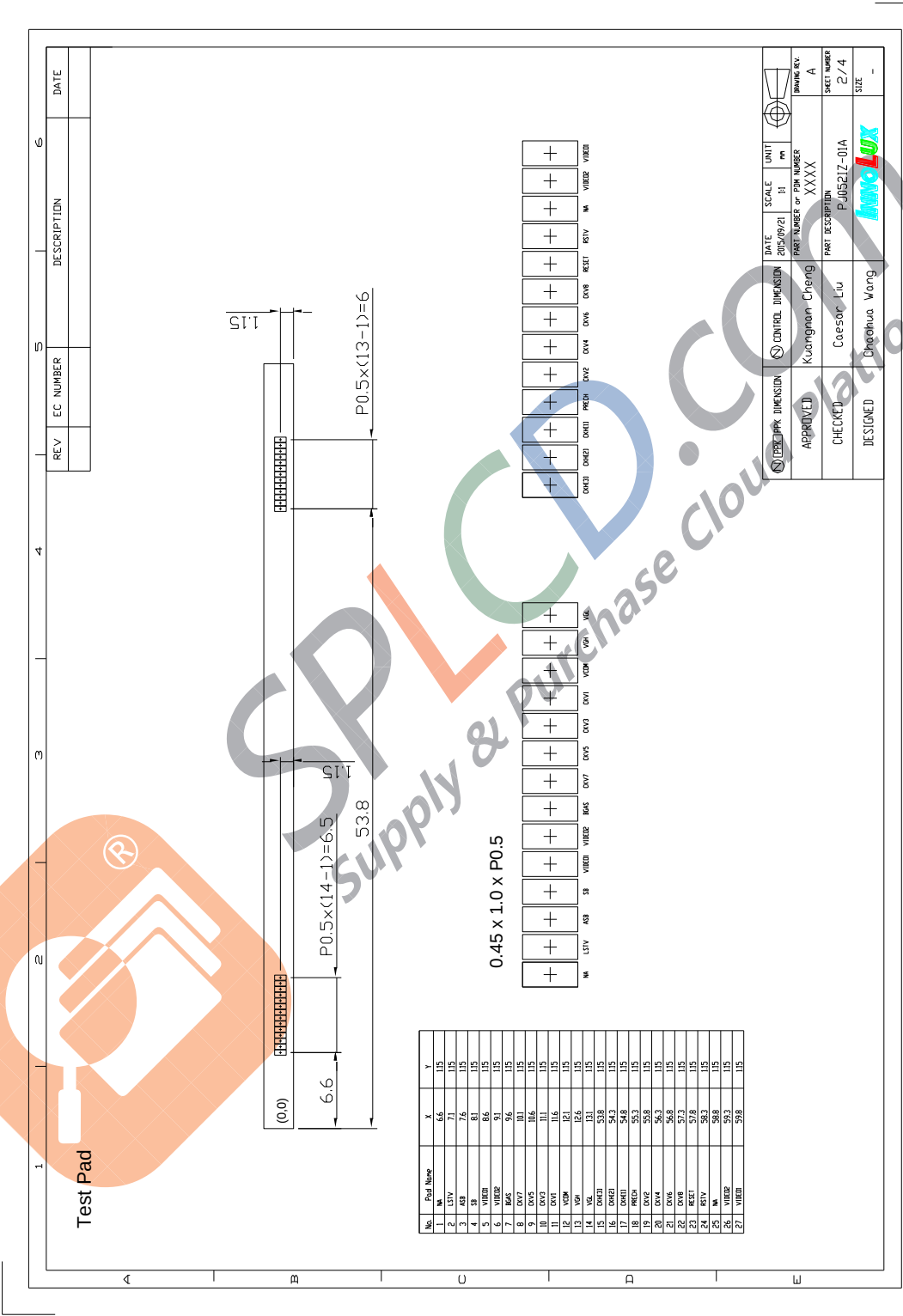


Note: (1) panel outline dimension tolerance ± 0.2 mm.

2.2.2 FPC DETAIL



2.2.3 TEST PAD



3. PIN ASSIGNMENT

3.1 LCD FPC/IC PIN ASSIGNMENT TABLE

IC information (bump up)		FPC information	
IC No.	Pad name	Pad name	FPC No.
	NT35596/Novatek/v0.07		
		DUMMY	1
		DUMMY	2
		VSS	3
		VSS	4
1	TEST(open)	OPEN	
2	TEST(open)	OPEN	
3	TEST(open)	OPEN	
4	VCOMDC3	VCOM	5
5	VCOMDC3		
6	VCOMDC3		
7	VCOMDC3		
8	VCOMDC3	VCOM	6
9	VCOMDC3		
10	AVSS	AGND	7
11	AVSS		
12	AVSS		
13	VGLO	GVSS	9
14	VGLO		
15	VGLO		
16	AVSS	GND	11
17	AVSS		
18	AVSS		
19	BOTM_PATEN	PBCTLA1	13
20	BOTM_PATEN		
21	BOTM_PATEN		
22	BOTM_COM	PBCTLA2	14
23	BOTM_COM		
24	BOTM_COM		
25	TOP_PATEN	PBCTLB1	15
26	TOP_PATEN		
27	TOP_PATEN		
28	TOP_COM	PBCTLB2	16
29	TOP_COM		
30	TOP_COM		
31	VGHO	GVDD	17
32	VGHO		

33	VGHO		
34	VGHO	GVDD	18
35	VGHO		
36	VGHO		
37	VGH	VGH	19
38	VGH		
39	VGH		
40	VGH		
41	VGH	VGH	20
42	VGH		
43	C21P		
44	C21P		
45	C21P	C21P	21
46	C21P		
47	C21P		
48	C21P		
49	C21P	C21P	22
50	C21P		
51	C21P		
52	C21M		
53	C21M	C21M	23
54	C21M		
55	C21M		
56	C21M		
57	C21M	C21M	24
58	C21M		
59	C21M		
60	VSSO		
61	AVDDR	OPEN	25
62	AVDDR		
63	AVDDR		
64	AVDDR		
65	AVDDR	SVDD	26
66	AVDDR		
67	AVDDR		
68	AVDDR		
69	AVDDR	SVDD	27
70	AVDD		
71	AVDD		
72	AVDD		
73	AVDD	VSP	28
74	AVDD		
		VSP	29
		VSP	30
		VSP	31
		VSP	32
		VSP	33

75	AVDD		
76	AVDD	VSP	34
77	AVDD		
78	AVDD		
79	ENPWRP	EXPWRP	35
80	ENPWRP		
81	CSP	DUMMY	36
82	EXTP	VSWP	37
83	EXTP		
84	EXTP	VSWP	38
85	AVEER	SVSS	39
86	AVEER		
87	AVEER		
88	AVEER		
89	AVEER	SVSS	40
90	AVEER		
91	AVEE	VSN	41
92	AVEE		
93	AVEE		
94	AVEE		
95	AVEE	VSN	42
96	AVEE		
97	AVSS	AGND	43
98	AVSS		
99	AVSS	AGND	44
100	AVSS		
101	AVSS	AGND	45
102	AVSS		
103	C41M	C41M	46
104	C41M		
105	C41M	C41M	47
106	C41M		
107	C41M	C41M	48
108	C41M		
109	C41P	C41P	49
110	C41P		
111	C41P	C41P	50
112	C41P		
113	C41P	C41P	51
114	C41P		
115	VCL	VCL	52
116	VCL		

117	VCL		
118	VCL		
119	VCL	VCL	53
120	VCL		
121	VCI		
122	VCI	VCI	54
123	VCI		
124	VCI		
125	VCI	VCI	55
126	VCI		
127	VCI		
128	VCI	VCI	56
129	VCI		
130	GVDDP	VPLVL	57
131	TEST(open)	OPEN	
132	GVDDN	VNLVL	58
133	VCOMDC3		
134	VCOMDC3	VCOM	59
135	VCOMDC3		
136	VCOMDC3		
137	VCOMDC3	VCOM	60
138	VCOMDC3		
139	VCOMDC3		
140	VCOMDC3	VCOMDC	61
141	VCOMDC3		
142	AVSS		
143	AVSS	AVSS	62
144	AVSS		
145	AVSS	AVSS	63
146	AVSS		
147	AVSS	AVSS	64
148	VDD		
149	VDD	VDD	65
150	VDD		
151	VDD	VDD	66
152	VDD		
153	VDD	VDD	67
154	VDDI_DC	IOVCCRF	68
155	VDDI_DC	IOVCCRF	69
156	VDDI_DC	IOVCCRF	70
157	VDDI		
158	VDDI	IOVCC	71

159	VDDI		
160	VDDI	IOVCC	72
161	VDDI		
162	VDDI		
163	VDDI		
164	VDDI	IOVCC	73
165	VDDI		
166	VSS		
167	VSS	GNDRF	74
168	VSS		
169	VSS		
170	VSS	VSS	75
171	VSS		
172	VSS		
173	VSS	VSS	76
174	EN4PWR		
175	TEST(open)		
176	LEDPWM	LEDPWM	78
177	TEST(open)	OPEN	
178	TEST(open)	OPEN	
179	EXCK	EXCK	79
180	TEST(open)	OPEN	
181	TEST(open)	OPEN	
182	FRM	FRM	80
183	TEST(open)	OPEN	
184	TEST(open)	OPEN	
185	DSWAP[1]	LNSW1	81
186	TEST(open)	OPEN	
187	TEST(open)	OPEN	
188	DSWAP[0]	LNSW0	82
		LNSW0	83
189	TEST(open)	OPEN	
190	TEST(open)	OPEN	
191	PNSWAP	PNSW	84
192	TEST(open)	OPEN	
193	TEST(open)	OPEN	
194	IM[2]	IM[2]	85
195	TEST(open)	OPEN	
196	TEST(open)	OPEN	
197	IM[1]	IM[1]	86
		IM[1]	87
198	TEST(open)	OPEN	

199	VSSO	OPEN	
200	IM[0]	IM[0]	88
201	TEST(open)	OPEN	
202	TEST(open)	OPEN	
203	VSOUT	VSOUT	89
204	TEST(open)	OPEN	
205	TEST(open)	OPEN	
206	HSOUT	HSOUT	90
207	TEST(open)	OPEN	
208	TEST(open)	OPEN	
209	FTE1	TE2	91
210	TEST(open)	OPEN	
211	TEST(open)	OPEN	
212	FTE	TE	92
		TE	93
213	TEST(open)	OPEN	
214	TEST(open)	OPEN	
215	CSX	CSX	94
216	TEST(open)	OPEN	
217	TEST(open)	OPEN	
218	DCX	DCX	95
219	TEST(open)	OPEN	
220	TEST(open)	OPEN	
221	WRX/SCL	WRX/SCL	96
222	TEST(open)	OPEN	
223	TEST(open)	OPEN	
224	SDO	DOUT	97
225	TEST(open)	OPEN	
226	SDI	DIN	98
227	SDI		
228	SDI	DIN	99
229	TEST(open)	OPEN	
230	RESX	RESX	100
231	VSSO	OPEN	
232	VDDI	IOVCC	101
233	VDDI		
234	VDDI		
235	VDDI	IOVCC	102
236	VDDI	IOVCC	103
237	VDDI	IOVCC	104
238	VSS	GND	105
239	VSS		

240	VSS	GND	106
241	VSS		
242	VSS	GND	107
243	VSS		
244	VSS	GND	108
245	VSS		
246	VSS		
247	VSS	GND	109
248	VSS		
249	VSS		
250	AVSS	AGND	110
251	AVSS		
252	AVSS	AGND	111
253	AVSS		
254	AVSS	AGND	112
255	AVSS		
256	AVSS	AGND	113
257	AVSS		
258	AVSS		
259	AVSS	AGND	114
260	AVSS		
261	AVSS		
262	VCI	VCI	115
263	VCI		
264	VCI	VCI	116
265	VCI		
266	VCI	VCI	117
267	VCI		
268	VCI	VCI	118
269	VCI		
270	VCI		
271	VCI	VCI	119
272	VCI		
273	VCI		
274	AVDDR	SVDD	120
275	AVDDR		
276	AVDDR		
277	AVDDR	SVDD	121
278	AVDDR		
279	AVDDR		
280	AVDDR	SVDD	122
281	AVDDR		

282	AVDDR		
283	AVDDR	SVDD	123
284	AVDDR		
285	AVDDR		
286	AVDDR		
287	AVDDR	SVDD	124
288	AVDDR		
289	VGS		
290	VGS		
291	VGS	VGS	125
292	VGS		
293	VGS		
294	VGS		
		DUMMY	126
295	AVEER		
296	AVEER		
297	AVEER		
298	AVEER	SVSS	127
299	AVEER		
300	AVEER		
301	AVEER		
302	AVEER	SVSS	128
303	AVEER		
304	AVEER		
305	AVEER		
306	AVEER	SVSS	129
307	AVEER		
308	AVEER		
309	AVEER		
310	VCL	VCL	130
311	VCL		
312	VCL		
313	VCL		
314	VCL	VCL	131
315	VCL		
316	VCL		
317	VCL		
318	VCL	VCL	132
319	VCL		
320	VCL		
321	VCL		
322	AVSS	AGND	140

323	AVSS		
324	AVSS	AGND	141
325	AVSS		
326	AVSS		
327	AVSS	AGND	142
328	AVSS		
329	AVSS		
330	AVSS	AGND	143
331	AVSS		
332	AVSS		
333	AVSS	AGND	144
334	VSS		
335	VSS		
336	VSS	GND	145
337	VSS		
338	VSS		
339	VSS	GND	146
340	VSS		
341	VSS		
342	VSS	GND	147
343	VSS		
344	VSS		
345	VSS	GND	148
346	VDD		
347	VDD		
348	VDD	VDD	149
349	VDD		
350	VDD		
351	VDD	VDD	150
352	VDDI		
353	VDDI		
354	VDDI	IOVCC	151
355	VSS		
356	VSS		
357	VSS	GND	152
358	VSS		
359	VSS		
360	VSS	GND	153
361	VG_HSSI		
362	VG_HSSI		
363	VG_HSSI	DPHYGND	154
364	VG_HSSI		

365	VG_HSSI		
366	VG_HSSI		
367	VG_HSSI	DPHYGND	160
368	VG_HSSI		
369	VG_HSSI		
370	HSSI_D2_P	DATA2P	161
371	HSSI_D2_P		
372	HSSI_D2_P		
373	HSSI_D2_P		
374	HSSI_D2_P		
375	HSSI_D2_P	DATA2N	162
376	HSSI_D2_N		
377	HSSI_D2_N		
378	HSSI_D2_N		
379	HSSI_D2_N		
380	HSSI_D2_N	DPHYGND	163
381	HSSI_D2_N		
382	VG_HSSI		
383	VG_HSSI	DATA1P	164
384	VG_HSSI		
385	HSSI_D1_P		
386	HSSI_D1_P		
387	HSSI_D1_P		
388	HSSI_D1_P	DATA1N	165
389	HSSI_D1_P		
390	HSSI_D1_P		
391	HSSI_D1_N		
392	HSSI_D1_N		
393	HSSI_D1_N	DPHYGND	166
394	HSSI_D1_N		
395	HSSI_D1_N		
396	HSSI_D1_N	CLK_P	167
397	VG_HSSI		
398	VG_HSSI		
399	VG_HSSI		
400	HSSI_CLK_P		
401	HSSI_CLK_P	CLK_N	168
402	HSSI_CLK_P		
403	HSSI_CLK_P		
404	HSSI_CLK_P	CLK_N	168
405	HSSI_CLK_P		
406	HSSI_CLK_N		

407	HSSI_CLK_N		
408	HSSI_CLK_N		
409	HSSI_CLK_N		
410	HSSI_CLK_N		
411	HSSI_CLK_N		
412	VG_HSSI	DPHYGND	169
413	VG_HSSI		
414	VG_HSSI		
415	HSSI_D0_P	DATA0P	170
416	HSSI_D0_P		
417	HSSI_D0_P		
418	HSSI_D0_P		
419	HSSI_D0_P		
420	HSSI_D0_P		
421	HSSI_D0_N	DATA0N	171
422	HSSI_D0_N		
423	HSSI_D0_N		
424	HSSI_D0_N		
425	HSSI_D0_N		
426	HSSI_D0_N		
427	VG_HSSI	DPHYGND	172
428	VG_HSSI		
429	VG_HSSI		
430	HSSI_D3_P	DATA3P	173
431	HSSI_D3_P		
432	HSSI_D3_P		
433	HSSI_D3_P		
434	HSSI_D3_P		
435	HSSI_D3_P		
436	HSSI_D3_N	DATA3N	174
437	HSSI_D3_N		
438	HSSI_D3_N		
439	HSSI_D3_N		
440	HSSI_D3_N		
441	HSSI_D3_N		
442	VDDAM	DPHYVCC	175
443	VDDAM		
444	VDDAM	DPHYVCC	176
445	VDDAM		
446	VDDAM	DPHYVCC	177
447	VDDAM		
448	VDDAM	DPHYVCC	178

449	VDDAM		
450	VDDAM		
451	VP_HSSI	VDDL P	179
452	VP_HSSI		
453	VP_HSSI	VDDL P	180
454	VP_HSSI		
455	VP_HSSI	VDDL P	181
456	VP_HSSI		
457	VSS	GND	182
458	VSS		
459	VSS		
460	VSS	GND	183
461	VSS		
462	VSS		
463	VSS	GND	184
464	VSS		
465	VSS		
466	AVSS	AVSS	185
467	AVSS		
468	AVSS		
469	AVSS	AVSS	186
470	AVSS		
471	AVSS		
472	AVSS	AVSS	187
473	AVSS		
474	AVSS		
475	COGTEST[1]	DUMMYR1	188
476	VCI1T(open)	OPEN	
477	VCI1T(open)	OPEN	
478	VCI1T(open)	OPEN	
479	VCI1T(open)	OPEN	
480	COGTEST[2]	DUMMYR2	189
481	VCL	VCL	190
482	VCL		
483	VCL	VCL	191
484	VCL		
485	VCL	VCL	192
486	VCL		
487	VCI	VCI	193
488	VCI		
489	VCI		
490	VCI		

491	VCI	VCI	194
492	VCI		
493	VCI		
494	VCI		
495	VCI		
496	AVDDR	SVDD	195
497	AVDDR	SVDD	196
498	AVDDR		
499	AVDDR	SVDD	197
500	AVDDR		
501	AVDDR	VSP	198
502	AVDD		
503	AVDD	VSP	199
504	AVDD		
505	AVDD	VSP	200
506	AVDD		
507	AVDD	EXPWRN	201
508	ENPWRN		
509	ENPWRN	OPEN	202
510	VSSO		
511	AVEE	VSN	203
512	AVEE		
513	AVEE	VSN	204
514	AVEE		
515	AVEE	VSN	205
516	AVEE		
517	AVEE	VSN	206
518	AVEE		
519	AVEE	SVSS	207
520	AVEER		
521	AVEER	SVSS	208
522	AVEER		
523	AVEER	SVSS	209
524	AVEER		
525	AVEER	SVSS	210
526	AVEER		
527	AVEER	VSWN	211
528	AVEER		
529	EXTN	VSWN	208
530	EXTN	VSWN	209
531	CSN	DUMMY	210
532	C31P	C31P	211

533	C31P		
534	C31P		
535	C31P	C31P	212
536	C31P		
537	C31P		
538	C31P	C31P	213
539	C31P		
540	C31P		
541	C31M	C31M	214
542	C31M		
543	C31M		
544	C31M	C31M	215
545	C31M		
546	C31M		
547	C31M	C31M	216
548	C31M		
549	C31M		
550	VGLOUT	VGL	217
551	VGLOUT		218
552	VGLOUT		219
553	VGL	VGL	220
554	VGL		221
555	VGL		222
556	VGLO	GVSS	223
557	VGLO		
558	VGLO		
559	VGLO	GVSS	224
560	VGLO		
561	VGLO		
562	AVSS	AVSS	225
563	AVSS		
564	AVSS		
565	AVSS	AVSS	226
566	AVSS		
567	AVSS		
568	VGHO	GVDD	227
569	VGHO	GVDD	228
570	VGHO	GVDD	229
571	AVSS	AGND	230
572	AVSS		
573	AVSS		
574	AVSS	AGND	231

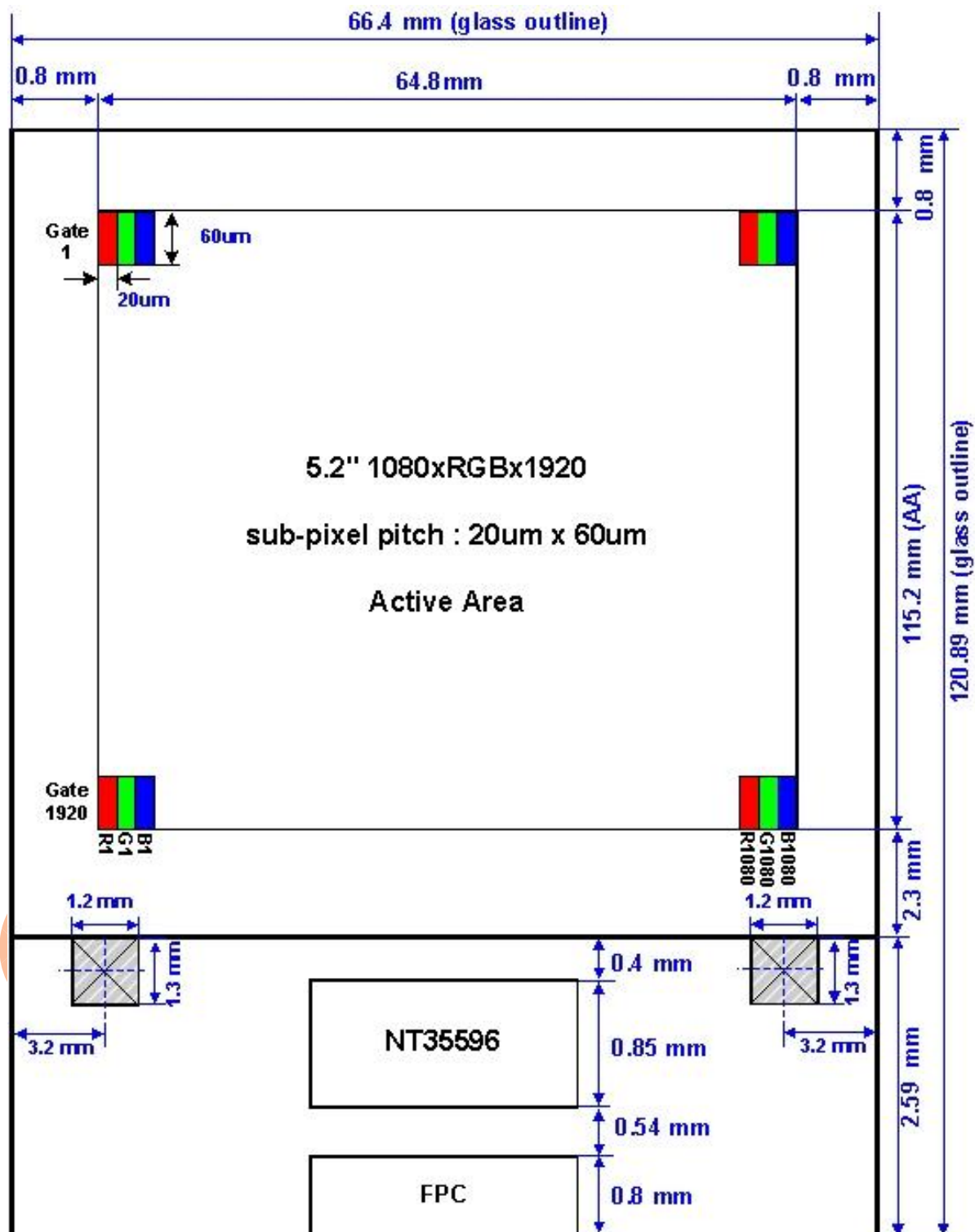
575	AVSS		
576	AVSS		
577	VCOMDC3	VCOM	230
578	VCOMDC3		
579	VCOMDC3		
580	VCOMDC3	VCOM	231
581	VCOMDC3		
582	VCOMDC3		
583	TEST(open)	OPEN	
584	TEST(open)	OPEN	
585	TEST(open)	OPEN	
		ASB	232
		SB	233
		VSS	234
		VSS	235
		DUMMY	236
		DUMMY	237

<Note>

- (1) FPC pin No. 3/4/234/235: Please connect these pins to GND on FPCa design.
- (2) FPC pin No. 232 (ASB) & No. 233 (SB): Please short these pins with FPC pin No. 9/10/221/222 on FPCa design.



3.2 SCHEMATIC PANEL LAYOUT



4. ELECTRICAL SPECIFICATION

Item	Symbol	Specification			Unit
		Min.	Typ.	Max.	
TFT gate on voltage	VGH	--	9	--	V
TFT gate off voltage	VGL	--	-5.6	--	V
TFT common electrode voltage	Vcom(DC)	--	OTP. (1)	--	V
Control signal Logic 'H'	STVs, CKVs, RESET, BGAS, CKHs		VGH		V
Control signal Logic 'L'	STVs, CKVs, RESET, BGAS, CKHs		VGL		V

Note (1) Vcom must be adjusted to optimize display quality :flicker, contrast ratio and etc.

(2) VGH is TFT gate operating voltage (acceptable variation : $\pm 0.2V$)

(3) VGL is TFT gate operating voltage (acceptable variation : $\pm 0.2V$)

(4) Environmental condition: 25 ± 5 °C

● Frame rate : 60Hz

● 1 frame time (F) : 16.67 ms

Video voltage for signals (VIDEO1) and (VIDEO2) must be opposite between frame(N) to frame (N+1)

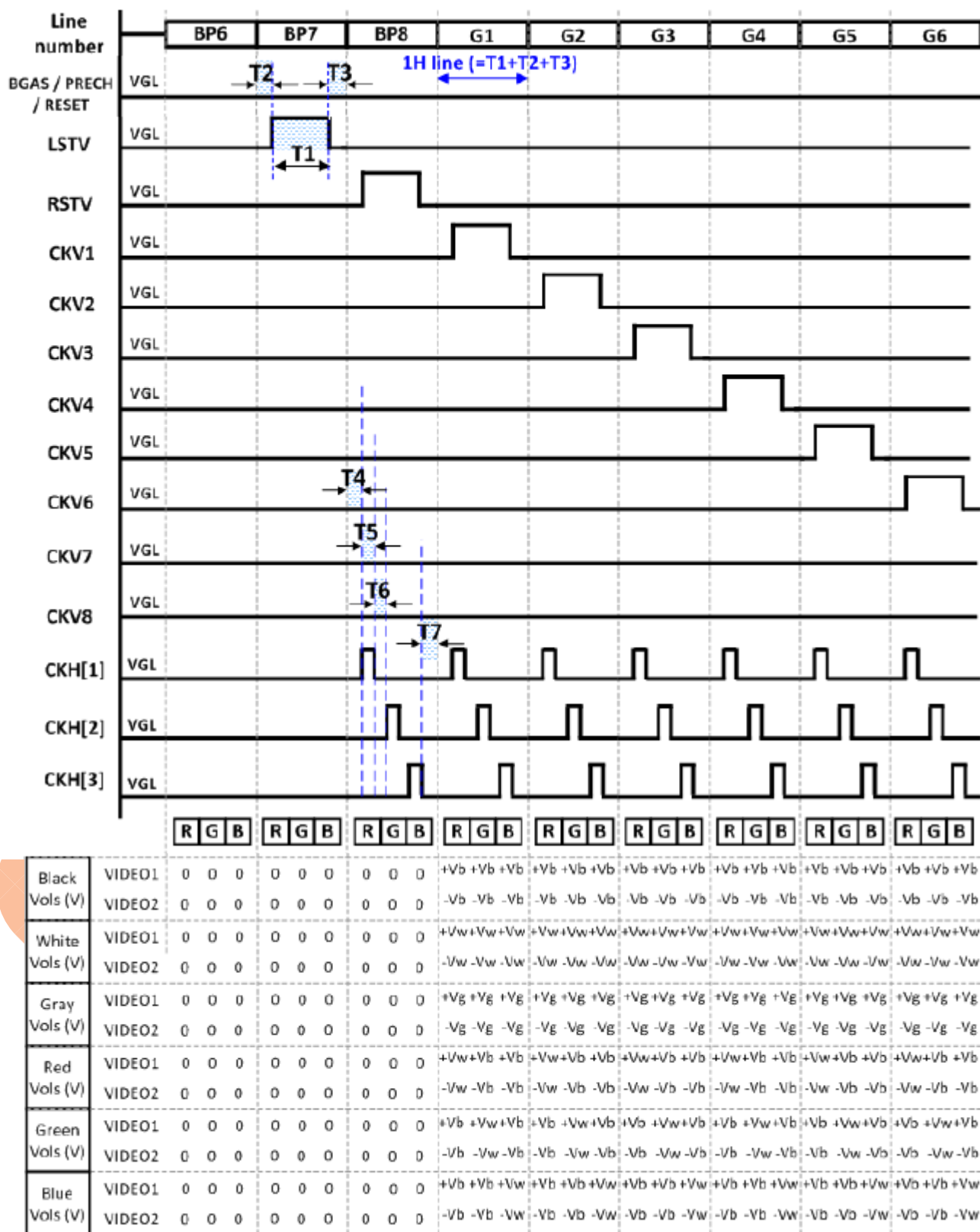
Ex: VIDEO1 for frame (N) colors B, G, R are 0.2, -0.2V, 0.2V respectively, then for frame (N+1) must be -0.2V, 0.2V, -0.2V

Suggest Video value	+Vw	-Vw	+Vg	-Vg	+Vb	-Vb
Voltage	+4.8	-4.8	+2.6	-2.6	+0.2	-0.2

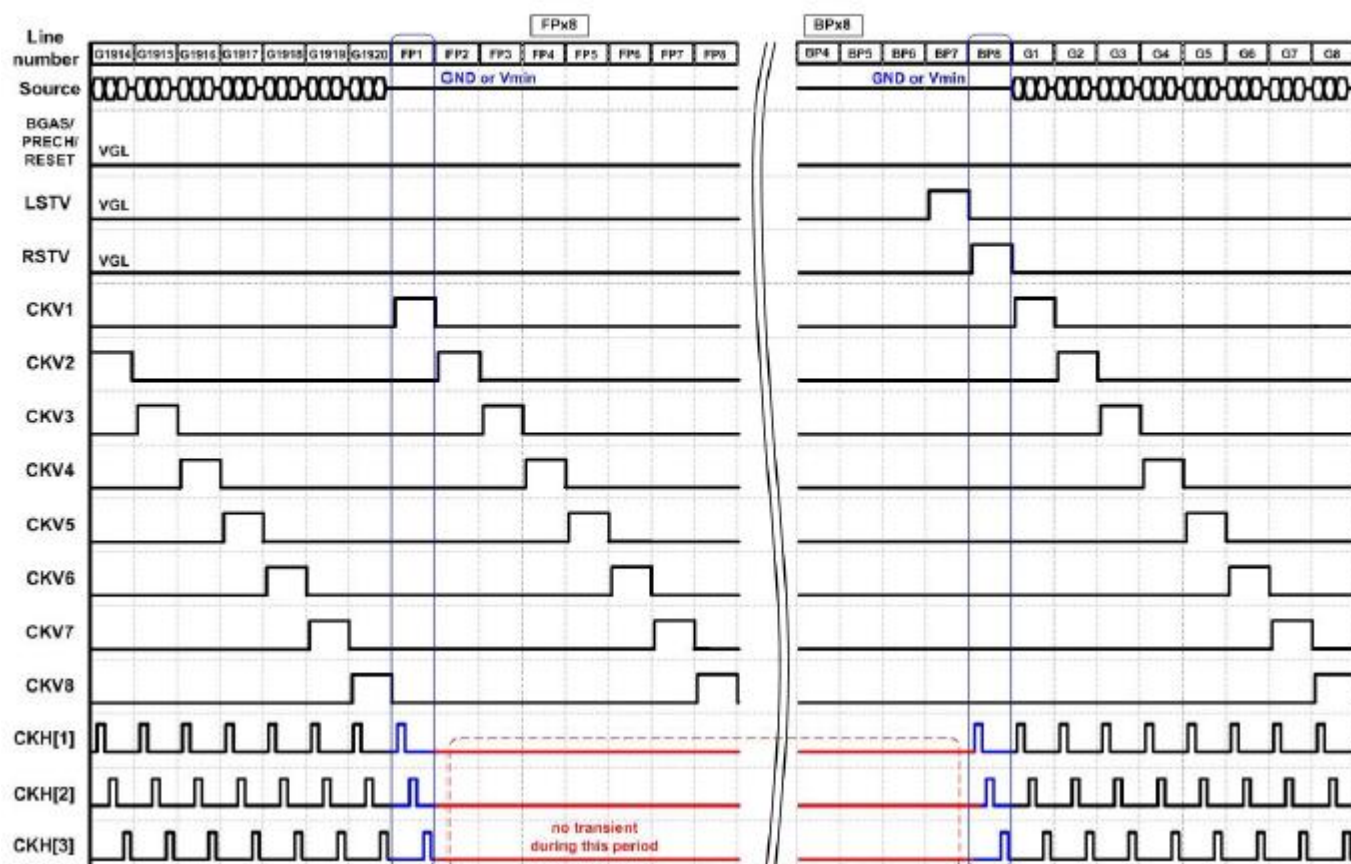
2-1. Timing Recipe

	T1	T2	T3	T4	T5	T6	T7	1H line
Timing (us)	7.35	0	1.25	0	1.9	0.55	1.8	8.6
Freq. (Hz)	60							
Blanking lines	16 (front porch=back porch=8, suggest value)							
Resolution (HxV)	1080 x RGB x 1920							

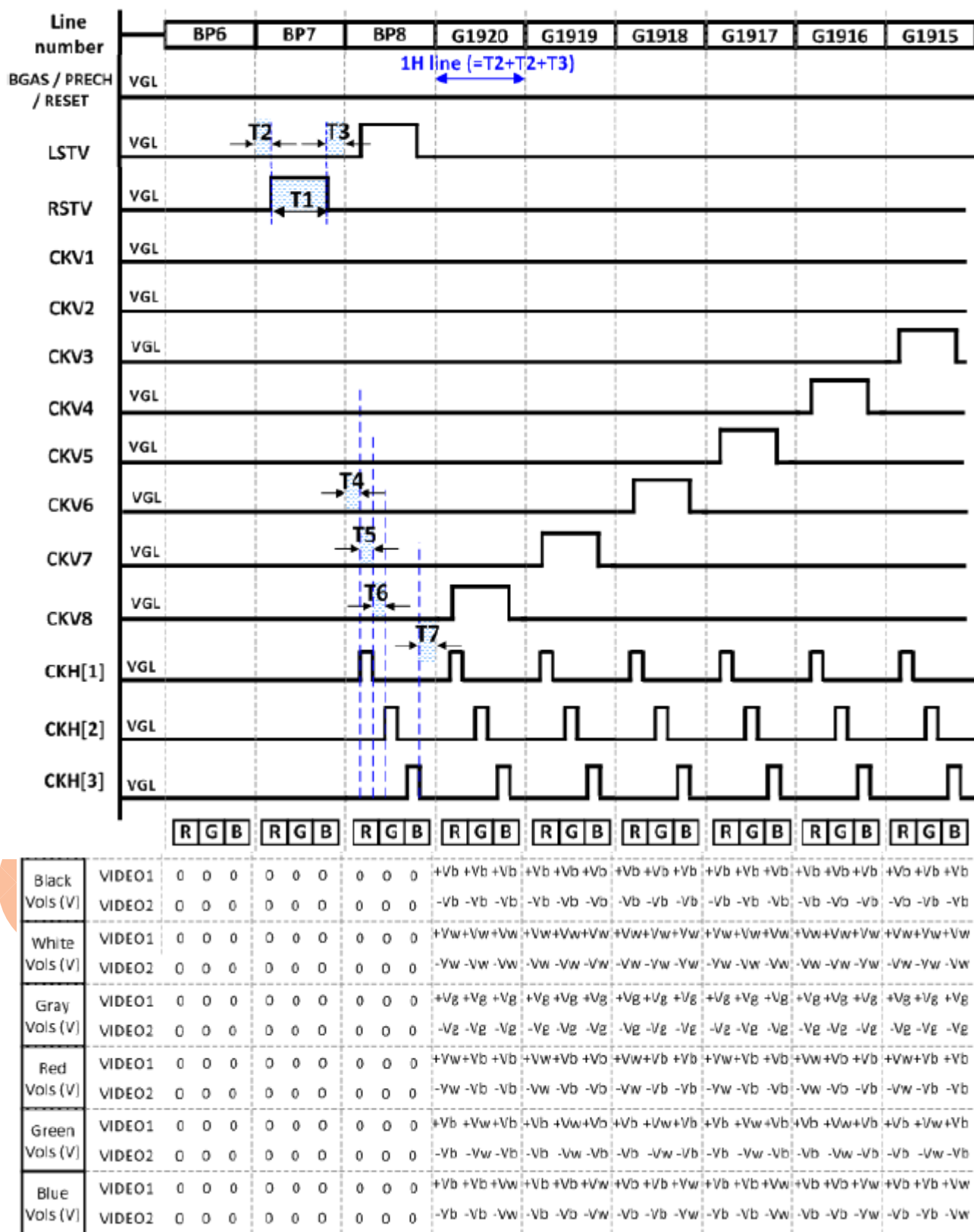
2-2. Timing Waveforms – forward scan



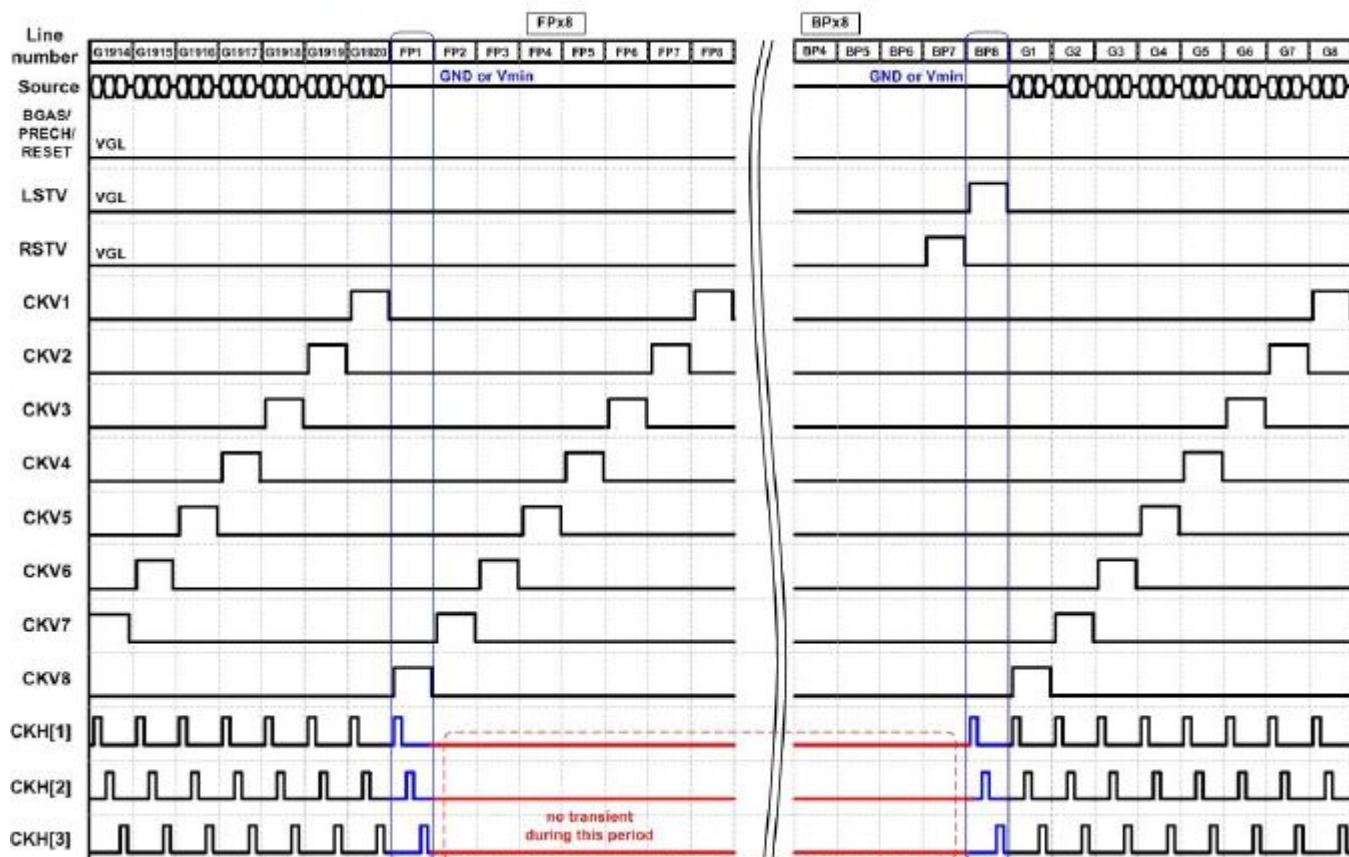
2-3. Two Frame Timing – forward scan



2-4. Timing Waveforms – backward scan



2-5. Two Frame Timing – backward scan



5. OPTICAL SPECIFICATION

Item		Symbol	Conditions	Specifications			Unit	Note
				Min.	Typ.	Max.		
Transmittance (without APCF) (no WOTP)		T%	Viewing normal angle θX = θY =0°	2.74	3.27		%	1.POL without APCF 2.Light course : Jef LED (0.290,0.283) 3.Metrology : DMS900 4.w/o TOD
Contrast Ratio		CR		700	1000			
Response Time		Ton + Toff			25	35	ms	
Viewing Angle	Hor.	θX+	Center CR>10	--	80	--	deg.	
		θX-		--	80	--		
	Ver.	θY+		--	80	--		
		θY-		--	80	--		
CF only (RGB+OC+ Backside ITO) Color Chromaticity (CIE 1931)	Red	XR	Viewing normal angle θx = θY =0°	0.632	0.662	0.692	--	w/i OC w/i ITO under C-light
		YR		0.292	0.322	0.352	--	
	Green	XG		0.231	0.261	0.291	--	
		YG		0.545	0.575	0.605	--	
	Blue	XB		0.113	0.143	0.173	--	
		YB		0.054	0.084	0.114	--	
	White	XW		0.272	0.302	0.332	--	
		YW		0.295	0.325	0.355	--	
	NTSC	xy		61.65	71.65	-	--	

*Note(1) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{255} / L_0$$

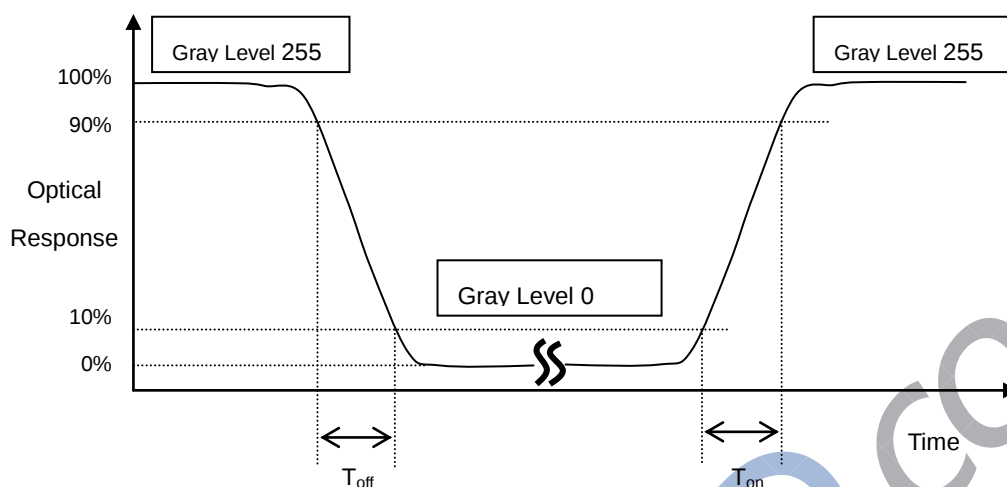
L255 : Luminance of gray level 255

L 0: Luminance of gray level 0

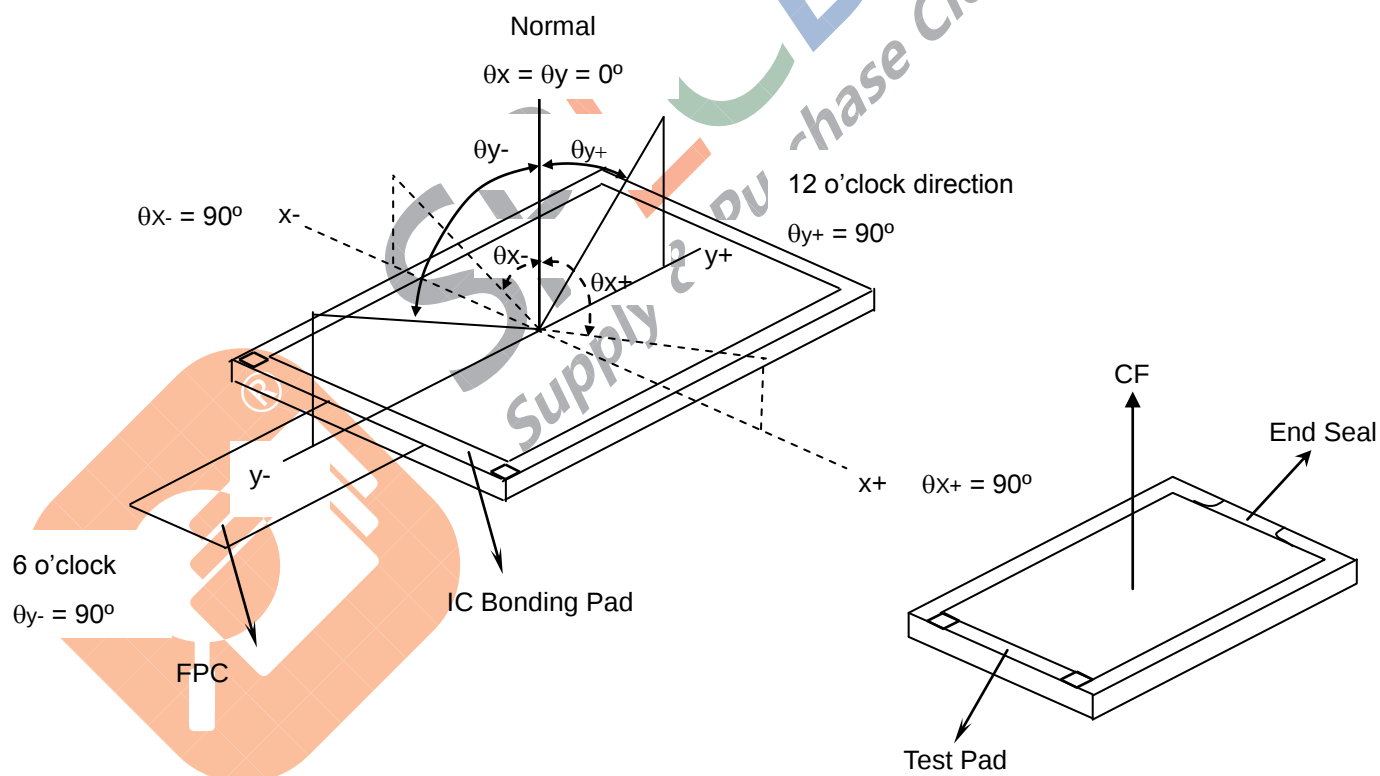
$$\text{CR} = \text{CR} (5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).

*Note (2) Definition of Response Time (T_{on} , T_{off}):

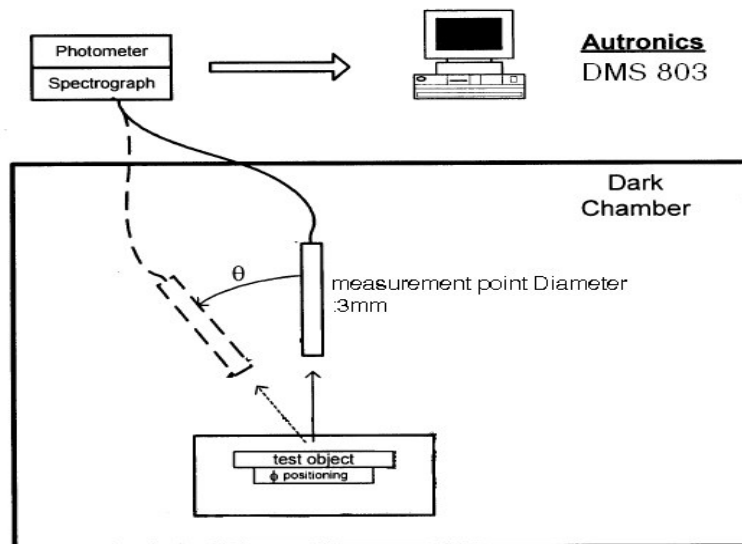


*Note(3) Definition of Viewing Angle

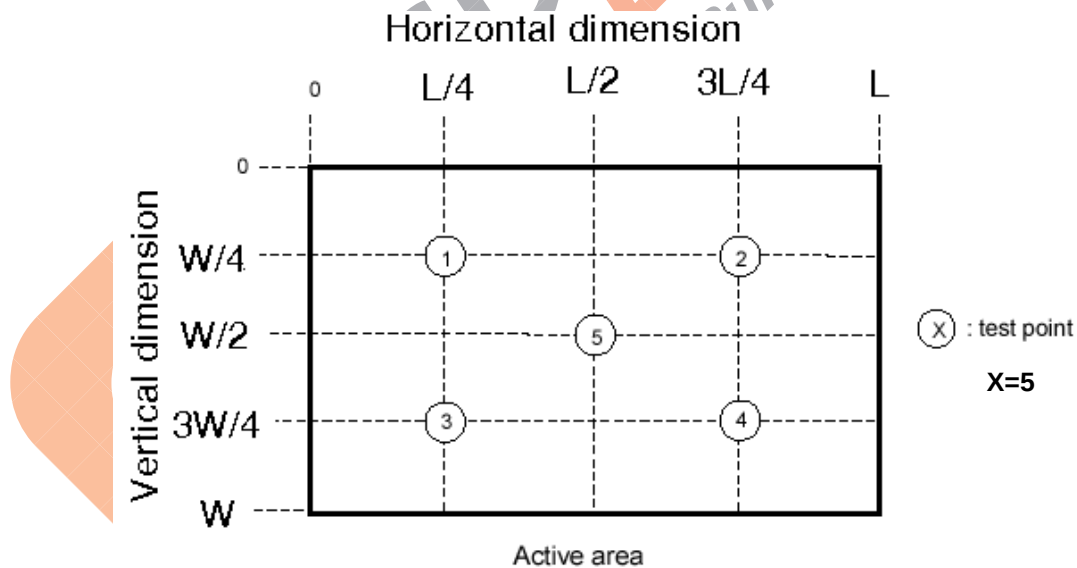


*Note (4) Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.

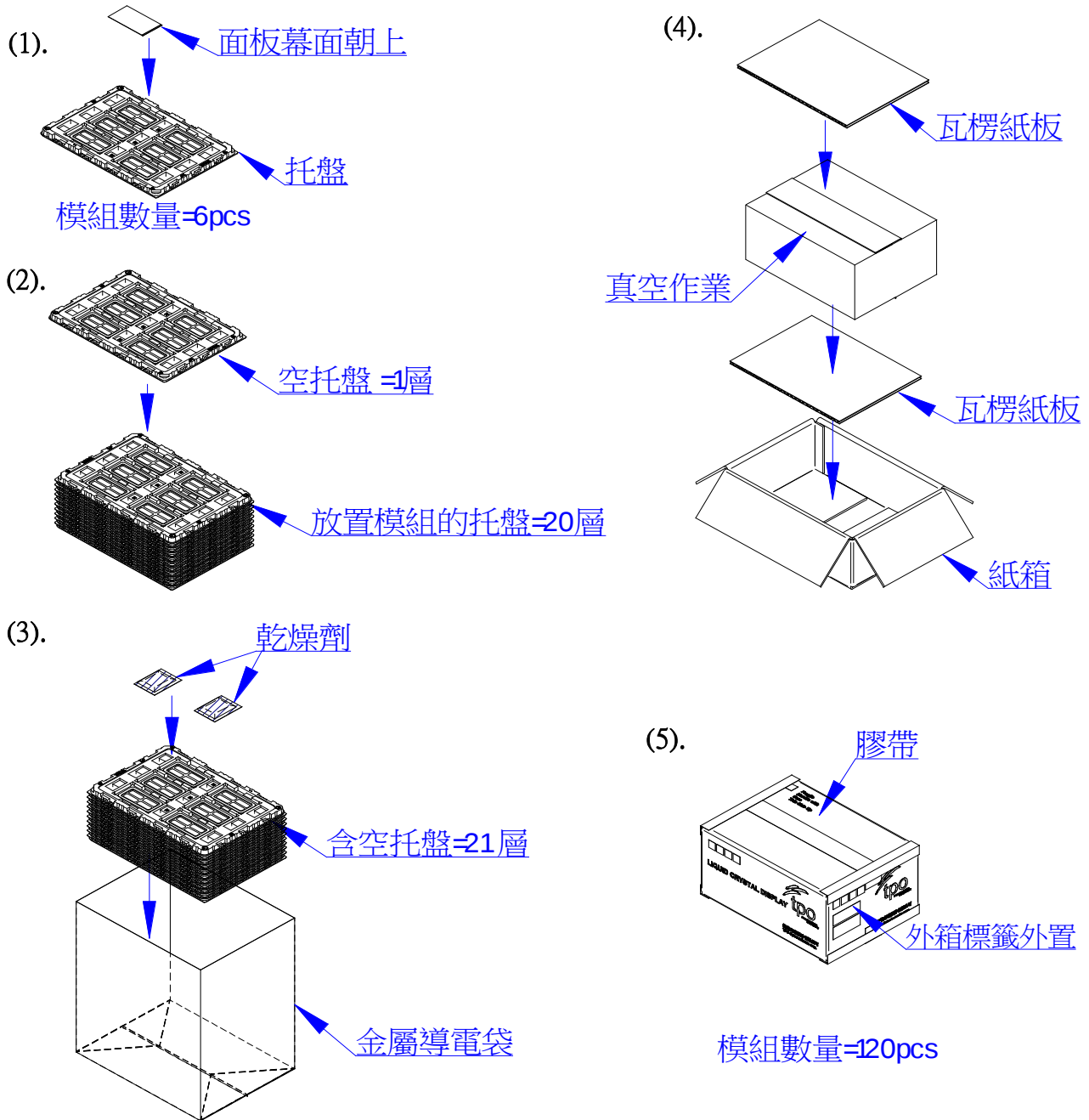


*Note (5)

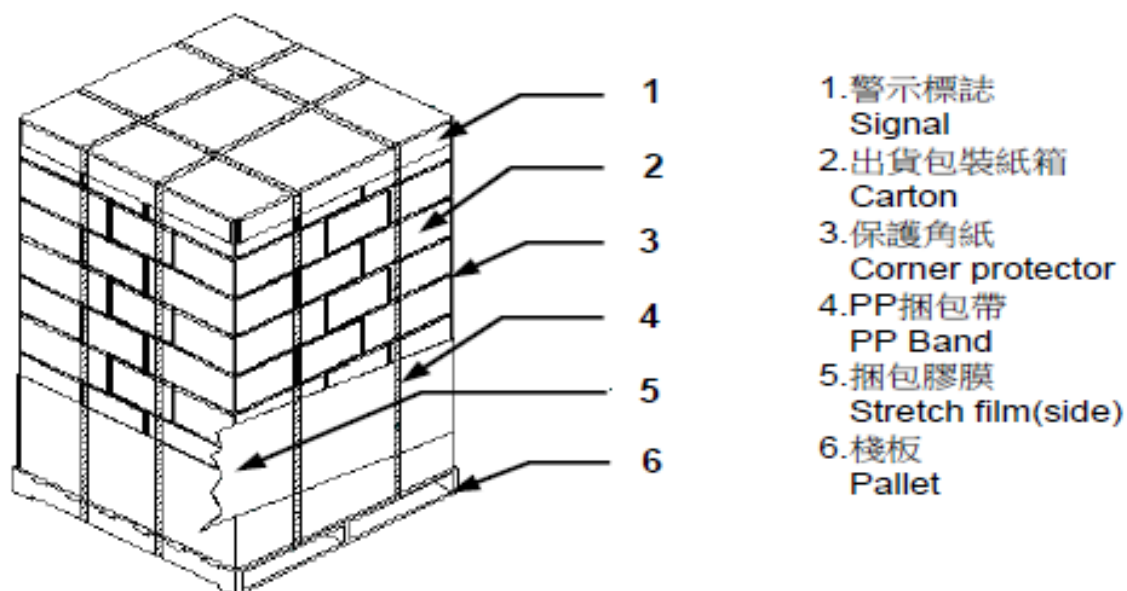


6. PACKAGE FORM

6.1 CELL PACKAGE



6.2 PALLET PACKAGESPEC

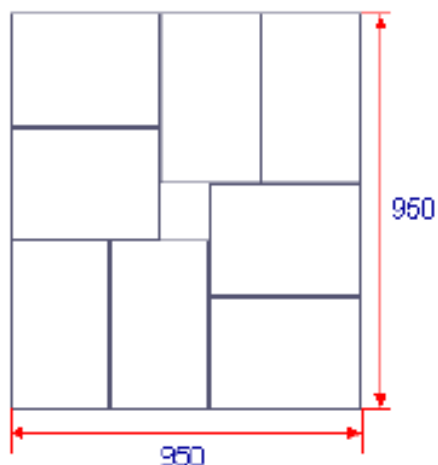


carton size: 450mm (L) x320mm(W) x 210mm(H)

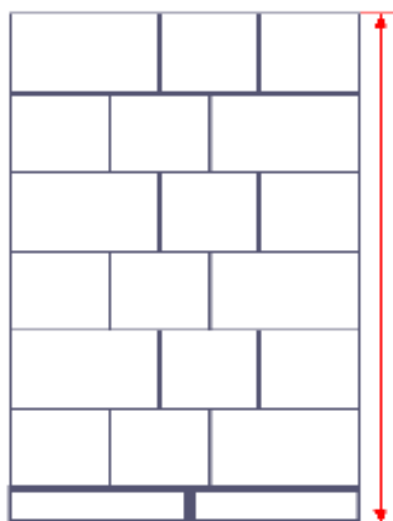
棧板 48ctn/pt

俯視圖

正/側視圖



棧板W1140*L1140*H130mm
每層採旋轉式交錯堆疊



堆疊6層(含棧板高度130mm)
W1140*L1140*H1390mm