

FlexOut Ultra Low Jitter Clock Generator

Features

- Ultra low jitter 156.25MHz clock generator <0.15ps max (12k to 20MHz) in LVPECL and LVDS configuration
- 6 differential outputs with 2 banks
- User configurable output signaling standard for each bank: LVDS or LVPECL or HCSL
- Separate supply voltages for customizable output levels
- Low skew between outputs within banks (<40ps)
- 2.5V / 3.3V power supply
- Industrial temperature support
- LQFP-48 package

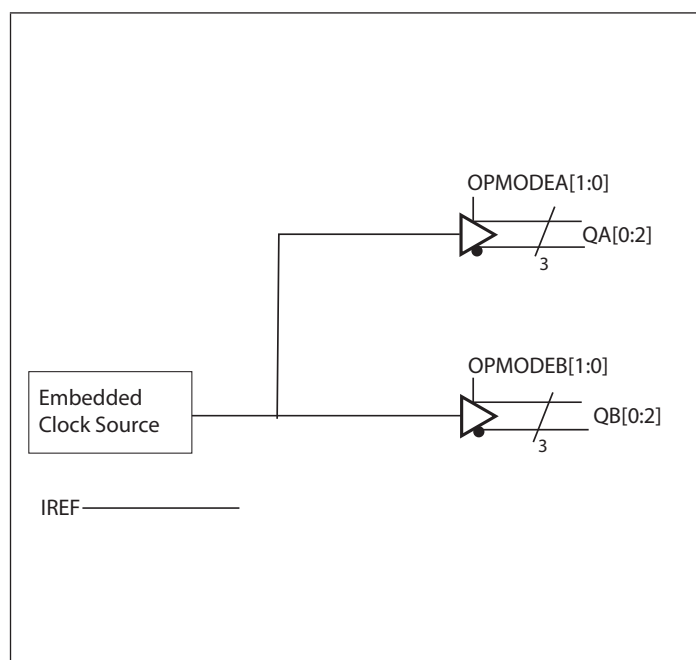
Description

The PI6CXG06F62a is part of Pericom's FlexOut clock generator family. FlexOut generators combine a low jitter high performance clock generator along with fanout capabilities. It also integrates a unique feature with user configurable output signaling standards on per bank basis which provide great flexibility to users. This device is ideal for systems that need to distribute low jitter clock signals to multiple destinations.

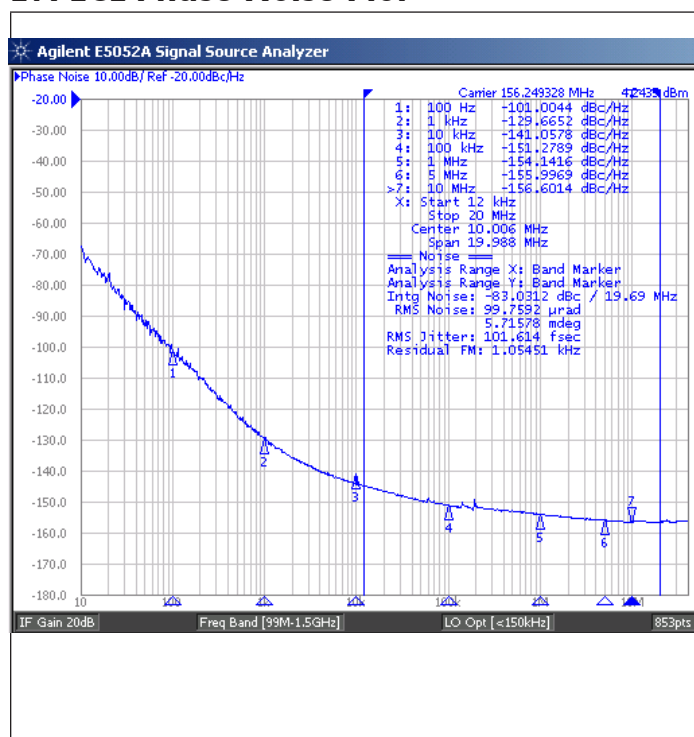
Applications

- Networking systems including switches and routers
- High frequency backplane based computing and telecom platforms

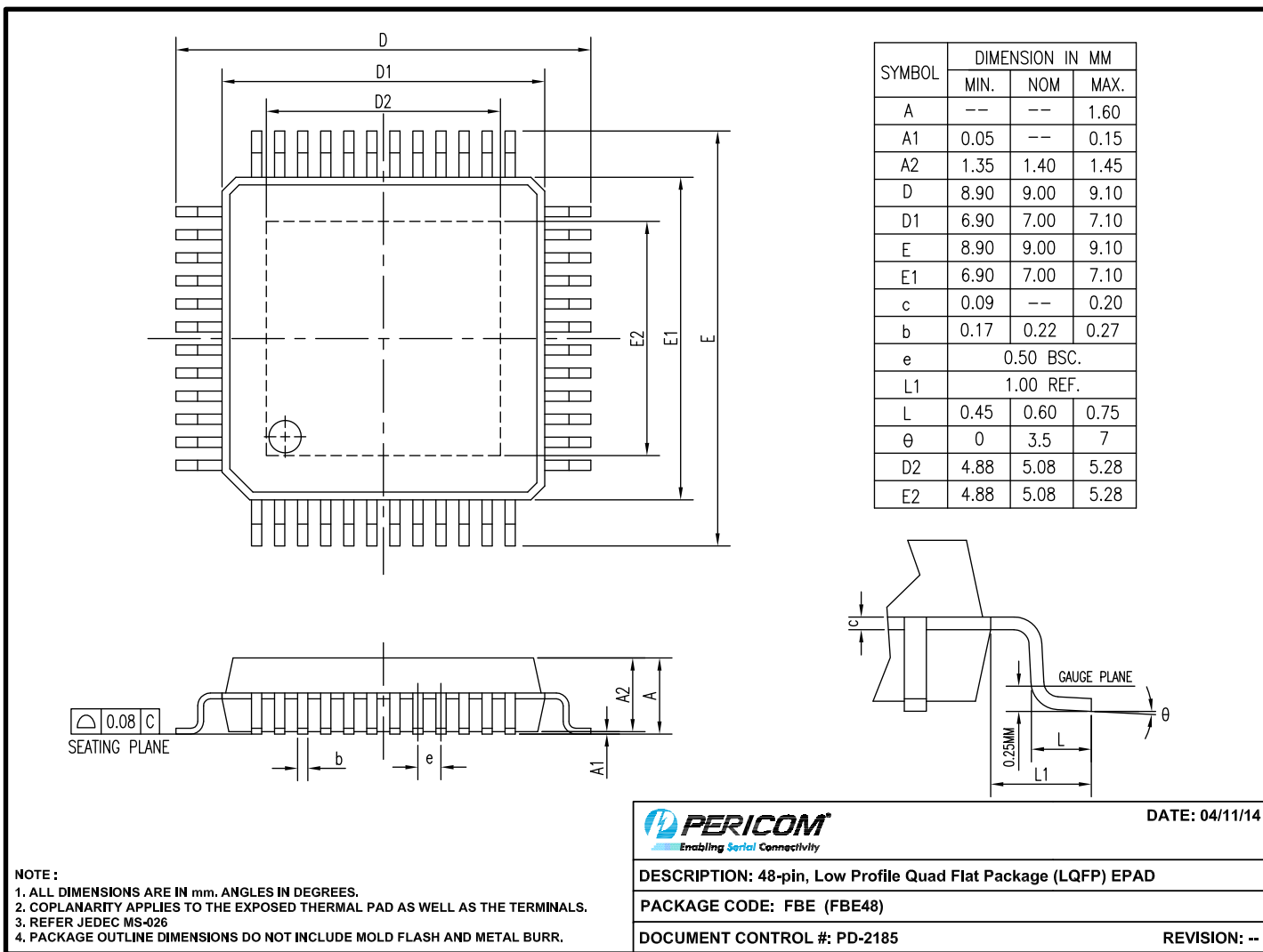
Block Diagram



LVPECL Phase Noise Plot



Packaging Mechanical:



| Ordering Code | Package Code | Package Type | Operating Temperature |
|-------------------|--------------|------------------------------|-----------------------|
| PI6CXG06F62aFBEIE | FBE | Pb-free & Green, 48-pin LQFP | -40 °C to 85 °C |

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
2. "E" denotes Pb-free and Green
3. Adding an "X" at the end of the ordering code denotes tape and Reel packaging