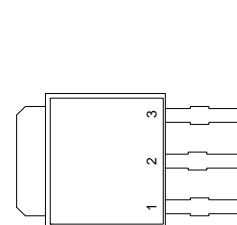
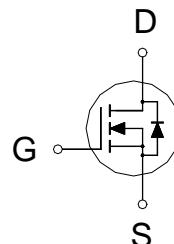


**NIKO-SEM**
**N-Channel Enhancement Mode  
Field Effect Transistor**
**PI506BZ**  
**TO-251(IS)**  
**Halogen-Free & Lead-Free**
**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	6mΩ	68A


 1. GATE  
 2. DRAIN  
 3. SOURCE
**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>2</sup>	$T_C = 25^\circ\text{C}$	$I_D$	68	A
	$T_C = 100^\circ\text{C}$		43	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	150	A
Avalanche Current		$I_{AS}$	30.4	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	46	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	50	W
	$T_C = 100^\circ\text{C}$		20	
Junction & Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>Package limitation current is 40A.**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.7	3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
		$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$		5.2	9	$\text{m}\Omega$
		$V_{GS} = 10\text{V}, I_D = 20\text{A}$		4.2	6	

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Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 10V, I_D = 20A$	80		S
<b>DYNAMIC</b>					
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$	1800		pF
Output Capacitance	$C_{oss}$		270		
Reverse Transfer Capacitance	$C_{rss}$		226		
Gate Resistance	$R_g$		0.9		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 15V, I_D = 20A$	43.3		nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		22.9		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		6.2		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$		11.3		
Rise Time <sup>2</sup>	$t_r$	$I_D \approx 20A, V_{GS} = 10V, R_{GEN} = 6\Omega$	25		nS
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		24		
Fall Time <sup>2</sup>	$t_f$		99		
			57		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>					
Continuous Current <sup>3</sup>	$I_S$			68	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 20A, V_{GS} = 0V$		1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 20A, dI_F/dt = 100A / \mu S$	21		nS
Reverse Recovery Charge	$Q_{rr}$		12		nC

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .

<sup>2</sup>Independent of operating temperature.

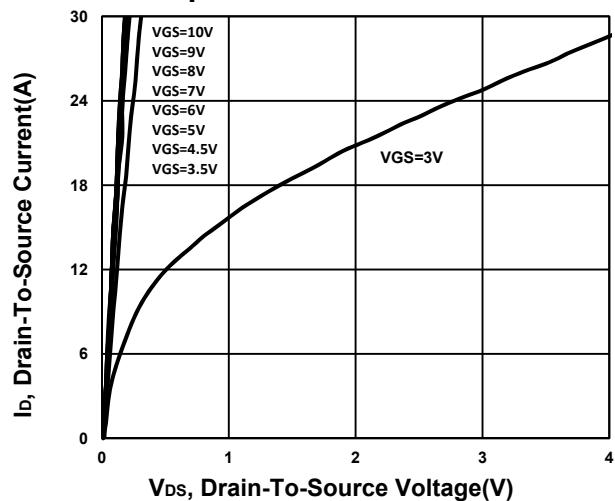
<sup>3</sup>Package limitation current is 40A.

**NIKO-SEM**

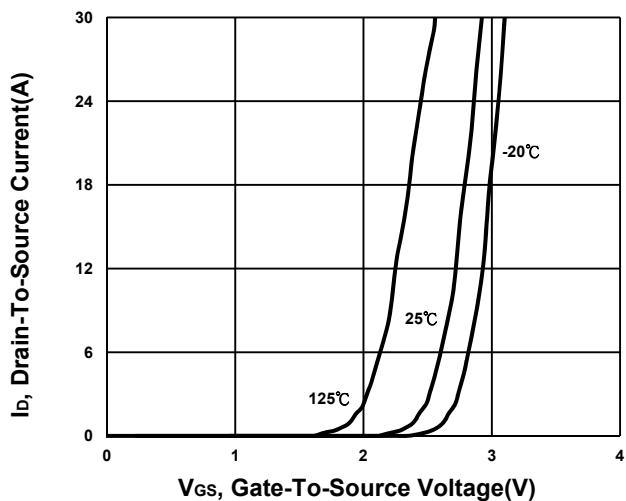
**N-Channel Enhancement Mode  
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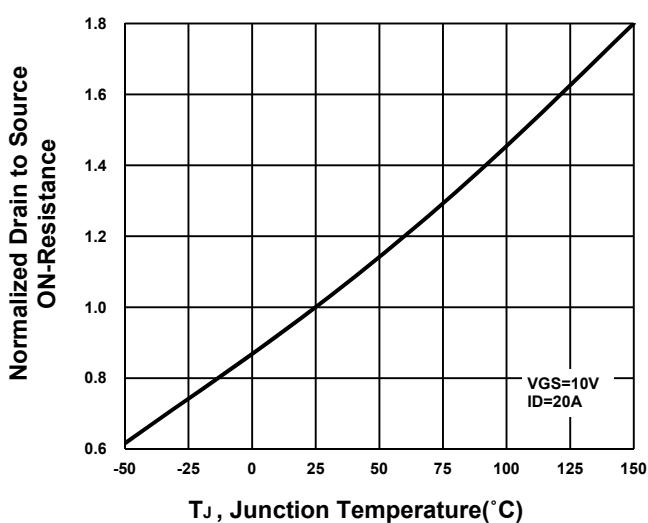
**Output Characteristics**



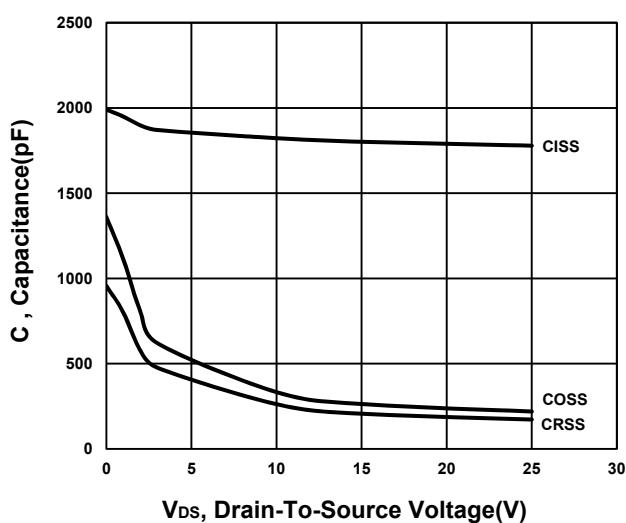
**Transfer Characteristics**



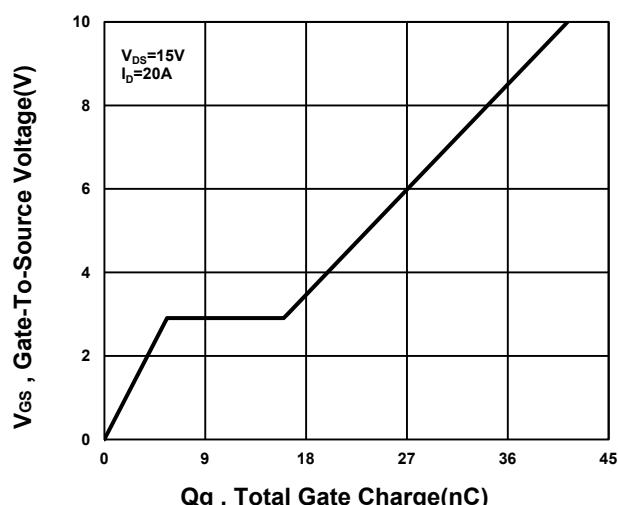
**On-Resistance VS Temperature**



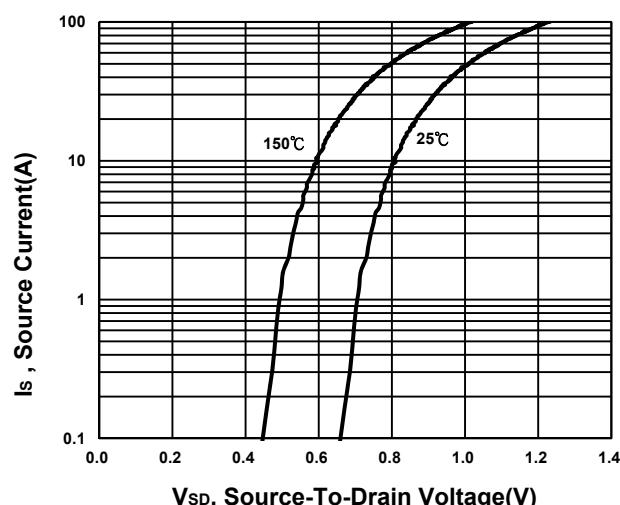
**Capacitance Characteristic**



**Gate charge Characteristics**



**Source-Drain Diode Forward Voltage**

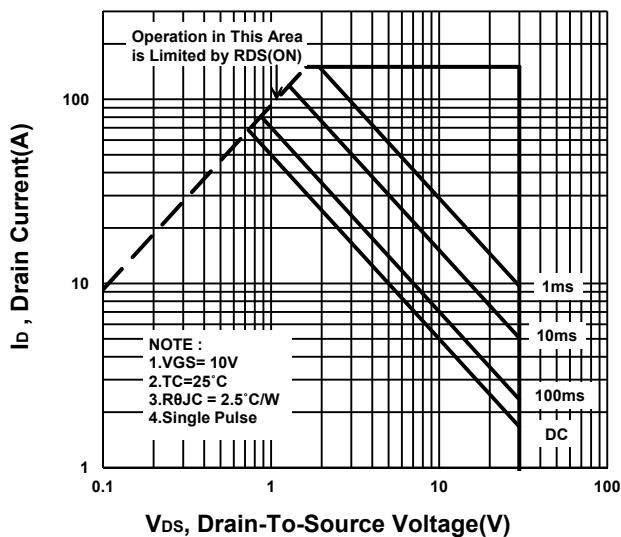


**NIKO-SEM**

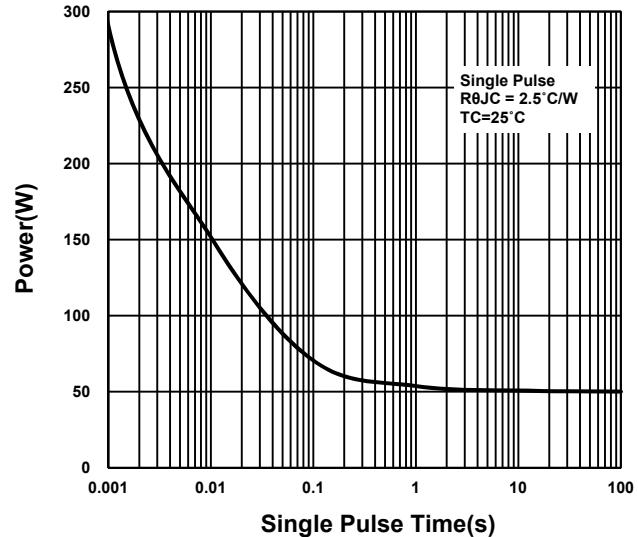
**N-Channel Enhancement Mode  
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**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**

