

SPECIFICATIONS

CUSTOMER	:
SAMPLE CODE	SH128800T004-ZZA
MASS PRODUCTION CODE	PH128800T004-ZZA
SAMPLE VERSION	- 01
SPECIFICATIONS EDITION	. 007
DRAWING NO. (Ver.)	LMD-PH128800T004-ZZA (Ver.007)
PACKAGING NO. (Ver.)	PKG-PH128800T004-ZZA (Ver.006)

Customer Approved

Date:

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	liminary specification cification for sample a	0 1	POWERT P 2017.10.20 TW RD APR
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History of Version

Date	Ver.	Edi.	Description	Page	Design by
09/06/2016	01	001	New Drawing.	-	Ackey
09/22/2016	01	002	Modify Drawing.	Appendix	Ackey
01/18/2017	01	003	New Sample.	-	Ackey
02/07/2017	01	004	Modify Drawing & DC Electrical Characteristics & Backlight Characteristics & Update Interface Pin Description.	Appendix 5, 8, 10	Ackey
03/13/2017	01	005	Modify Drawing.	Appendix	Ackey
04/19/2017	01	006	Update Spec.	Appendix	Ackey
10/23/2017	01	007	Modify Drawing.	Appendix	Ackey

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1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Screen size(inch)	10.1(Diagonal)
Driver element	Normally Black
Resolution	1280* (R 、 G 、 B) * 800 Dots
Display mode	Transmissive, ANTI-GLARE
Color	16.7M
Weight	224 g
Interface	LVDS
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

ltem	Standard Value	
Outline Dimension	229.8(W) * 149.0 (L) * 5.9 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	216.96 (W) * 135.60 (L)	mm

Note : For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
POWER SUPPLY VOLTAGE	VDD		-0.3	+7.0	V
OPERATING TEMPERATURE	Тор	-	-30	+80	°C
STORAGE TEMPERATURE	T _{ST}	-	-30	+80	°C
Storage humidity	H⊳	Ta<60 ℃	20	90	%RH

1.4 DC Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	
Power Supply Voltage	VDD	-	3.0	3.3	3.6	V	
Power Supply Voltage For Led Driver	VLED	-	9.0	12.0	15.0	V	
Supply Current	IDD	Pattern= Picture*1	-	130	195	mA	
VLED Supply Current	ILED	Duty=100%	-	1.0	1.5	А	

Note1: Maximum current display.



1.5 Optical Characteristics

TFT LCD Panel

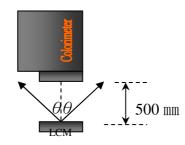
Ta=25℃

Item	Item		Condition	Min.	Тур.	Max.	Unit	-
Response tin	ne	Tr + Tf	-	-	25	50	ms	Note2
	Тор	ΘY+		75	85	-		
	Bottom	ΘY-		75	85	-	Dea	Niete 4
Viewing angle	Left	ΘX-	CR ≥ 10	75	85	1	Deg.	Note4
	Right	ΘX+		75	85	-		
Contrast rati	0	CR		600	800	I	I	Note3
	White	Х		0.27	0.32	0.37		
	vvriite	Y		0.30	0.35	0.40		
	Deal	Х		0.55	0.60	0.65		
Color of CIE	Red	Y	-	0.29	0.34	0.39		Natad
Coordinate (With B/L)	Green	Х		0.28	0.32	0.37	-	Note1
(1111 2) 2)	Green	Y		0.53	0.58	0.63		
	Blue	Х		0.09	0.14	0.19		
	Diue	Y		0.12	0.17	0.22		
Average Brightr Pattern=white di		IV		800	1000	-	cd/m2	Note1
Luminance unifo	ormity	YU	-	70	-	-	%	Note1

Note1:

- $1 : \triangle B = B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: $25^{\circ}C \pm 5^{\circ}C / 60 \pm 20\%$ R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 \pm 50 $\,\text{mm}^{-}$, (0= 0 °)
 - c: Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$





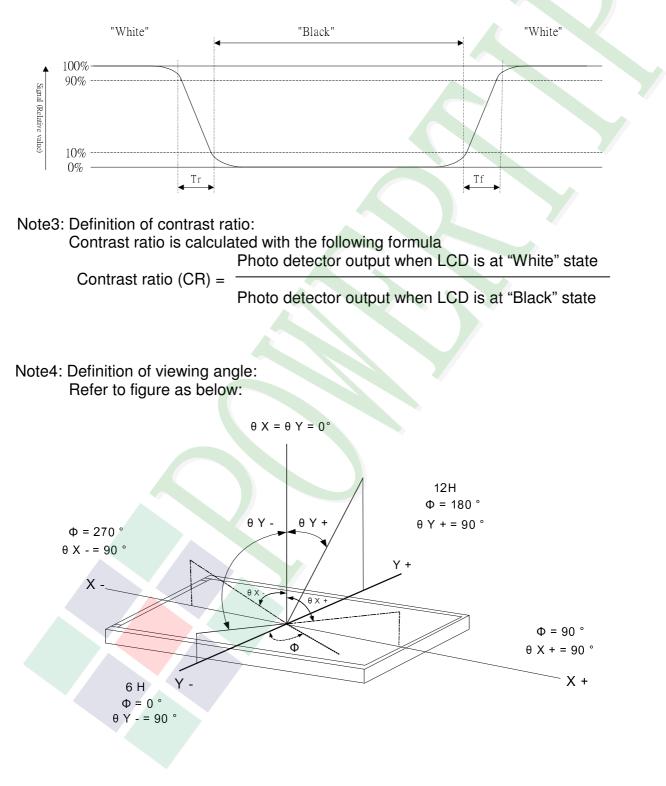
Colorimeter=BM-7 fast



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



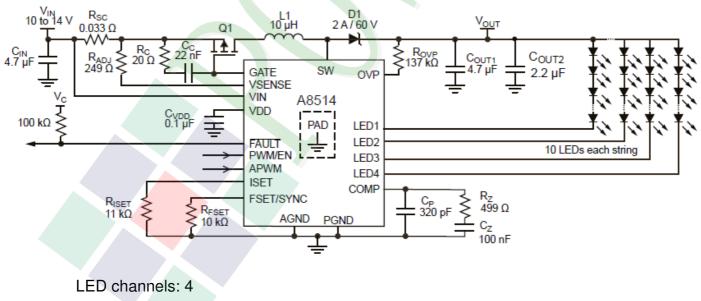


1.6 Backlight Characteristics

ltem		Symbol	Min.	Тур.	Max.	Unit	Note
Backlight Power		VLED	9.0	12.0	15.0	V	To 05.00
Backlight Power		ILED	-	1.0	1.5	А	Ta = 25℃
LED Driver output Vo	ltage	VF	26.0	28.0	30.0	V	-
LED Driver output Cu	irrent	IF	-	200		mA	-
EN Signal Valtage	High	PWM/EN	1.5		ŀ	V	
EN Signal Voltage	Low		-	-	0.4		-
PWM Frequency	,	PWM/EN	200	-	1000	Hz	*1)
APWM Frequency		APWM	20		1000	KHz	
Lifetime		-	70000	-	-	Hr	*2)
Color				White			-

*1) PWM/EN = 5 V

*2) Definition of the LED life time: Luminance (L) under 50% of the initial value. LED life time is restricted under normal condition, ambient temperature=25 ℃



Series LEDs per channel: 10



2. MODULE STRUCTURE

2.1 Counter Drawing

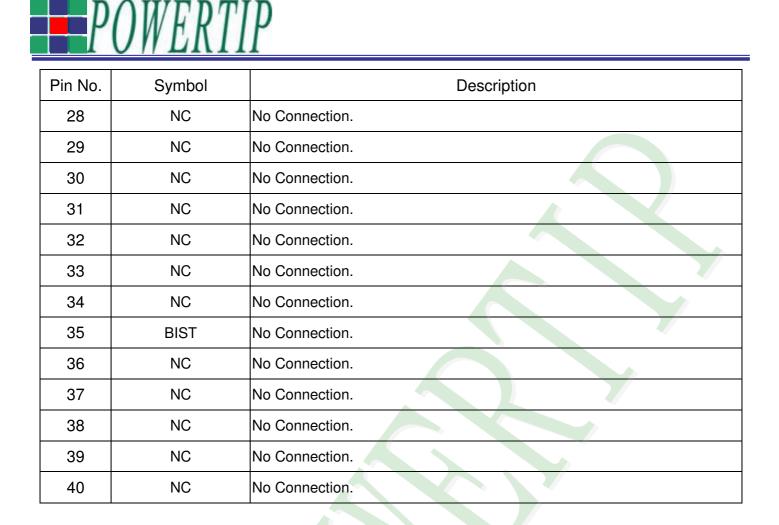
2.1.1 LCM Mechanical Diagram

* See Appendix



2.2 Interface Pin Description

Pin No.	Symbol	Description
1	NC	No Connection.
2	VDD	Power Supply.
3	VDD	Power Supply.
4	NC	No Connection.
5	NC	No Connection.
6	NC	No Connection.
7	NC	No Connection.
8	LVON	-LVDS Differential Data Input.
9	LV0P	+LVDS Differential Data Input.
10	GND	Ground.
11	LV1N	-LVDS Differential Data Input.
12	LV1P	+LVDS Differential Data Input.
13	GND	Ground.
14	LV2N	-LVDS Differential Data Input.
15	LV2P	+LVDS Differential Data Input.
16	GND	Ground.
17	LVCLKN	-LVDS Differential Clock Input.
18	LVCLKP	+LVDS Differential Clock Input.
19	GND	Ground.
20	LV3N	-LVDS Differential Data Input.
21	LV3P	+LVDS Differential Data Input.
22	GND	Ground.
23	NC	No Connection.
24	NC	No Connection.
25	NC	No Connection.
26	NC	No Connection.
27	NC	No Connection.



CN1: Backlight

Pin No.	Symbol	Description	
1	VLED	Power Supply.(+12.0V)	
2	VLED	Power Supply. (+12.0V)	
3	EN/PWM	LED Enable Pin. (Active Hi). PWM dimming pin, used to control the LED intensity by using pulse width modulation. Also used to enable the A8514.	
4	APWM	Analog trimming option for dimming. Applying a digital PWM signal to this pin adjusts the internal ISET current.	
5	GND	Ground.	
6	GND	Ground.	

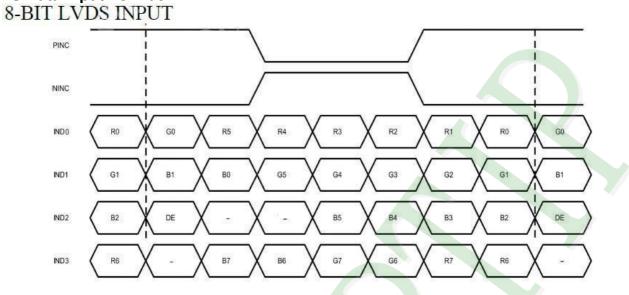


2.3 Timing Characteristics 2.3.1 POWER ,SIGNAL SEQUENCE

$0.5 < t1 \le 10 \text{ms}$ $0 < t2 \le 50 \text{ms}$ $0 < t3 \le 50 \text{ms}$ $0 < t4 \le 10 \text{ms}$	200ms≤t5 200ms≤t6 200ms≤t7		
		VDD=3.3V	
LCD POWER SU LOGIC SIGNAL		data	<u>3.0V</u> 0.3V 0.3V
		2	T3 T4 T5
	27	VLED	
BACKLIGHT PO	OWER	T6	T7



2.3.2 LVDS Data Input Format

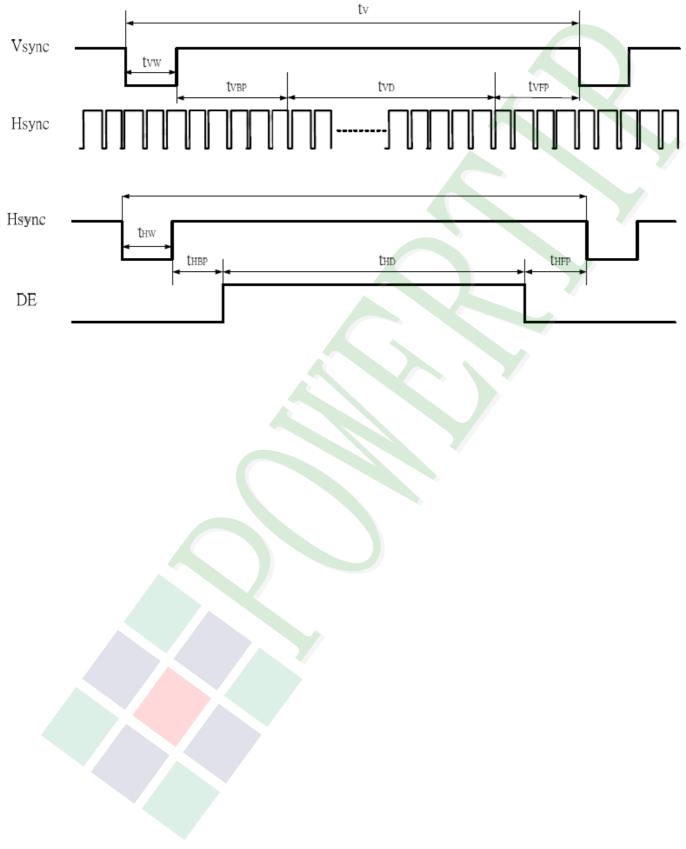


2.3.3 Interface Timings

Parameter	Symbol	Unit	Min.	Тур.	Max.
Frame Rate		Hz	-	60	-
Frame Period	TV	line	815	823	1023
Vertical Display Time	TVD	line		800	
Vertical Blanking Time	Tvw+Tvbp+Tvfp	line	15	23	33
1 Line Scanning Time	Тн	clock	1410	1440	1470
Horizontal Display Time	THD	clock		1280	
Horizontal Blanking Time	THW+THBP+THFP	clock	60	160	190
Clock Rate	1/Tc	MHz	68.9	71.1	73.4



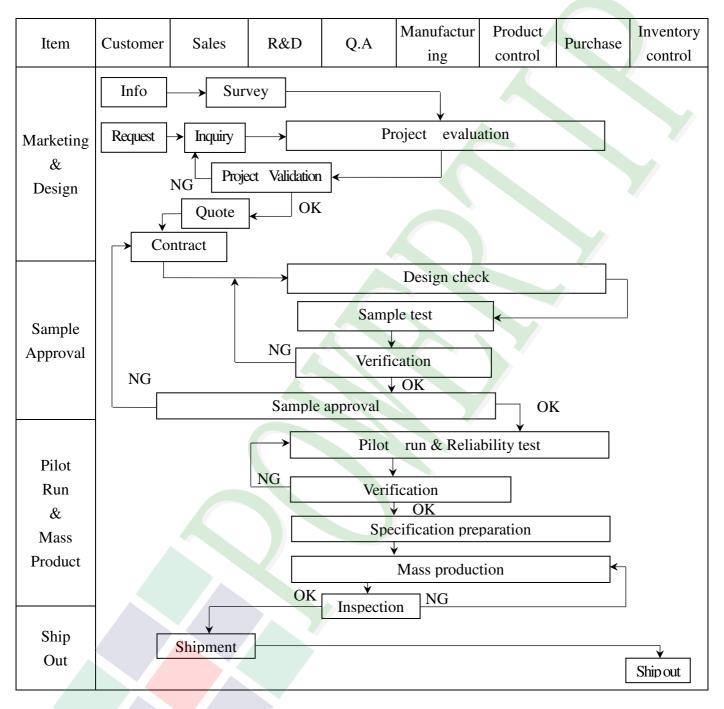
2.3.4 Timing Diagram of Interface Signal (DE mode)





3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



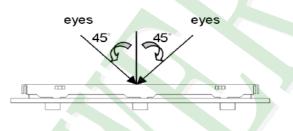


Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service	Info	➤ Claim	[Trackin	Failure an Corrective			
Q.A Activity	 ISO 9001 Equipment Standardi 		n	4. E	ocess improv ducation An			

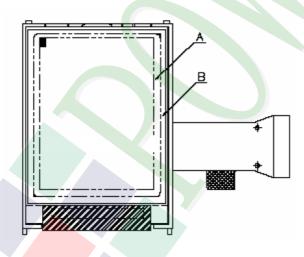
POWERTIP

3.2. Inspection Specification

- ◆Scope: The document shall be applied to TFT-LCD Module for 3. 5″~10″ (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ♦OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test :
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



◆Specification For TFT-LCD Module 3. 5″ ~10″:

♦Spe	cification For TFT-L	CD Module 3. 5″~10″:	Ver.B01)	
NO	Item	Criterion		
	Product condition	1. 1The part number is inconsistent with work order of production.		
01		1. 2 Mixed product types.		
		1. 3 Assembled in inverse direction.	Major	
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major	
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major	
	Electrical Testing	4. 1 Missing line character and icon.		
		4. 2 No function or no display.		
04		4. 3 Display malfunction.		
		4. 4 LCD viewing angle defect.		
		4. 5 Current consumption exceeds product specifications.	Major	
	Dot defect (Bright dot \ Dark dot) On -display 5	Item Acceptance (Q'ty)		
		Bright Dot ≤ 4		
		Dot Dark Dot ≤ 5		
		Defect Joint Dot ≤ 3		
05		Total ≤ 7	Minor	
		5. 1 Inspection pattern : full white , full black , Red , Green and blue screens.		
		5. 2 It is defined as dot defect if defect area $>1/2$ dot.		
		5. 3 The distance between two dot defect ≥ 5 mm.		

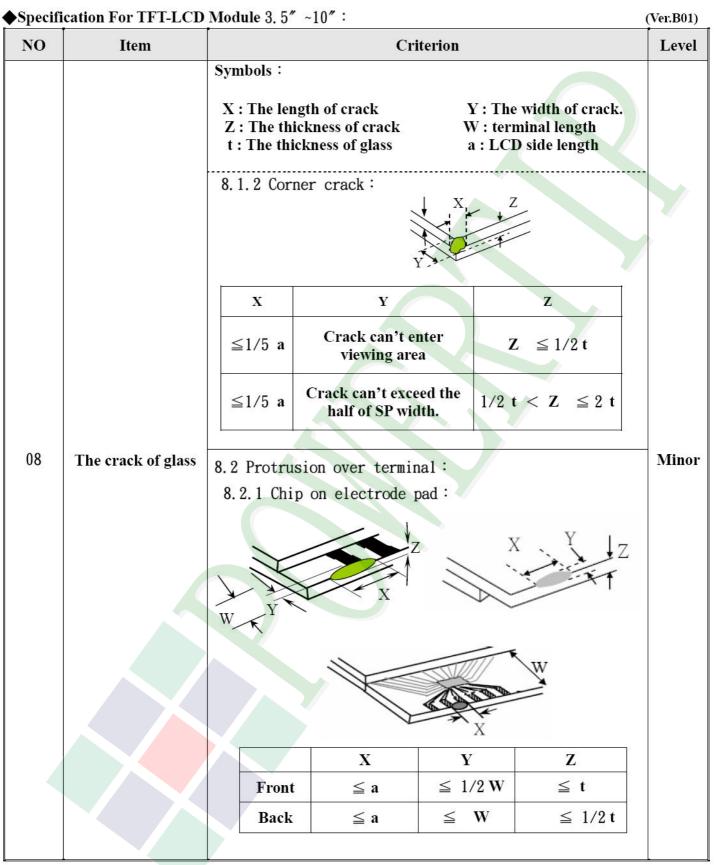


◆Specification For TFT-LCD Module 3. 5″~10″: (Ve						
NO	Item	Criterion				
		6. 1 Round type (Non-display or display) :				
		Dimension (diameter : Φ)Acceptance (Q'ty)A areaB area				
	Black or white dot > scratch >	$\Phi \leq 0.25$ Ignore				
	contamination Round type	$0.25 < \Phi \leq 0.50 \qquad 5$ Ignore				
		$\Phi > 0.50$ 0				
06		Total 5	Minor			
	$\Phi = (x+y)/2$	6. 2 Line type(Non-display or display) :				
	Line type	Length (L) Width (W) Acceptance (Q'ty) A area B area				
	(¥ W	W ≤ 0.03 Ignore				
		$L \leq 10.0$ 0.03 < W ≤ 0.05 4				
		L ≤ 5.0 0.05 < W ≤ 0.10 2 Ignore				
		W >0.10 As round type				
		Total 5				
		Dimension (diameter : Φ)Acceptance (Q'ty)A areaB area				
		$\Phi \leq 0.25$ Ignore				
07	Polarizer	$0.25 < \Phi \leq 0.50$ 4	Minor			
	Bubble	$0.50 < \Phi \leq 0.80$ 1 Ignore				
		$\Phi > 0.80$ 0				
		Total 5				

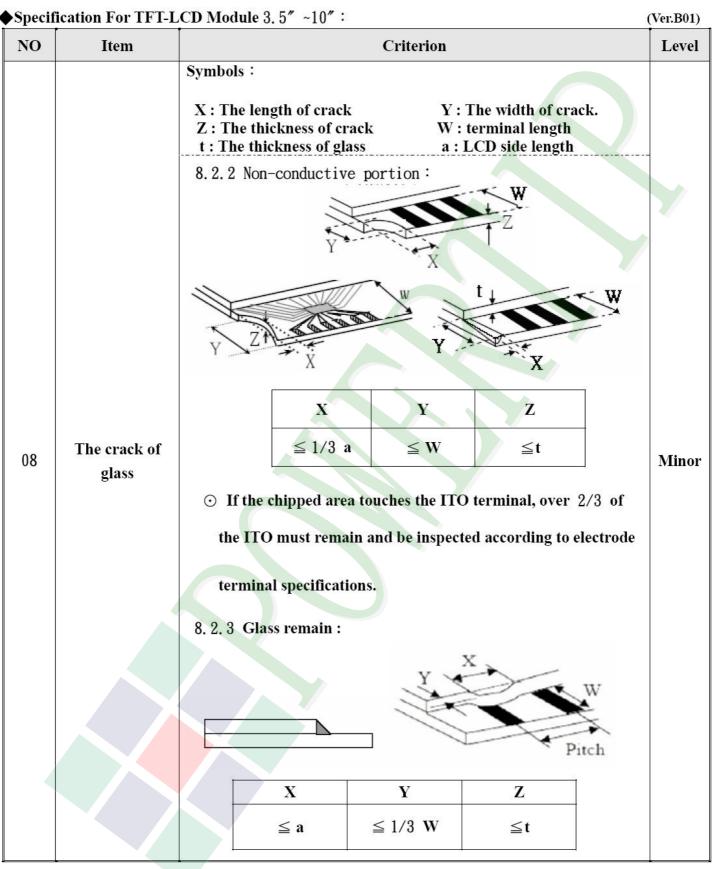


◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.B0)						
NO	Item	Criterion				
		Z : The thickness of crack	Y : The width of crack. W : terminal length a : LCD side length			
		8.1 General glass chip: 8.1.1 Chip on panel surface and cra	ack between panels:			
08	The crack of glass	X Y SP Y Y IOK	Y Y X Y ING	Minor		
		Seal width	Y			
		X Y ≤ a Crack can't enter viewing area	$\frac{z}{\leq 1/2 t}$			
		$\leq a \qquad \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array}$	$1/2 t < Z \leq 2 t$			











4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION			
1	High Temperature Storage Test	Keep in +80 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage Test	Keep in −30 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	Temperature Cycling Storage Test	$-30^{\circ}C \rightarrow +25^{\circ}C \rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ $(30^{\text{mins}}) (5^{\text{mins}}) (5^{\text{mins}}) (5^{\text{mins}})$ 10 Cycle Surrounding temperature, then storage at normal condition 4hrs.			
5	ESD Test	Air Discharge:Contact Discharge:Apply 2 KV with 5 timesApply 250 V with 5 timesDischarge for each polarity +/-discharge for each polarity +/-1. Temperature ambiance : 15°C ~35°C2. Humidity relative : 30% ~60%3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%4. Discharge Resistance(Rd) : 330 Ω±10%5. Discharge, mode of operation :Single Discharge (time between successive discharges at least 1 sec)(Tolerance if the output voltage indication : ±5%)			
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X \cdot Y \cdot Z) duration for 2 Hrs 			
7	Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45. 4 45. 4 ~ 90. 8 90. 8 ~ 454 Over 454 Drop direction : %1 corner / 3 edge	Drop Height (cm) 122 76 61 46 es / 6 sides each 1times		



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

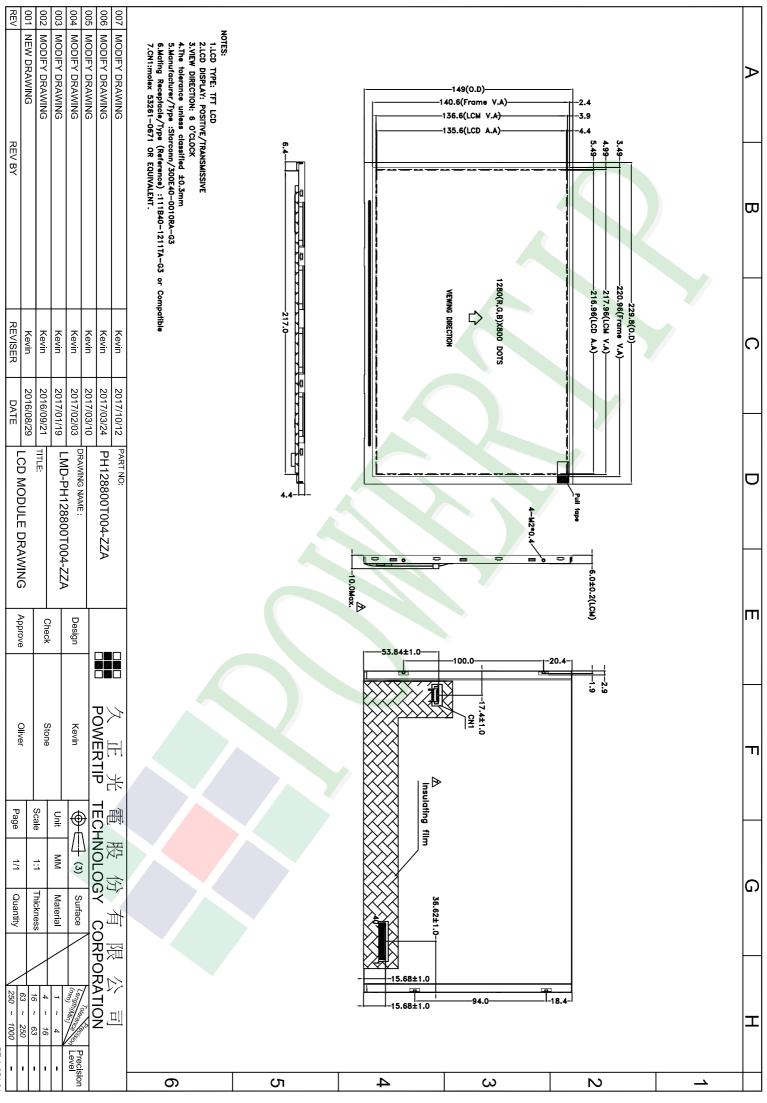
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



PT-A-054-01

