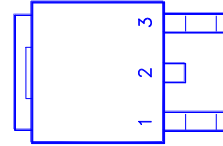
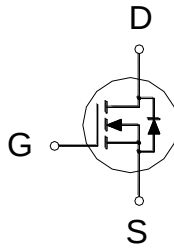




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	7.4m Ω	77A

GATE
DRAIN
SOURCEABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ²	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	77	A
	$T_C = 100\text{ }^{\circ}\text{C}$		49	
Pulsed Drain Current ¹		I_{DM}	300	
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	96	W
	$T_C = 100\text{ }^{\circ}\text{C}$		38	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.3	$^{\circ}\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.²Package limitation current is 55A.ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.4	2	3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$			1	μA
		$V_{DS} = 100V, V_{GS}=0V, T_J=125\text{ }^{\circ}C$			100	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 14A$		5.7	7.4	m Ω
		$V_{GS} = 4.5V, I_D = 10A$		7.6	9.8	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 5V, I_D = 14A$		40		S

DYNAMIC						
Input Capacitance	C _{iSS}	V _{GS} = 0V, V _{DS} = 50V, f = 1MHz		3255		pF
Output Capacitance	C _{oSS}			285		
Reverse Transfer Capacitance	C _{rSS}			14		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		1.6		Ω
Total Gate Charge ²	Q _g	V _{DS} = 50V, I _D = 14A, V _{GS} = 10V		59		nC
Gate-Source Charge ²	Q _{gs}			10		
Gate-Drain Charge ²	Q _{gd}			16		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 50V I _D ≅ 14A, V _{GS} = 10V, R _{GS} = 6Ω		14		nS
Rise Time ²	t _r			42		
Turn-Off Delay Time ²	t _{d(off)}			55		
Fall Time ²	t _f			72		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current ³	I _S				77	A
Forward Voltage ¹	V _{SD}	I _F = 14A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 14A, dI _F /dt = 100A / μS		37		nS
Reverse Recovery Charge	Q _{rr}			47		nC

¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

³Package limitation current is 55A.